## **FROM INTUITION TO COO** A CASE STUDY IN VERIFIED RESPONSE-TIME ANALYSIS OF FIFO SCHEDULING

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## **PAPER IN A NUTSHELL**

### **CASE STUDY**

### A formally verified response-time analysis (RTA) for FIFO

→ Formal verification ensures correctness → How much effort does it take to formally verify a result? → Can RTS researchers with limited Coq know-how do it?

```
Variable R : duration.
Hypothesis H R max:
     (A : duration),
     is in concrete_search_space A \rightarrow
       ∃ (F : nat),
          A + F \ge \langle sum (tsk < - ts) RBF tsk (A + E) \rangle
          \wedge F \leq R.
```

Theorem uniprocessor response time bound FIFO: task response time bound tsk R.

### **EMPIRICAL EXPLORATION**



### Why FIFO?

- $\rightarrow$  Trivial to implement
- → Low run-time overhead
- → Surprisingly little prior attention
- → Good enough for certain workloads





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MOTIVATION

## WHY FORMAL VERIFICATION?

The field of real-time systems aims to give strong guarantees →Traditionally backed by pen & paper proofs



# Pen & paper analyses are not immune to bugs!

Thorny Timing Analysis of Real-Time Systems with Limit Pa alle sm

[21]) and rith Deferred Pre-emp Dcessor l, and Priority Sc. ling, Univers CISTER/IN. can be view lability analysis non-preeferred pre-emption priority (gFPNS) and strictly dominates both. With fixed systems. gFPDS is a two key parameters that affect schedul oility: cheduling (gFPPS) assigned to each task, and the length of each task's final nonheduling (gFPNS). pre-emptive region (FNR). The FNR length affects both the ed via appropriate schedulability of the task itself, and the schedulability of second approach is specifically based on response-time analysis and models the response-time computation as a linear optimization problem. The latter linear-programming-based approach has better runtime complexity than the former reduction-based approach. Schedulability ammuy masks are presented. In the first approach, the schedula "global-like" sub-problems to which existing global schedulability second approach is specifically based on response-time analysis and a pre-emptive region tasks with higher priorities. This is a trade-off as increasing nize schedulability the FNR length can improve schedulability for the task itself on shows that gFPDS by reducing the number of times it can be pre-empted, but orms both gFPPS and gFPNS. potentially increases the blocking effect on higher priority I. INTRODUCTION

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### Abstract

Conventional real-time system implementations assume a von Neumann processor-memory architecture with at most one software process executing at any time. Scheduling approaches for such systems multiplex the processor among tation). Also, the co-processors can execute in runnable processes. Fixed priority scheduling, notably, utilises timing analysis to determine offline the run-time timing behaviour of the system. Recent alternative real-time system imple-

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software process is implemented  $_{in}$ rdware so as to speed-up overall execution. reconfigurable hardware can be seen as a number of application specific co-processors, where each one can speed-up the execution of parallel. In the resulting limited parallel architecture, at most one software process can execute in parallel with a number of co-processors. Traditional co-processor use is exemplified by

computation as a linear optimization problem. The latter linear-programming-based approach has better runtime complexity than the former reduction-based approach. Schedulability experiments show the proposed techniques to be effective. has better runtime complexity than the former reduction-experiments show the proposed techniques to be effective. A common misconception with regard to fixed priority

### **MPI-SWS**

Schedulability A galysis of the Linux Push and Pull Schedul







## WHY FORMAL VERIFICATION?

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A common misconcep



## **BUT, ISN'T FORMAL VERIFICATION REALLY HARD?**

Prior work has used formal verification to prove:

- ► EDF, FP RTA (Bozhko and Brandenburg, 2020)
- Results in network calculus (Roux et al., 2022)

►etc.

### How much **effort** does it take?



**MPI-SWS** 

### How much **prior knowledge** does it take?



Case study: Verification of an RTA

Bedarkar et al.



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## **OVERVIEW OF CASE STUDY**

Each element corresponds to some Coq code!

## **SETUP: SYSTEM MODEL**



- →Ideal uni-processor
- →Set of *n* sporadic, independent real-time tasks
- →Arbitrary deadlines
- →Worst-case execution time
- →Arbitrary arrival curves



# BACKGROUND

Theorem a simple theorem: V x y, x + y = y + x. Proof. move  $\rightarrow x y$ . induction x. - by rewrite add0n addn0. (\* base \*) - by rewrite addSn IHx addnS. (\* step \*) Qed.

- → The **proof engine** is not fully automatic!
- Coq is a **proof assistant** → You can write **programs/definitions** and then **prove theorems** about them

From Intuition to Coq: A Case Study in Verified Response-Time Analysis of FIFO Scheduling

## **BACKGROUND: COQ**







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## **BACKGROUND: PROSA**

### Prosa is a Coq library of definitions and proofs about RTS

- → Basic definitions (jobs, tasks, processor, *etc.*)
- $\rightarrow$  Proofs of classic results as well as novel ones

Prosa emphasizes **readable** specifications

### Higher- and Equal-Priority Interference Next, we establish a bound on the interference produced by higher- and equal-priority jobs. Section BoundOnHEPWorkload. Consider again a job j of the task under analysis tsk with a positive cost. Variable j : Job. Hypothesis H\_job\_of\_task : job\_of\_task tsk j. Hypothesis H\_j\_in\_arrivals : arrives\_in arr\_seq j. Hypothesis H\_job\_cost\_positive : job\_cost\_positive j.



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# CASE STUDY: FIFO RTA

## **INTUITIVE VS. FORMAL REASONING**



Natural Language

Intuitive definitions and results usually have a natural mechanized counterpart.

Definition work\_conserving :=
∀ j t,
backlogged j t →
∃ j\_other,
scheduled\_at j\_other t.

Gallina (Coq)





## **OVERVIEW OF CASE STUDY**

Each element corresponds to some Coq code!





## **OVERVIEW OF CASE STUDY**

Each element corresponds to some Coq code!



### **SYSTEM MODEL: WORKLOAD**

### **Definition** duration := nat. **Definition** $\varepsilon := 1$ .

### We employ a discrete time model, and let $\mathbb{T} = \mathbb{N}$ denote the time domain and $\varepsilon \triangleq 1$ the indivisible least unit of time.





### **SYSTEM MODEL: WORKLOAD**

 $\{\tau_1, \tau_2, ..., \tau_n\}.$ 

### **Definition** duration := nat. Definition $\varepsilon := 1$ .







## **SYSTEM MODEL: WORKLOAD**

**Definition** duration := nat. **Definition**  $\varepsilon := 1$ .





**Context** {Task : TaskType}. Variable ts : seq Task.

**Context** `{TaskCost Task}. **Context** `{MaxArrivals Task}. **Context** `{TaskDeadline Task}.



## **SYSTEM MODEL: WORKLOAD**

**Definition** duration := nat. **Definition**  $\varepsilon := 1$ .

**Class** MaxArrivals (Task : TaskType) := max arrivals : Task  $\rightarrow$  duration  $\rightarrow$  nat.

**MPI-SWS** 









## **OVERVIEW OF CASE STUDY**

Each element corresponds to some Coq code!



## **SYSTEM MODEL: VALIDITY CONSTRAINTS**

We employ a discrete time model, and let  $\mathbb{T} = \mathbb{N}$  denote the time domain and  $\varepsilon \triangleq 1$  the indivisible least unit of time. The workload is a set of *n* sporadic real-time tasks  $\tau \triangleq \{\tau_1, \tau_2, ..., \tau_n\}$ . Each task  $\tau_i \triangleq (C_i, D_i, \alpha_i)$  has a worst-case execution time  $C_i$ , a relative deadline  $D_i$ , and an arrivalbound function  $\alpha_i(\Delta)$ . The role of  $\alpha_i(\Delta)$  is to upper-bound the number of activations of  $\tau_i$  in any time window of length  $\Delta$ .



Mathematical Language

**MPI-SWS** 

 $J_{i,j} := j^{\text{th}} job of i^{\text{th}}$ 

task

![](_page_20_Figure_6.jpeg)

![](_page_20_Picture_8.jpeg)

![](_page_21_Figure_2.jpeg)

## **OVERVIEW OF CASE STUDY**

Each element corresponds to some Coq code!

![](_page_21_Picture_8.jpeg)

## **ANALYSIS: INTERFERENCE BOUND FUNCTION**

### Our RTA applies the **busy-window principle**

 $\rightarrow$  Cumulative interference incurred within the busy window of job  $\leq$  Interference Bound Function (IBF).

![](_page_22_Figure_4.jpeg)

MPI-SWS

![](_page_22_Picture_8.jpeg)

![](_page_22_Picture_9.jpeg)

![](_page_23_Figure_2.jpeg)

## **OVERVIEW OF CASE STUDY**

Each element corresponds to some Coq code!

![](_page_23_Picture_8.jpeg)

## **ANALYSIS: FINAL RESPONSE-TIME BOUND**

### The final response-time bound is stated as a fixed point

Let R denote the search result, *i.e.*, the least positive value s.th.

$$\forall A \in \mathcal{A}, \ \exists F, \ A + F = \sum_{\tau_k \in \tau} RBF_k(A + \varepsilon) \land F \leq R.$$
 (5)

**Theorem 1.** If a finite bound L on the maximum busy-window length exists, then any job  $J_{i,j}$  of any given task  $\tau_i \in \tau$  will finish execution by time  $a_{i,j} + R$ .

### Mathematical Language

![](_page_24_Figure_9.jpeg)

### Gallina (Coq)

![](_page_24_Picture_12.jpeg)

## WHAT DID IT TAKE?

### **Proof effort:** $\approx$ 3 months

 $\rightarrow$  One person with limited prior Coq experience → And limited RTS experience

### **Proof artifact**

- $\rightarrow$  Proof artifact has since been modified
- → Comments and structure aiding accessibility
- → Artifact with proof and profusely commented specs: https://people.mpi-sws.org/~bbb/papers/details/rtss22/

## Slightly more than 400 lines of code

- → Surprisingly low
- $\rightarrow$  Made possible by building on

existing Prosa definitions

Total LOC	432
Specifications	92
Proof scripts	132
Comments	208

![](_page_25_Picture_25.jpeg)

# **EMPIRICAL EXPLORATION**

![](_page_27_Picture_1.jpeg)

### Real World Automotive Benchmarks For Free

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unsatisfactory given the potential for front loading with formal The progress and comparability of real-time analysis methods that are applicable to real-world is slowed by the absence of analysis techniques. realistic benchmarks, mainly due to intellectual property (IP) Due to the introduction of multi-core execution platforms, concerns. We propose a method that supports the generation of the risk of divergence between academic research and realistic but IP free benchmark sets. Further, we provide the industrial practice is currently increasing. The reason is the application characteristics of a specific real-world automotive strongly increased problem space for timing analysis induced software system. by multi-core systems.

*Keywords—benchmarks, timing analysis, automotive software* 

### • Generated each task $\tau_i$ with:

Period: non-uniform distribution over the set {1, 2, 5, 10, 20, 50, 100, 200, 1000} ms

Cost: Randomly generated using Kramer et al.'s tables

• For each cardinality  $\in \{2,3,\ldots,30\}$ , 500 tasks were generated

## SETUP

Extending existing approaches is very challenging since

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## **BASELINE COMPARISON**

### How does our RTA compare with the <u>baseline</u>?

### The Case for FIFO Real-Time Scheduling

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Abstract—Selecting the right scheduling policy is a crucial issue in the development of an embedded real-time application. Whereas scheduling policies are typically judged according to their ability to schedule task sets at a high processor utilizations, other concerns, such as predictability and simplicity are often overlooked. In this paper, we argue that FIFO scheduling with offsets is a suitable choice when these concerns play a key role. To this end, we examine the predictability of FIFO, present a schedulability analysis for it and evaluate both, performance and predictability of FIFO scheduling with and without offsets. Our results show that FIFO with offsets exhibits competitive performance for task with regular periods, at an unmatched predictability.

other concerns than performance such as simplicity and predictability become important.

In this context, we re-visit FIFO scheduling under modified conditions and make a case for FIFO scheduling with strictly periodic task activation and release offsets to increase the predictability and to improve the performance. The contributions of our paper are threefold:

- We show that FIFO with offsets is unique in the sense that it is both work-conserving and exhibits a single, welldefined execution order.
- We provide a schedulability analysis for FIFO, both with

Feasibility and proposed FIFO RTA curves overlap

![](_page_28_Figure_14.jpeg)

![](_page_28_Picture_16.jpeg)

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### For which workloads can FIFO be a suitable choice?

7.0 Task period 10 ms 5.0 1 ms 4.0 20 ms 2 ms 50 ms 3.0 5 ms 2.0 FIFO Ratio of response times Ratio: FP /  $\cap$ 0.7 of tasks in FP and FIFO 0.5 schedules 0.4 0.3 0.2 0.1

![](_page_29_Figure_5.jpeg)

![](_page_29_Picture_9.jpeg)

CONCLUSION

## CONCLUSION

### **Case study**

- → Similarity of formal and intuitive arguments
- → Roadmap for formalizing RTS results

### **Empirical exploration**

- → Proposed RTA works for all feasible workloads
- → FIFO scheduling beneficial for lower rate-tasks (at the expense of higher-rate tasks)

![](_page_31_Picture_8.jpeg)

https://people.mpi-sws.org/~bbb/papers/details/rtss22/

For a one-to-one mapping of pen and paper results to code, check out the Prosa webpage!

# formally proven schedulability analysis

https://prosa.mpi-sws.org/

### Library prosa.results.fifo.rta

- Response-Time Analysis for FIFO Schedulers
  - A. Defining the System Model
    - Tasks and Jobs
    - The Job Arrival Sequence
    - Absence of Self-Suspensions and WCET Compliance
    - The Task Set
    - The Task Under Analysis
    - The Schedule
  - B. Encoding the Scheduling Policy and Preemption Model
  - Classic and Abstract Work Conservation ° C.
  - D. Bounding the Maximum Busy-Window Length
  - E. Defining the Interference Bound Function (IBF)
    - Absence of Priority Inversion
    - Higher- and Equal-Priority Interference
    - Correctness of IBF
  - F. Defining the Search Space
  - G. Stating the Response-Time Bound R
  - H. Soundness of the Response-Time Bound

![](_page_31_Picture_34.jpeg)

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