Complexity of Multiprocessor Blocking Analysis with Nested Critical Sections

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This talk

Complexity of tight blocking analysis for:

- multiprocessor systems
- locks with strong ordering guarantees
- nested critical sections







The Blocking Analysis Problem



Worst-case blocking duration?

The Blocking Analysis Problem



Worst-case blocking duration?

Blocking Analysis Problem:

Bound the blocking duration that a task can incur in the worst case.

The Blocking Analysis Problem



Worst-case blocking duration?

Blocking Analysis Problem:

Bound the blocking duration that a task can incur in the worst case.

Tight Blocking Bounds:

There is a schedule in which the blocking bound is reached.

Main Result

multiprocessor system

locks with strong ordering guarantees nested critical sections

Main Result

FIFO/priority

multiprocessor system

ocks with strong ordering guarantees

nested critical sections

Main Result



Context: Mutex Locks

protect shared resources such as

- shared bus
- shared data structures
- peripheral devices

Context: Mutex Locks



Context: Mutex Locks



multiprocessor system

locks with strong ordering guarantees nested critical sections



So what? Formally, most interesting scheduling problems are hard!

Complexity of Scheduling-Related Problems

- Response-Time Analysis (Eisenbrand and Rothvoß, 2008)
- Deciding Periodic Task Set Feasibility (Leung and Whitehead, 1982)
- Scheduling Task Sets with Self-Suspensions (Ridouard et al., 2006)
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Blocking analysis is a seemingly much easier problem!

for commonly used protocols

Architecture



for commonly used protocols

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for commonly used protocols



for commonly used protocols

Architecture

		Uniprocessor	Multiprocessor
Nested Critical Sections	no	polynomial	polynomial
	yes	polynomial	

for commonly used protocols

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Nested Critical Sections	no	polynomial	polynomial
	yes	polynomial	NP-hard

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Architecture

		Uniprocessor	Multiprocessor
Nested Critical Sections	no	polynomial	polynomial
	yes	polynomial	NP-hard

What makes it difficult on multiprocessors with nesting?

Outline

- Introduction
- Intuition: Why does nesting make the analysis difficult?
- Reduction: From Multiple Choice Matching to Blocking Analysis
- Summary and Conclusion

What makes the blocking analysis difficult?

Architecture



26

What makes the blocking analysis difficult?

Architecture

		Uniprocessor	Multiprocessor
Nested Critical Sections	no	polynomial	polynomial
	yes	polynomial	NP-hard

FIFO-ordered locks:

not a processor-local problem: Consider all critical sections from all processors at once!





FIFO-ordered spin locks

Each critical section can be blocked by **at most one** critical section for the same resource **from each remote processor**.












Similarities to Matching

Blocking analysis is non-local problem!

Similarities to Matching

Blocking analysis is non-local problem!

Blocking Analysis at least as hard as Multiple Choice Matching

Input:

- graph **G=(V,E)**
- edge partitions E₁,...,E_t

Problem:



Problem:



Problem:



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Problem:







Encoding Vertices as Resources

Multiple-Choice Matching Problem



Encoding Vertices as Resources



Encoding Edges as Nested Critical Sections

Multiple-Choice Matching Problem



Encoding Edges as Nested Critical Sections



edges \rightarrow outer critical sections







Multiple-Choice Matching Problem



Multiple-Choice Matching Problem





Multiple-Choice Matching Problem



Is there a set of edges **F** such that

F contains exactly one green and one purple edge



and

?

Multiple-Choice Matching Problem



Is there a set of edges **F** such that

F contains exactly one green and one purple edge

all edges in **F** are pairwise disjoint



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Multiple-Choice Matching Problem



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Blocking Analysis Problem



Is there a worst-case schedule such that

exactly one CS for **D** from CPU 1 and CPU 2 block

Multiple-Choice Matching Problem



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Example: MCM Solution Does Not Exist

Multiple-Choice Matching Problem



Is there a set of edges **F** such that

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Is there a worst-case schedule such that

exactly one C/3 for D rom CPU 1 and C/PU 2 block

no short CS from CPU 3 transitively blocks

Example: MCM Solution Does Not Exist

Multiple-Choice Matching Problem



Is there a set of edges **F** such that

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Blocking Analysis Problem



Is there a worst-case schedule such that

exactly one CS for **D** from CPU 1 and CPU 2 block

no short CS from CPU 3 transitively blocks

Result of blocking analysis is **bound on blocking duration!**

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and

Is there a worst-case schedule such that

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no short CS from CPU 3 transitively blocks

Result of blocking analysis is **bound on blocking duration!**

Is there a worst-case schedule such that

exactly one CS for **D** from CPU 1 and CPU 2 block

no short CS from CPU 3 transitively blocks



How to derive from blocking bound whether conditions hold?

Blocking Analysis Problem



Blocking Analysis Problem



Choose critical section lengths such that it can be inferred from the blocking bound:

- how many requests from CPU 1 and CPU 2 block, and
- how many short requests from CPU 3 transitively block.

Blocking Analysis Problem



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Generality of Reduction



Generality of Reduction



Reduction uses a single job per processor.

Generality of Reduction

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Reduction oblivious to

spin based vs. suspension based locks preemptable vs. nonpreemptable spinning

any work conserving scheduler

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Summary

multiprocessor system

locks with strong ordering guarantees (FIFO/Priority)

nested critical sections



Blocking analysis **NP-hard!**

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Blocking analysis **NP-hard!**



Reduction from Multiple Choice Matching Problem

Future Work

FIFO/priority-ordered locks: Approximation hardness? PTAS?

Future Work

FIFO/priority-ordered locks: Approximation hardness? PTAS?

Nesting and efficient analysis possible?

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