THE CASE FOR AN OPINIONATED, THEORY-ORIENTED REAL-TIME OPERATING SYSTEM

NGOSCPS'19 April 15, 2019

Björn Brandenburg <u>bbb@mpi-sws.org</u>



MAX PLANCK INSTITUTE FOR SOFTWARE SYSTEMS





European Research Council Established by the European Commission



MAX-PLANCK-GESELLSCHAFT

REAL-TIME SYSTEMS A SUCCESS STORY

FIVE DECADES OF REAL-TIME SYSTEMS RESEARCH

Scheduling Algorithms for Multiprogramming in a Hard-Real-Time Environment

C. L. LIU

Project MAC, Massachusetts Institute of Technology

AND

JAMES W. LAYLAND

Jet Propulsion Laboratory, California Institute of Technology

ABSTRACT. The problem of multiprogram scheduling on a single processor is studied from the viewpoint of the characteristics peculiar to the program functions that need guaranteed service. It is shown that an optimum fixed priority scheduler possesses an upper bound to processor utilization which may be as low as 70 percent for large task sets. It is also shown that full processor utilization can be achieved by dynamically assigning priorities on the basis of their current deadlines. A combination of these two scheduling techniques is also discussed.

KEY WORDS AND PHRASES: real-time multiprogramming, scheduling, multiprogram scheduling, dynamic scheduling, priority assignment, processor utilization, deadline driven scheduling

CR CATEGORIES: 3.80, 3.82, 3.83, 4.32

1. Introduction

MPI-SWS

The use of computers for control and monitoring of industrial processes has expanded greatly in recent years, and will probably expand even more dramatically in the near future. Often, the computer used in such an application is shared between a certain number of time-critical control and monitor functions and a nontime-critical batch processing job stream. In other installations, however, no non-time-critical jobs exist, and efficient use of the computer can only be achieved by a careful scheduling of the time-critical control and monitor functions themselves. This latter group might be termed "pure process control" and provides the background for the combinatoric scheduling analyses presented in this paper. Two

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This paper presents the results of one phase of research carried out at the Jet Propulsion Laboratory, California Institute of Technology, under Contract No. NAS-7-100, sponsored by the National Aeronautics and Space Administration.

Authors' present addresses: C. L. Liu, Department of Computer Science, University of Illinois at Urbana-Champaign, Urbana, IL 61801; James W. Layland, Jet Propulsion Laboratory, California Institute of Technology, 4800 Oak Grove Drive, Pasadena, CA 91103.

Journal of the Association for Computing Machinery, Vol. 20, No. 1, January 1973, pp. 46-61.

(Liu & Layland, 1973)

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\approx 12k citations (Google Scholar)

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\rightarrow (where) does this get used in practice???

≈12k citations (Google Scholar)

MAJOR USERS OF REAL-TIME SYSTEMS TECHNOLOGY

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To name just a few examples...

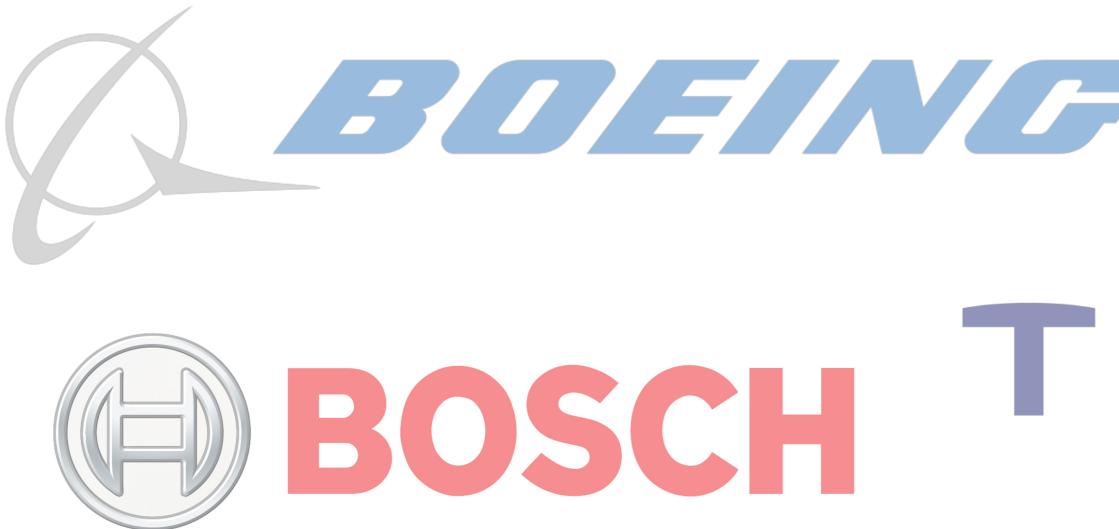
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Honeywell

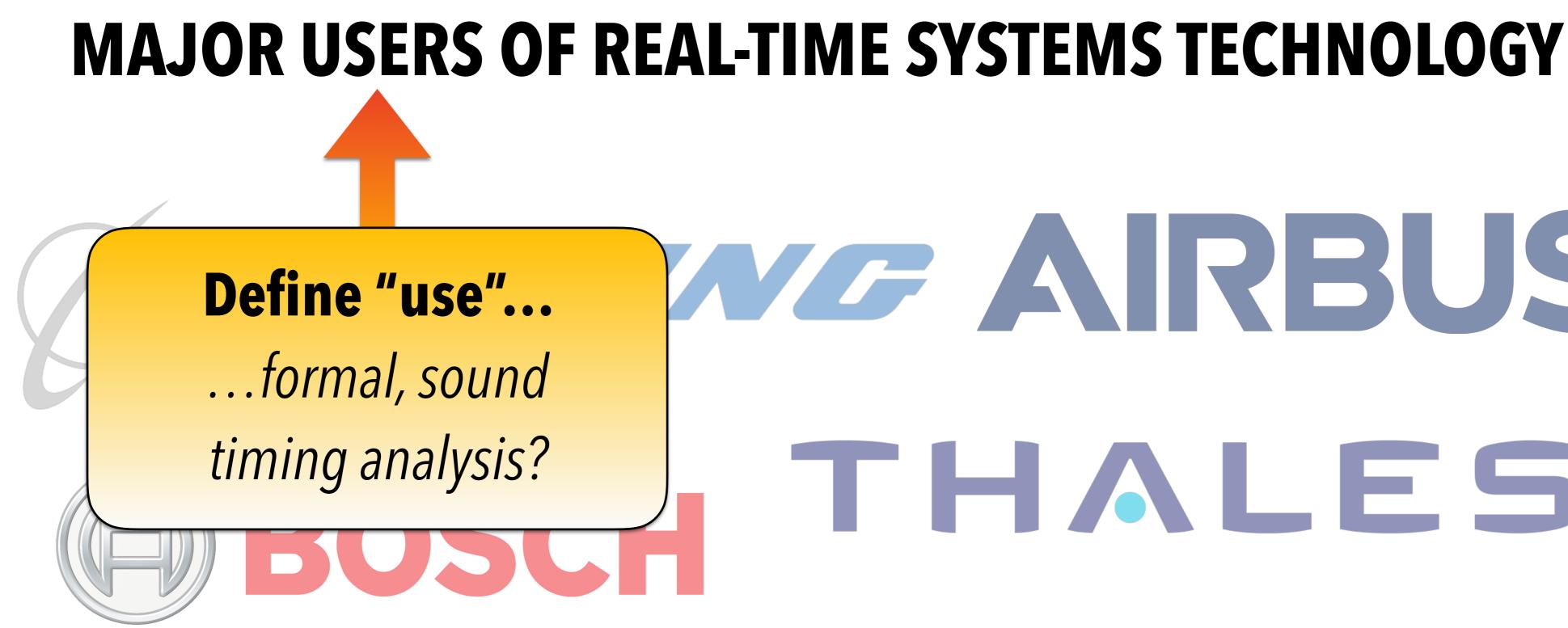
Rockwell Colins

MAJOR USERS OF REAL-TIME SYSTEMS TECHNOLOGY EDENCE AIRBUS BOSCH THALES



Large international companies with large R&D budgets and dedicated real-time specialists

 \rightarrow Tremendous amount of in-house real-time expertise!



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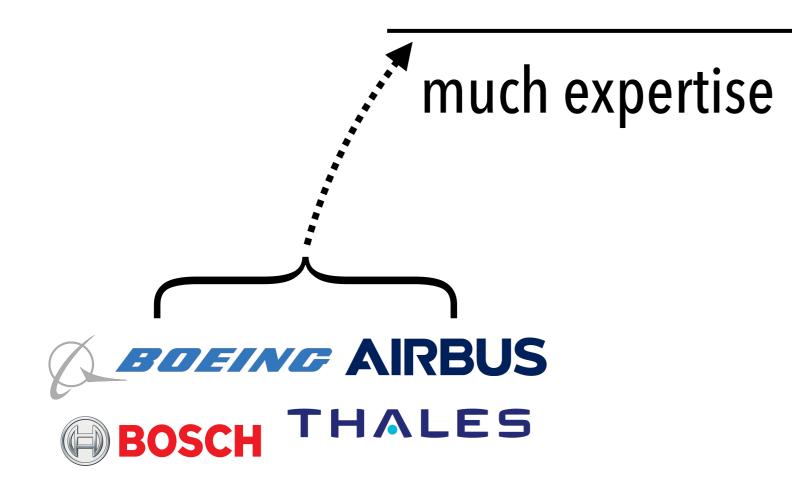
BUT WHAT ABOUT USERS IN THE "LONG TAIL"?

much expertise

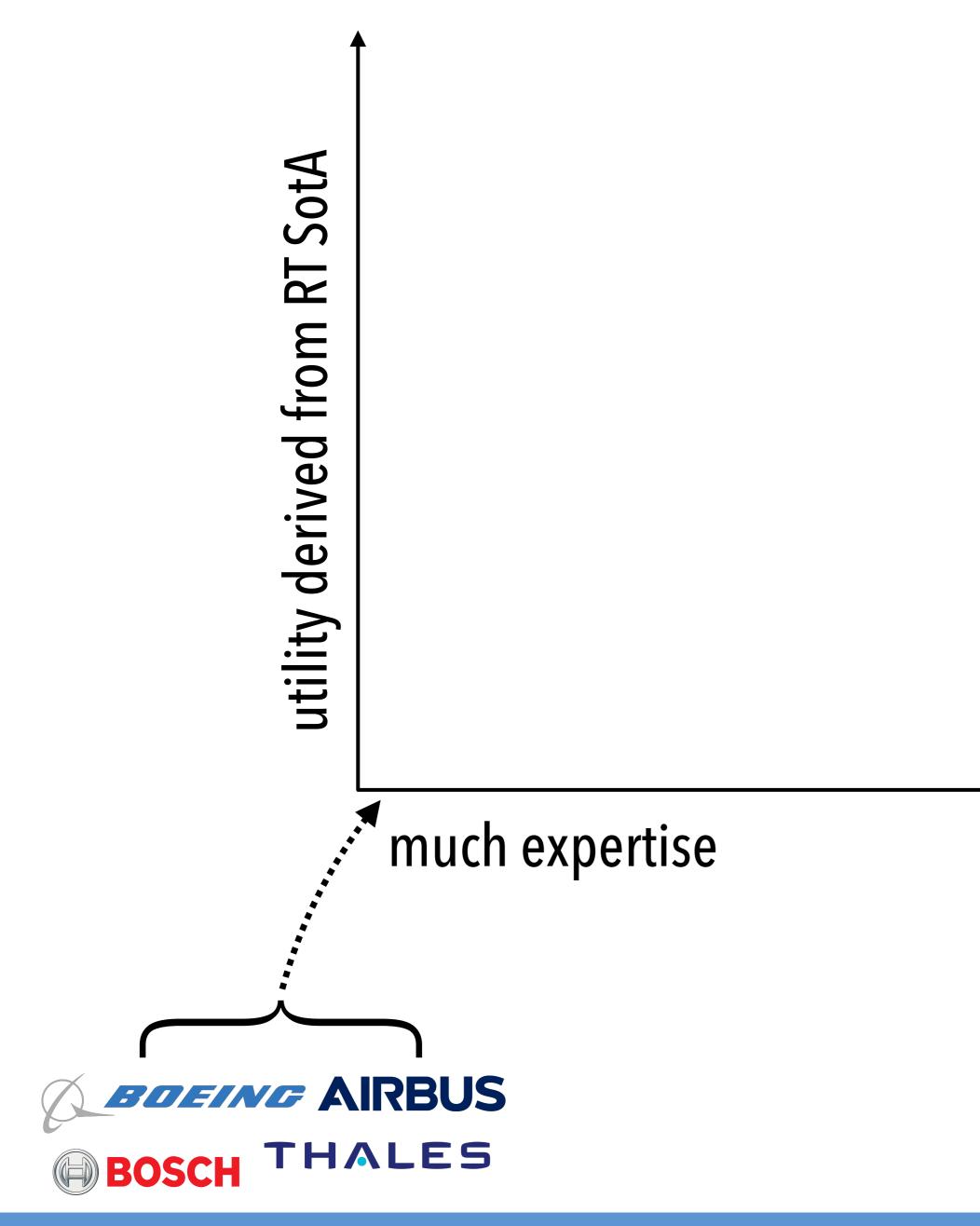


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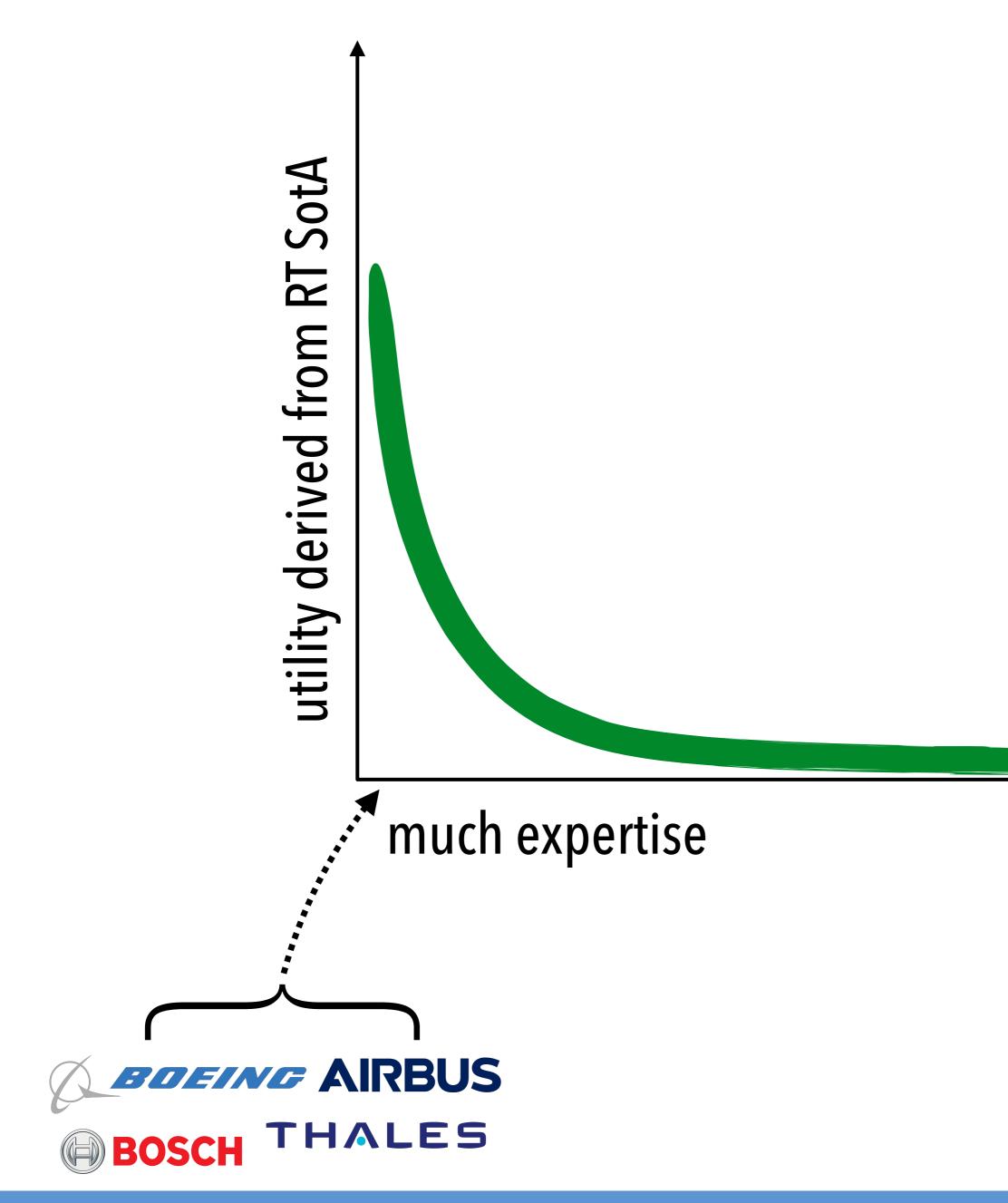
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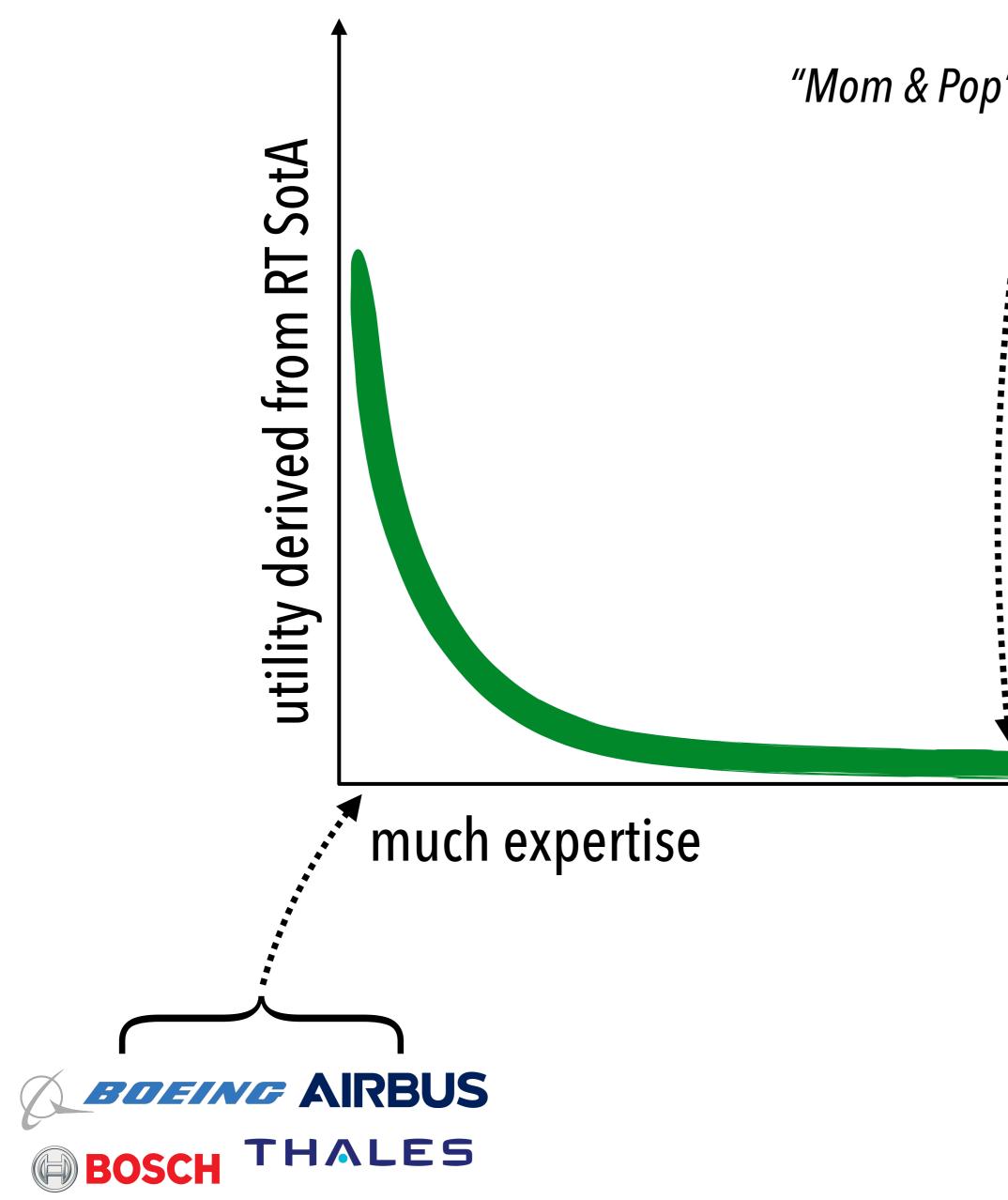
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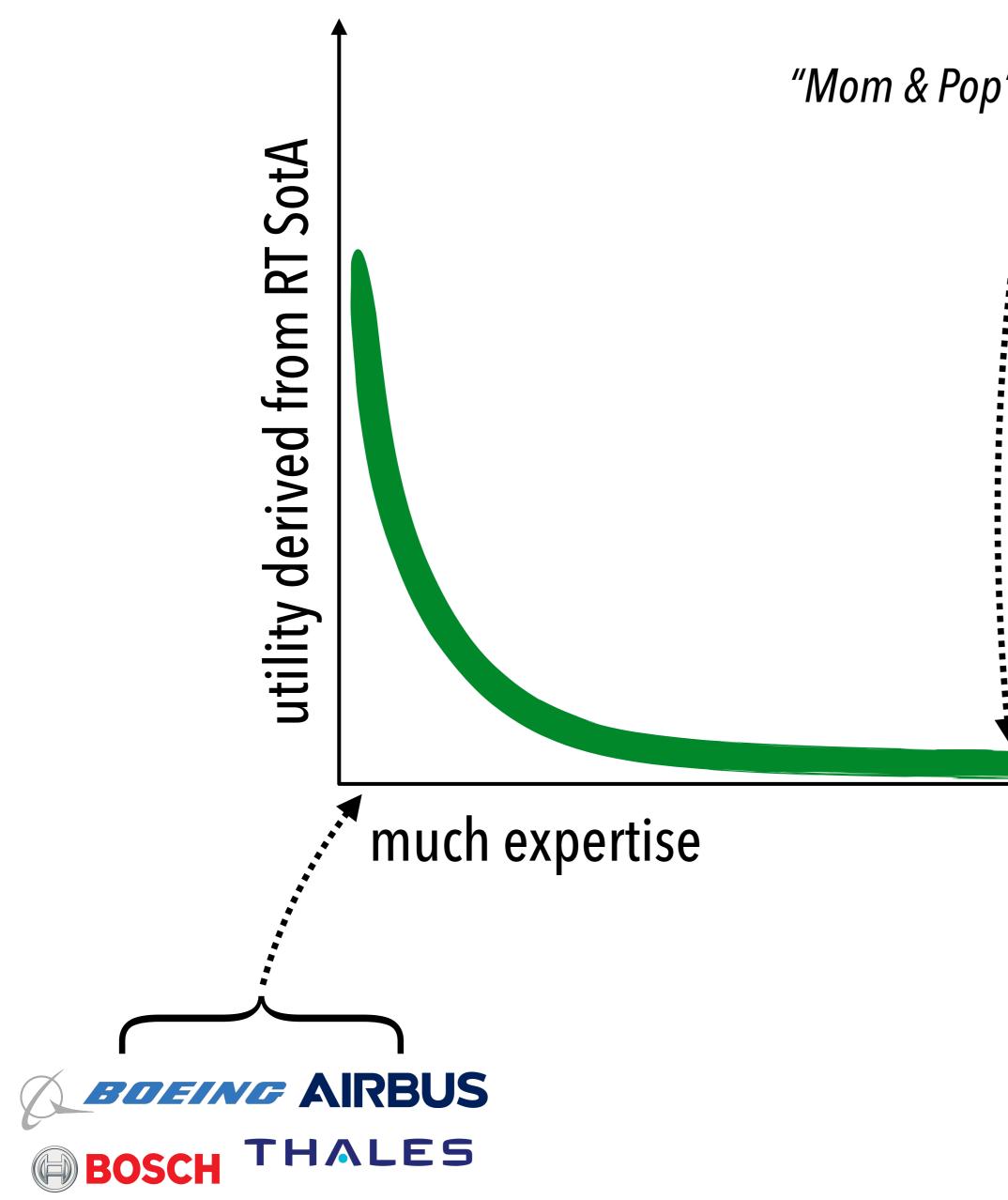
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"Mom & Pop's Artisanal UAVs"



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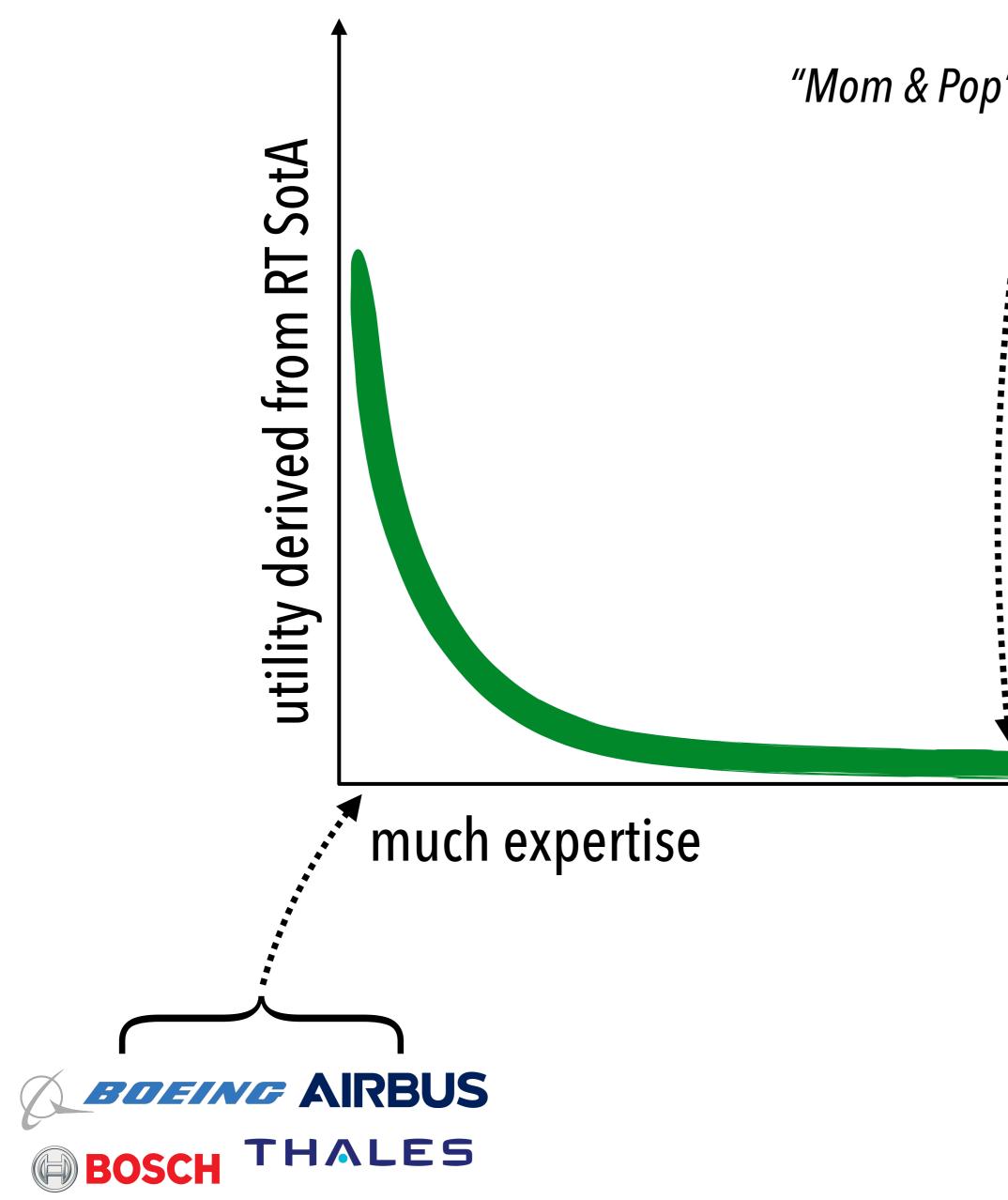
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Less technologically savvy consumer electronics companies...



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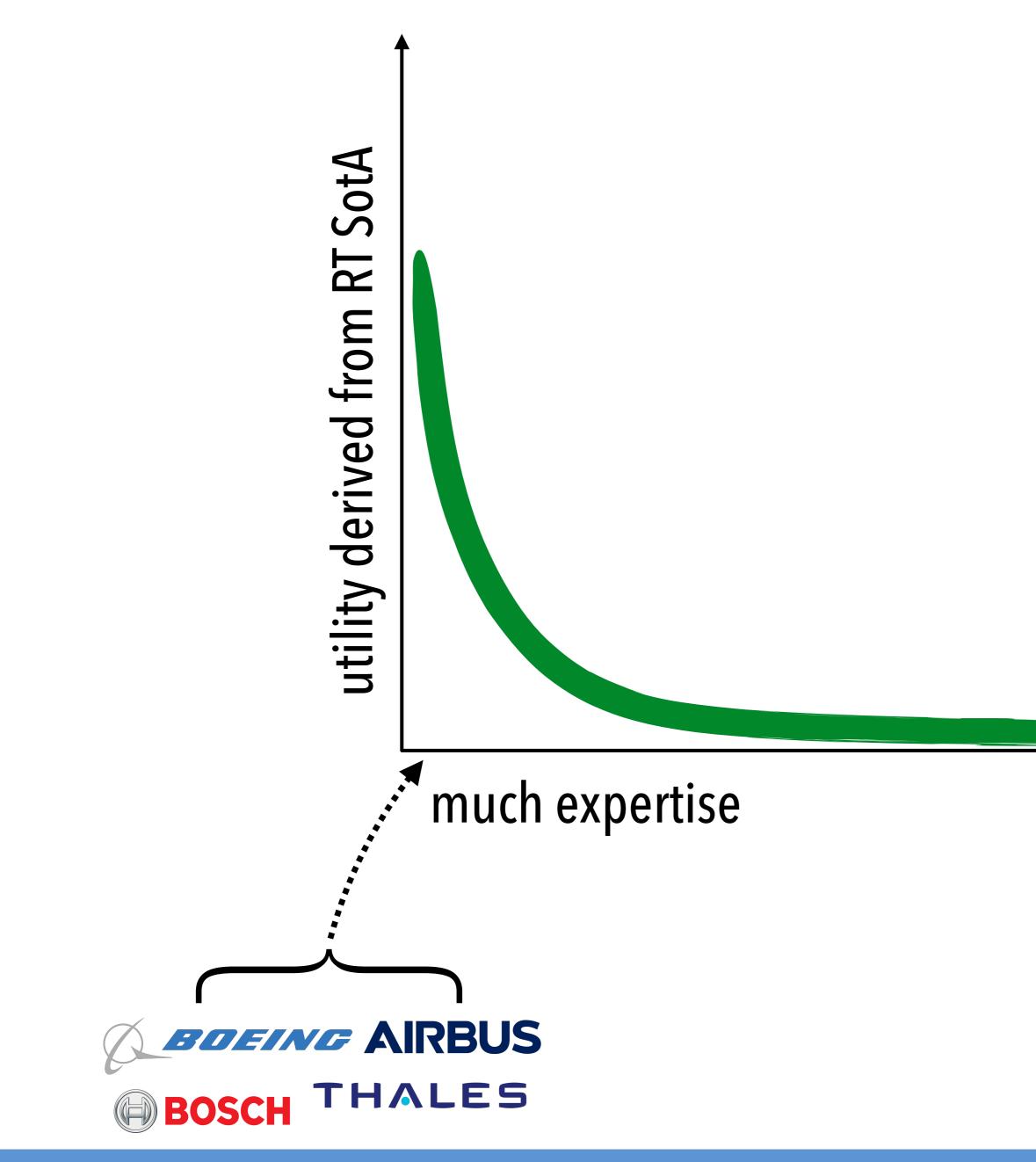


Less technologically savvy consumer electronics companies...



<u>Download Linux & PREEMPT-RT & ROS</u> → many applied robotics **researchers**...

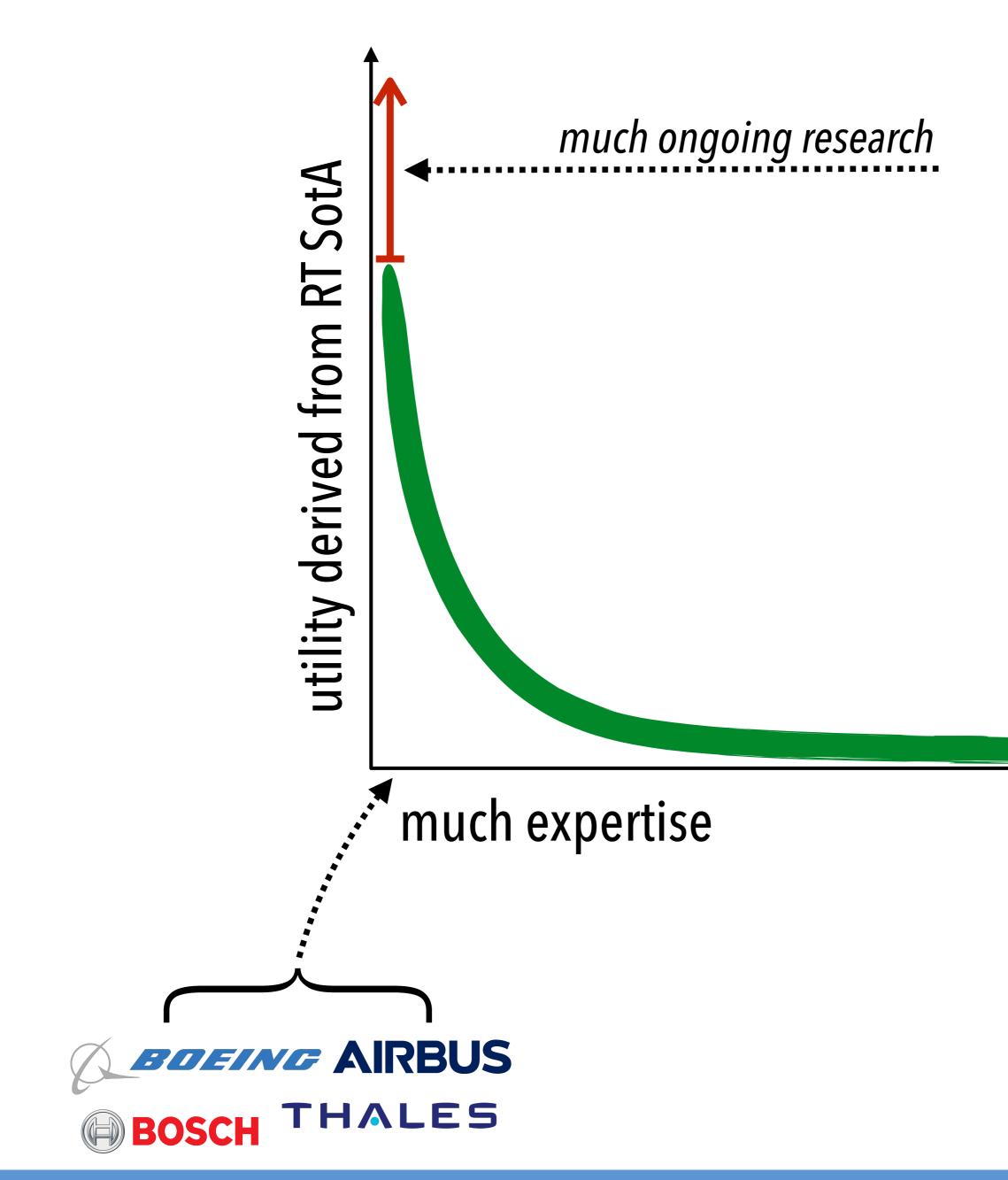
MOTIVATING OBSERVATION



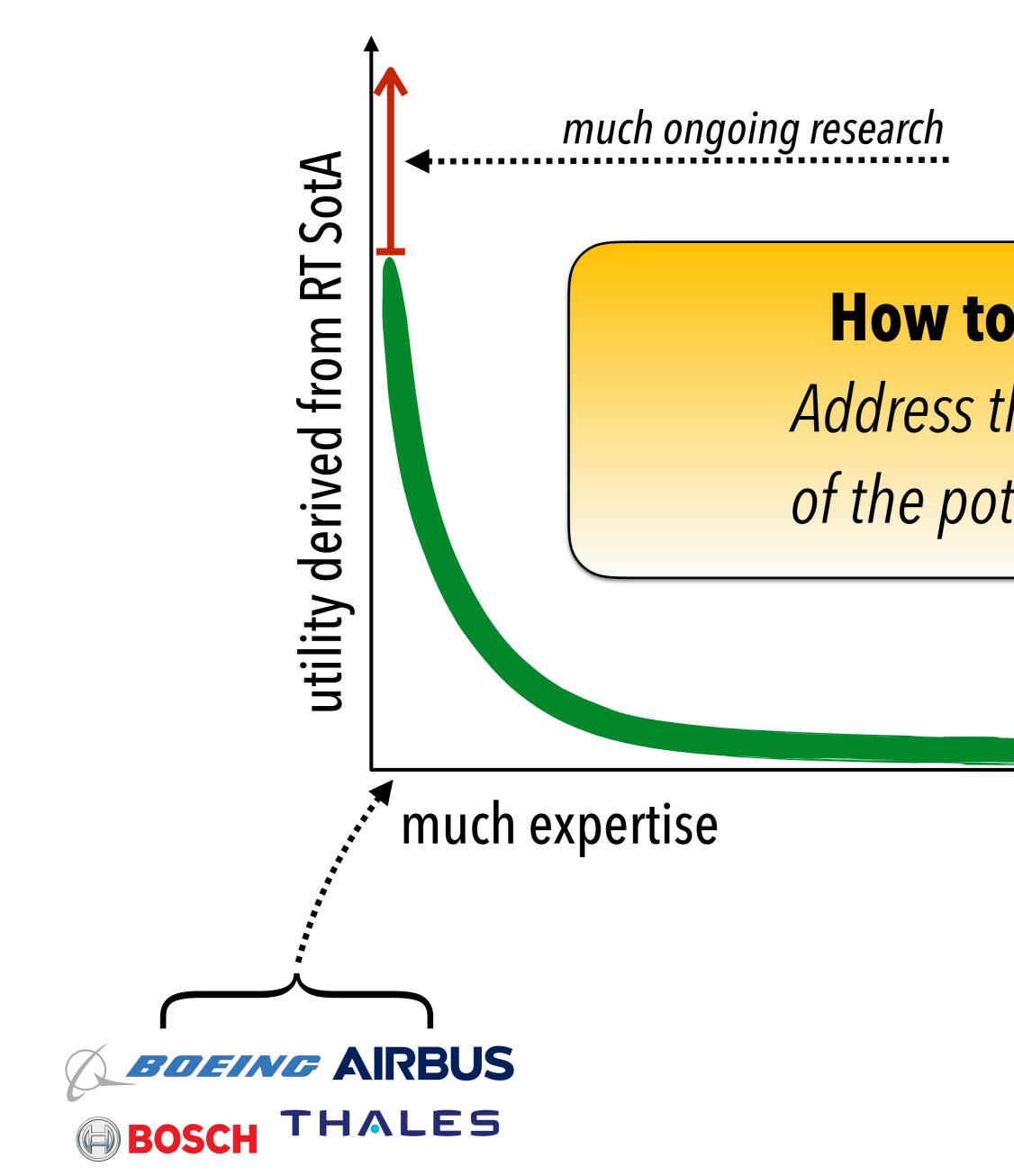
MPI-SWS

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MOTIVATING OBSERVATION



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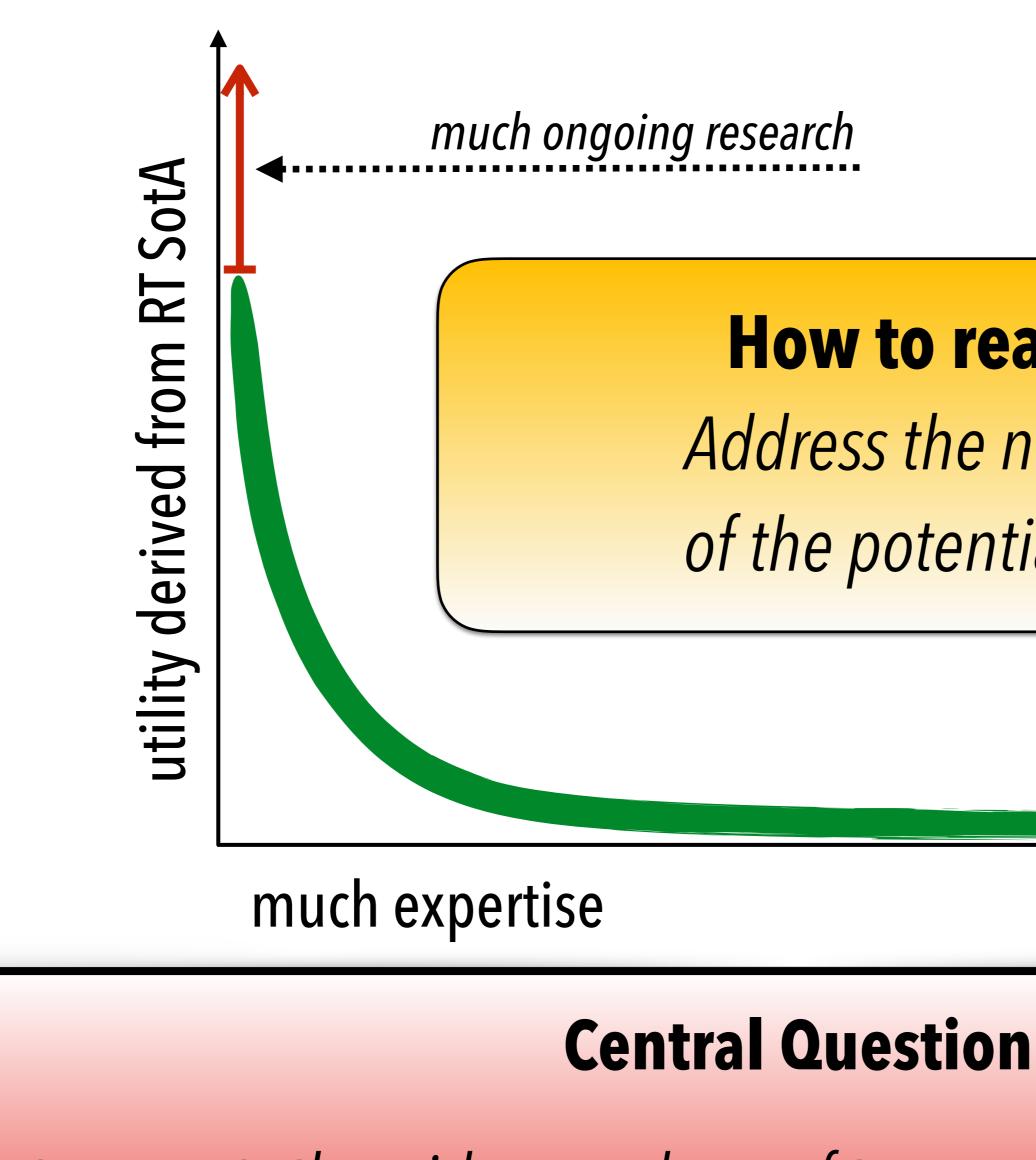


MPI-SWS

How to reach these users?

Address the needs of the "fat tail" of the potential users population.

MOTIVATING OBSERVATION



What prevents the widespread use of temporally sound system design?

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MPI-SWS

How to reach these users?

Address the needs of the "fat tail" of the potential users population.

HURDLES TO ADOPTION

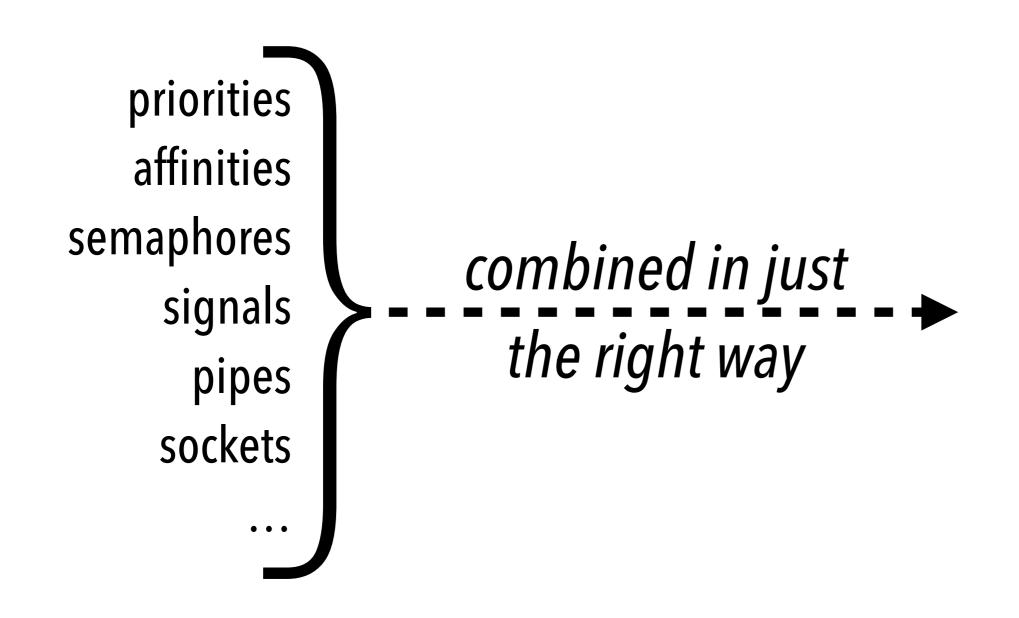
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priorities affinities semaphores signals pipes sockets

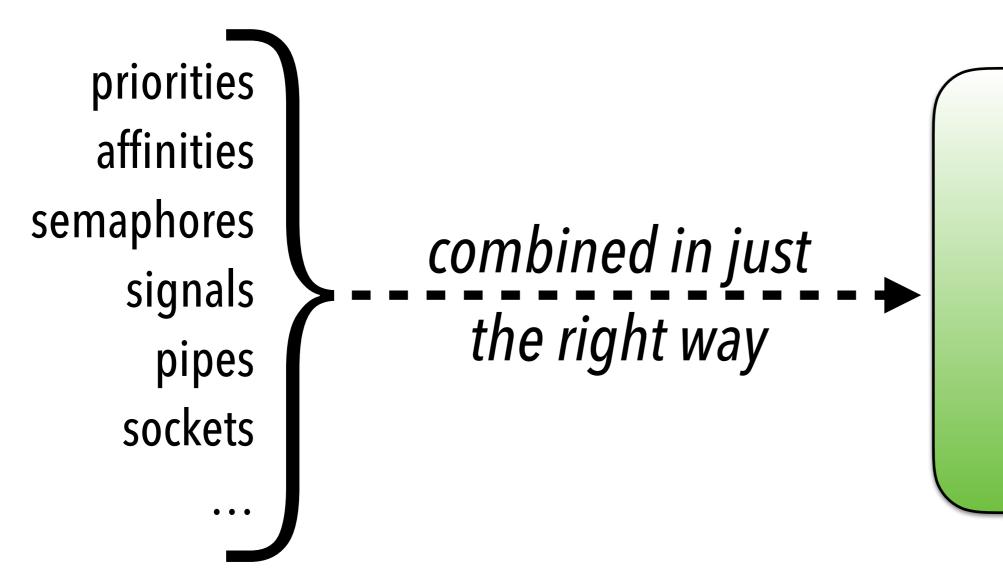
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EXPERTISE BARRIER

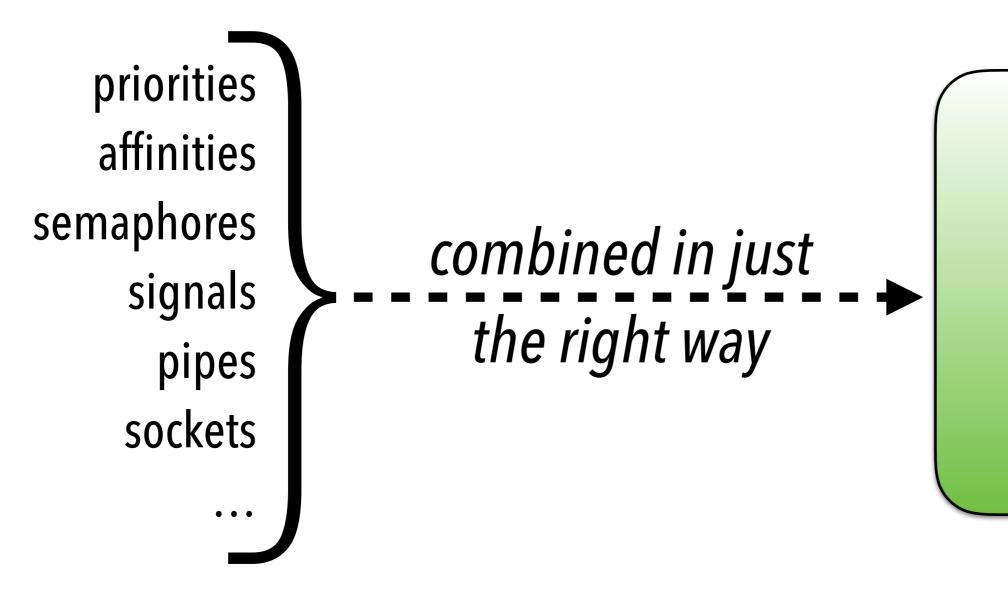
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In theory: temporally sound, predictable system amenable to formal analysis

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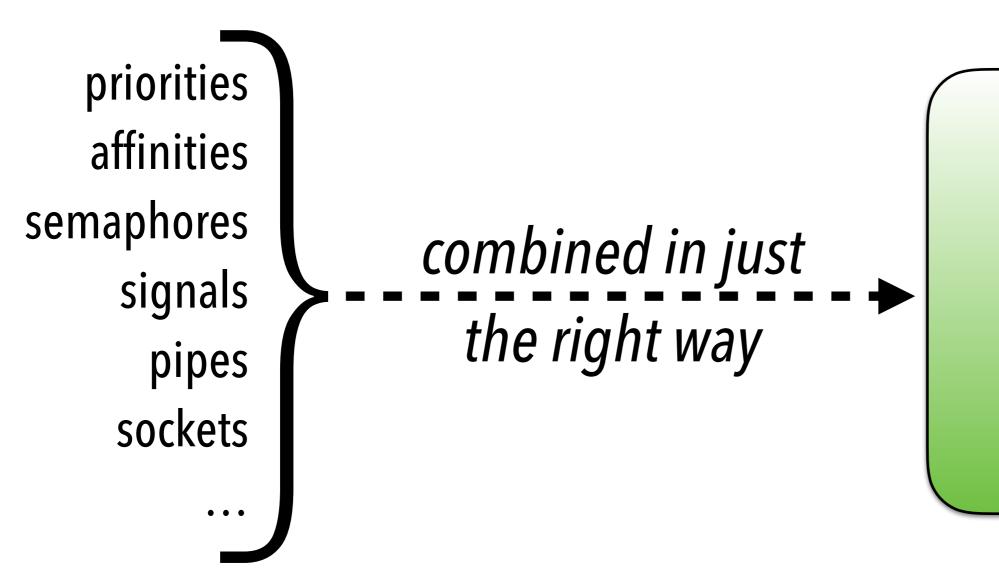
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If you know which pitfalls to avoid…

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In practice: Domain experts are rarely also scheduling and timing analysis experts – and why should they be?

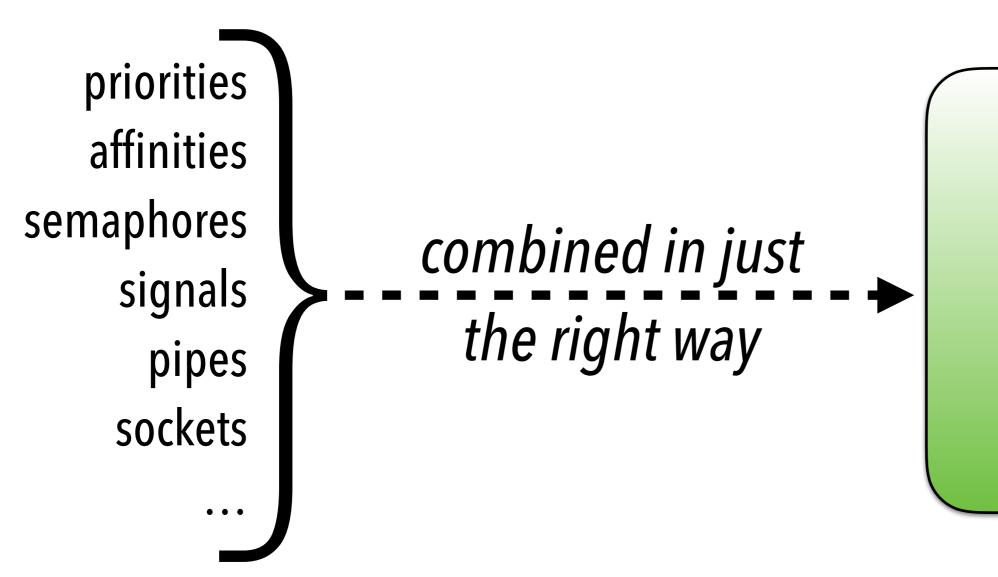
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EXPERTISE BARRIER

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Don't need to be a compact flash expert to store a file...! Don't need to be a concurrency control expert to query a database...!

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If you know which pitfalls to avoid...

COMPLEX TOOLING Who wants to add "yet another tool" as a build dependency?

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Static Timing Analysis Tooling Today \$\$\$ and/or not exactly user-friendly and/or difficult to integrate

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This may work for customers that **can't avoid** it...

B. Brandenburg

<u>Fooling Today</u>	
riendly	
grate	
<i>strictive</i>	

...but it won't entice users in the "tail".

10

Suppose:

- → wake-ups of SCHED_FIFO tasks **automatically** tracked • over finite window (e.g., one second)
- \rightarrow re-compute response-time bound whenever observations change
 - based on <u>observed</u> peak arrivals and <u>observed</u> CPU consumption
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- → performance testing
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Formal, sound timing analysis based on estimated parameters "for free"!

 \rightarrow much higher confidence from existing testing

ADAPTIVE BELOW-WORST-CASE PROVISIONING

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Standard Assumptions in the RT Literature

- \rightarrow static workload
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Reality

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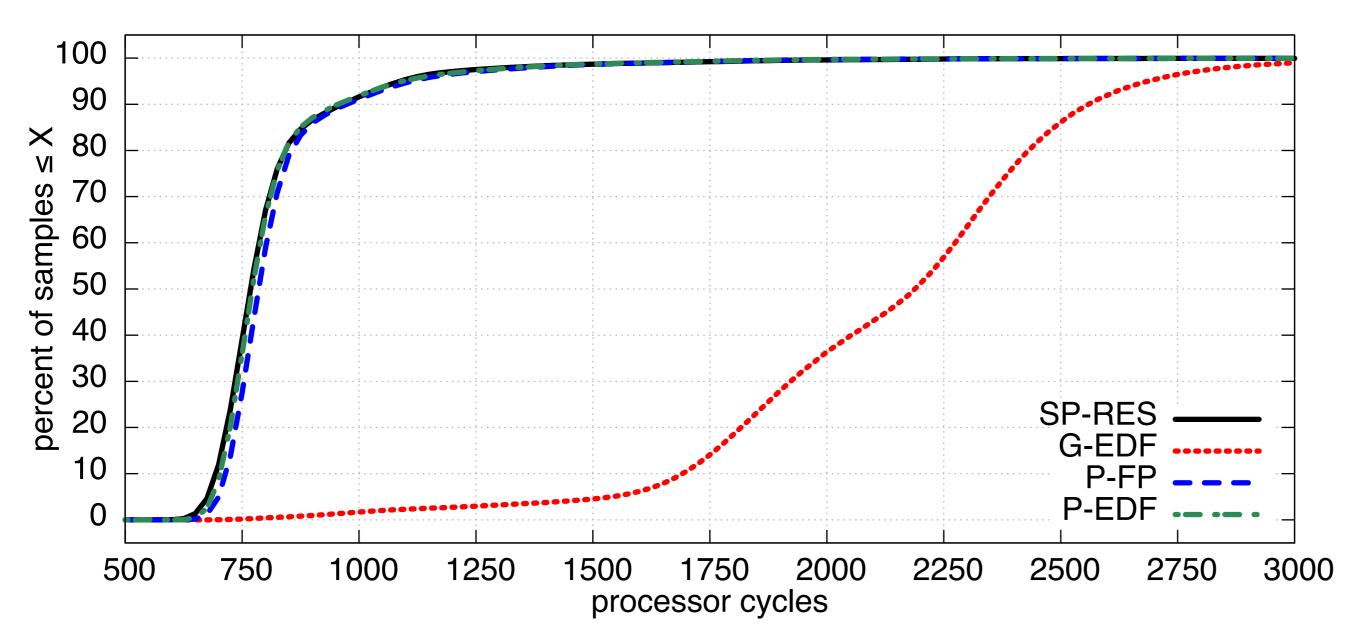
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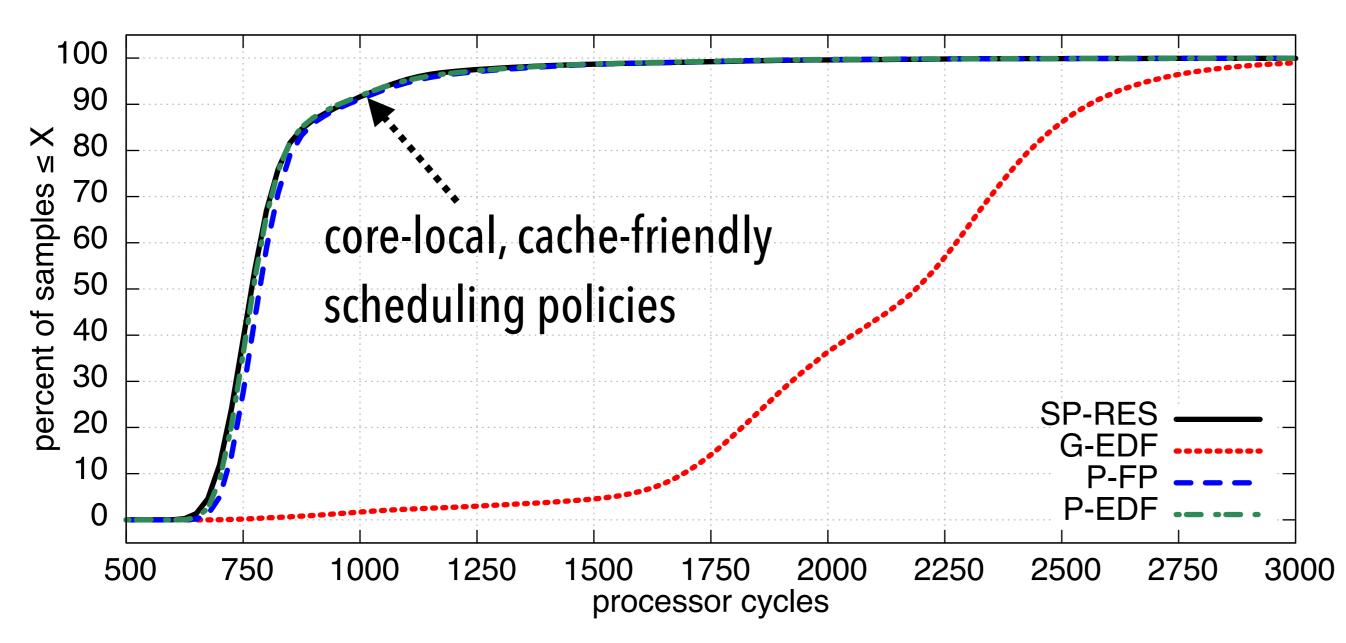
The essence of real-world engineering is graceful degradation

rather than static worst-case guarantees that are established once and then hold "forever".



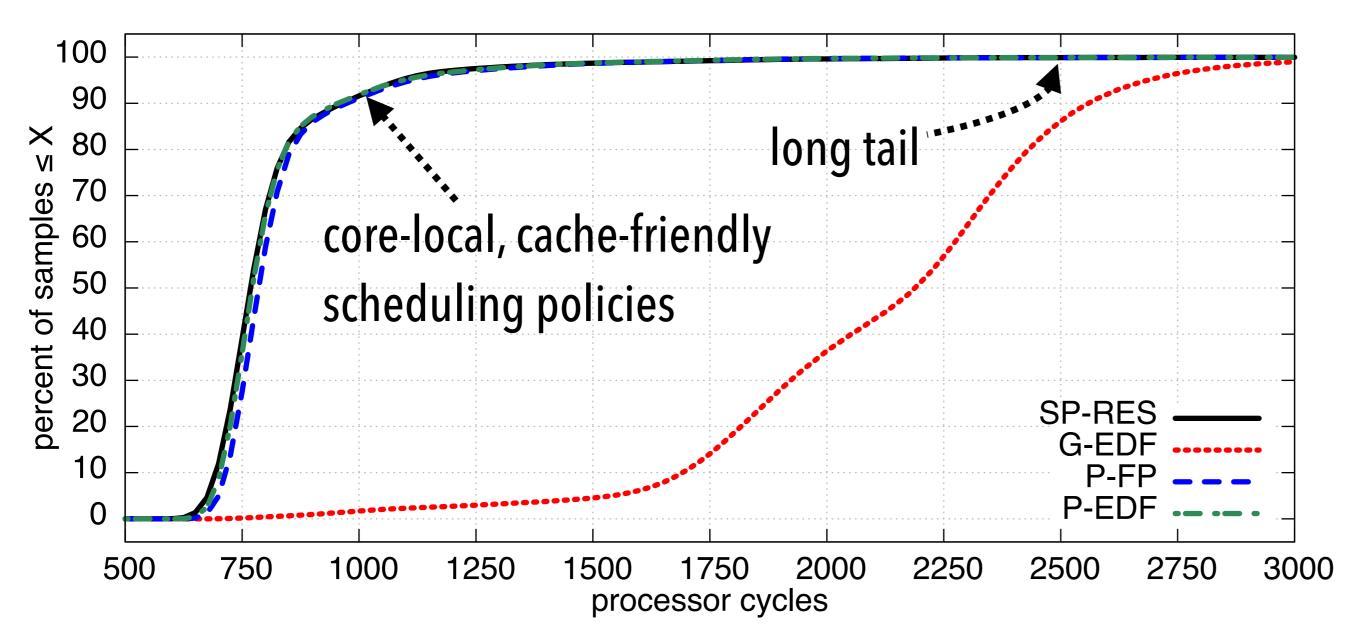


Xeon E5-2699 v4 @ 2.2 GHz [RTSS'16]



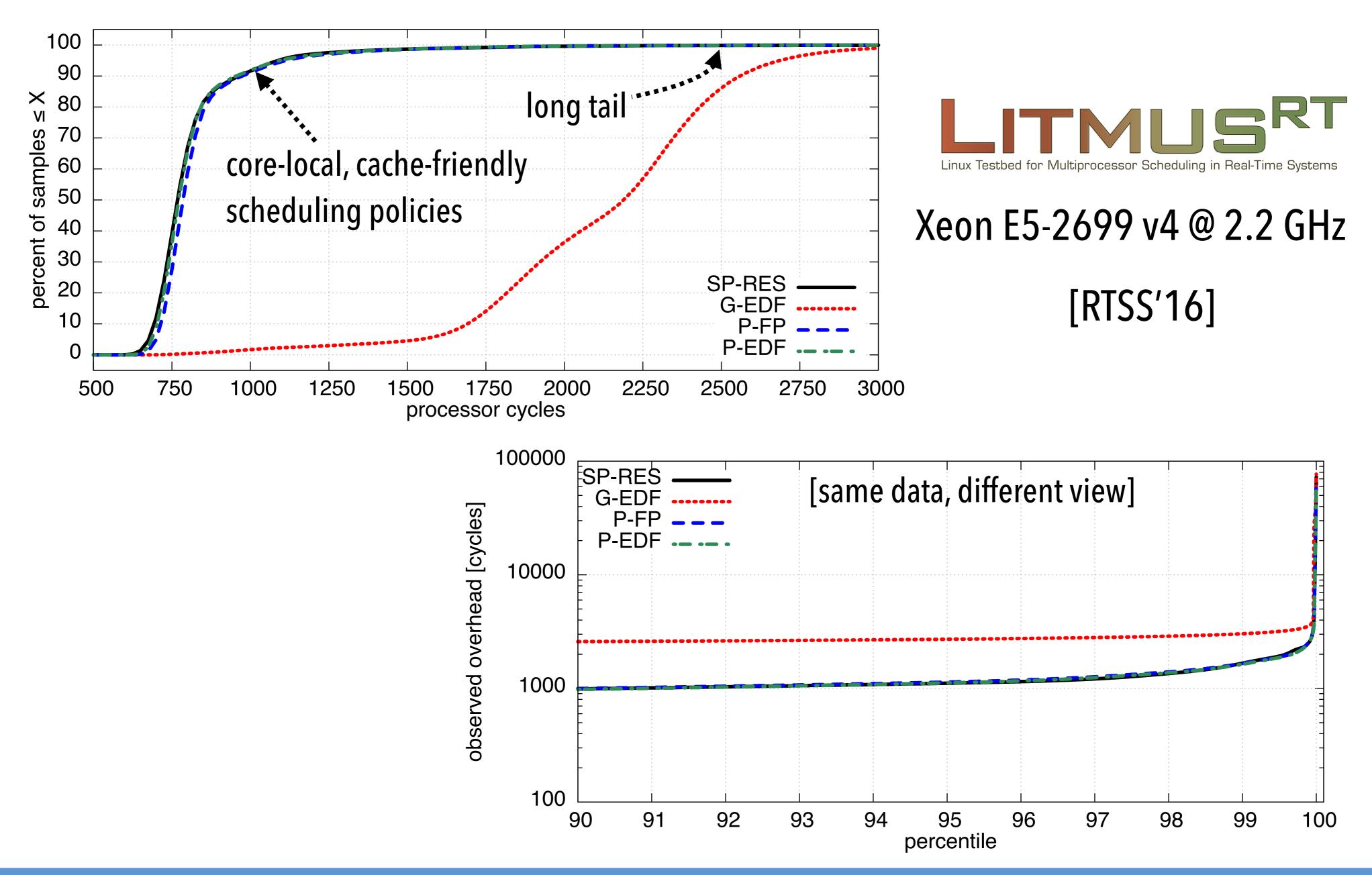


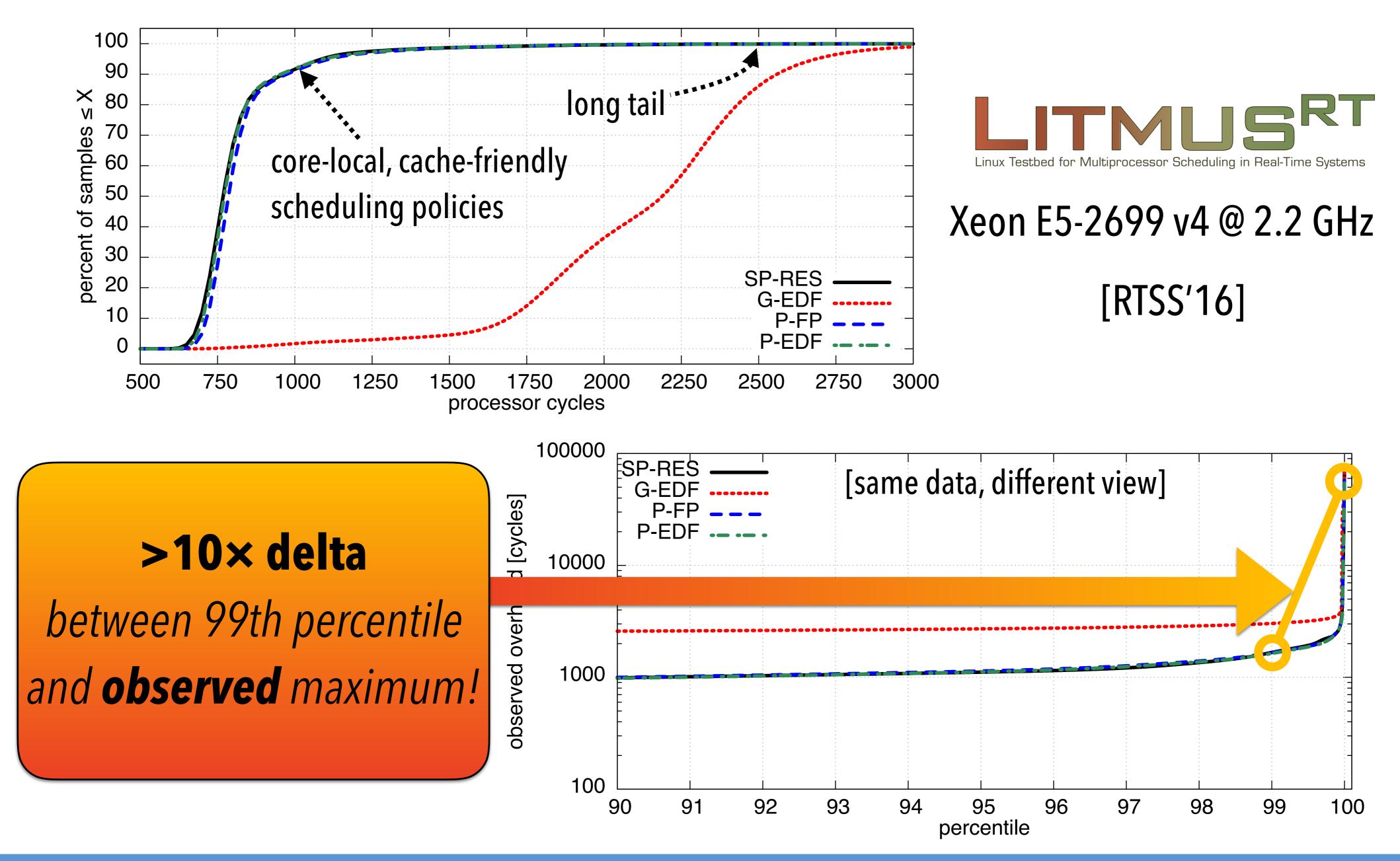
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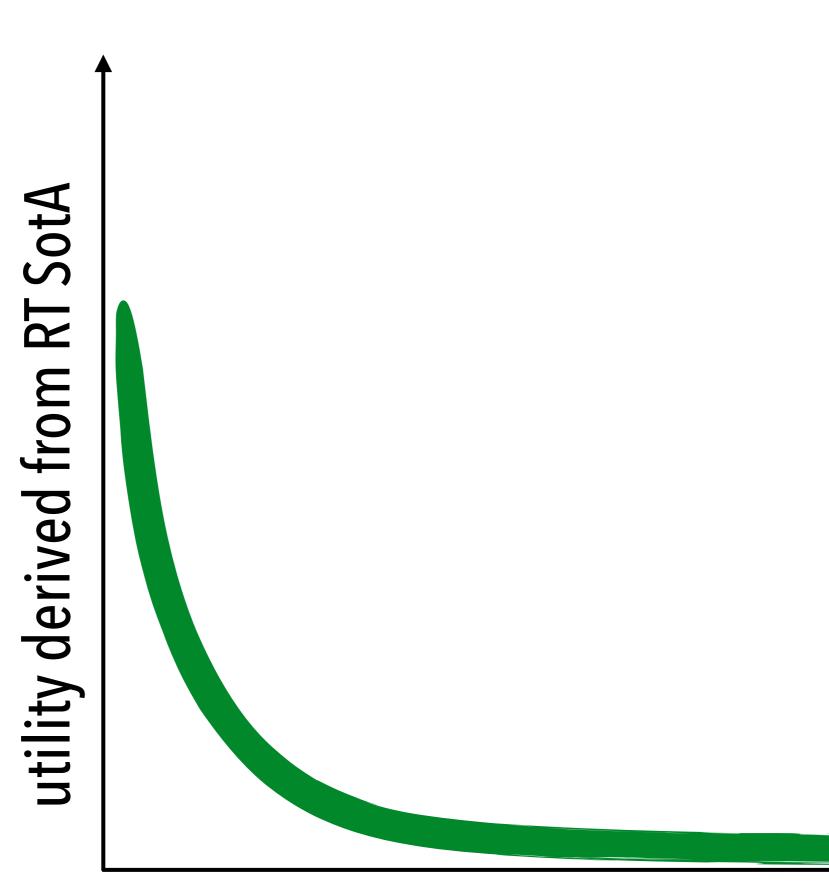


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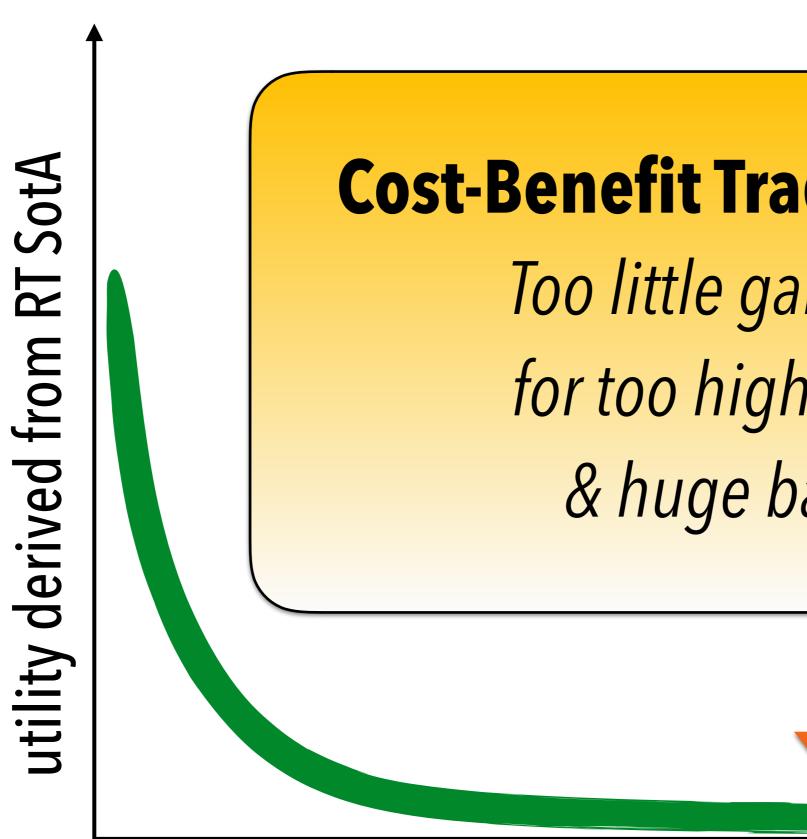
THE PROBLEM



much expertise

little expertise

THE PROBLEM



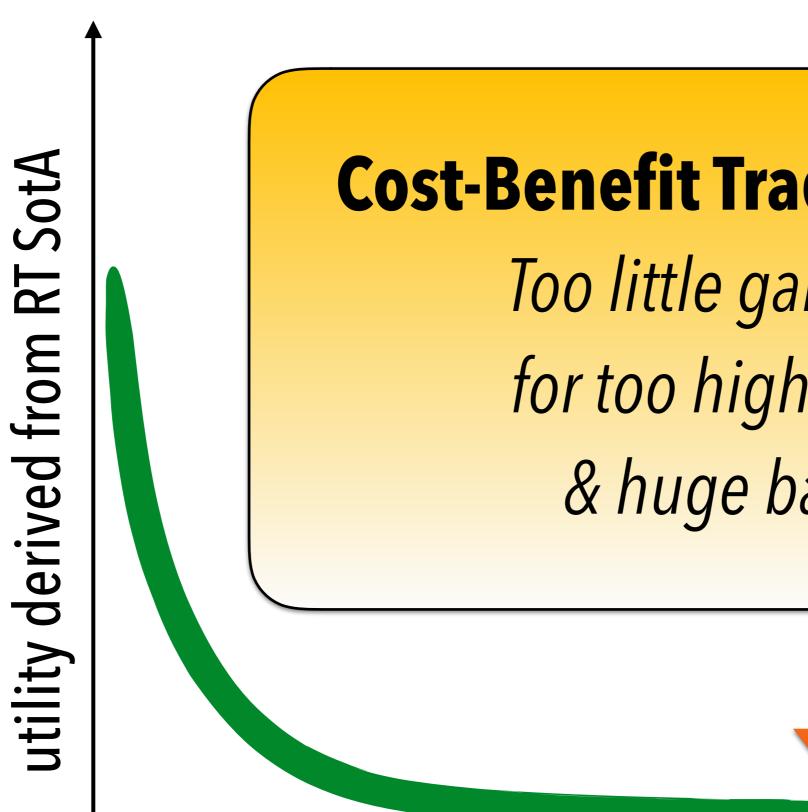
much expertise

Cost-Benefit Tradeoff not Favorable *Too little gain in confidence for too high an investment,*

& huge barrier to entry.



THE PROBLEM



much expertise

Current **RTOSs** expose primarily difficult-to-use, **low-level mechanisms**

MPI-SWS

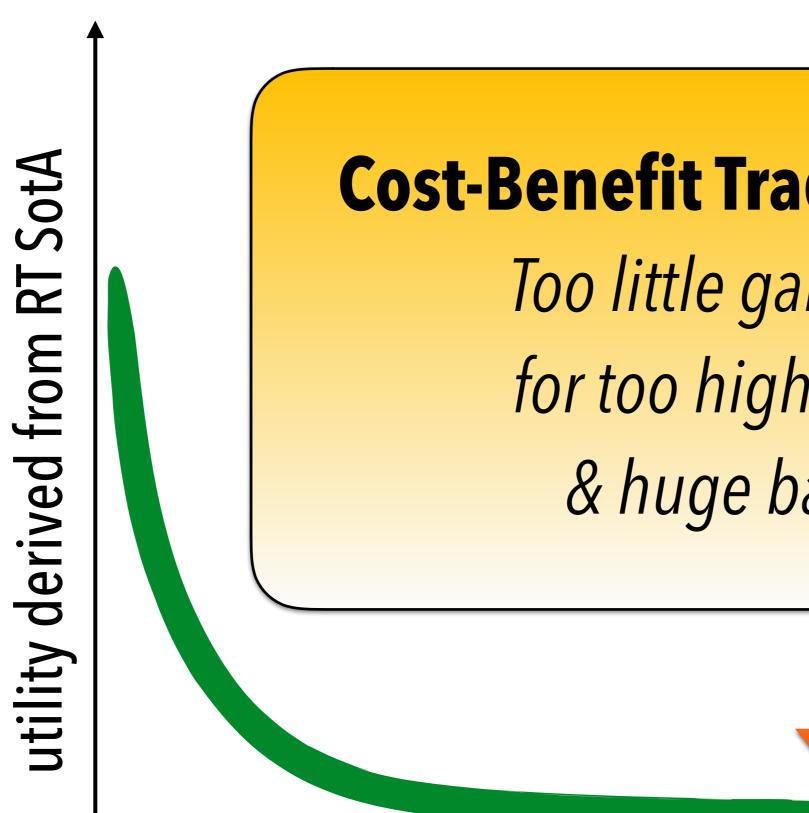
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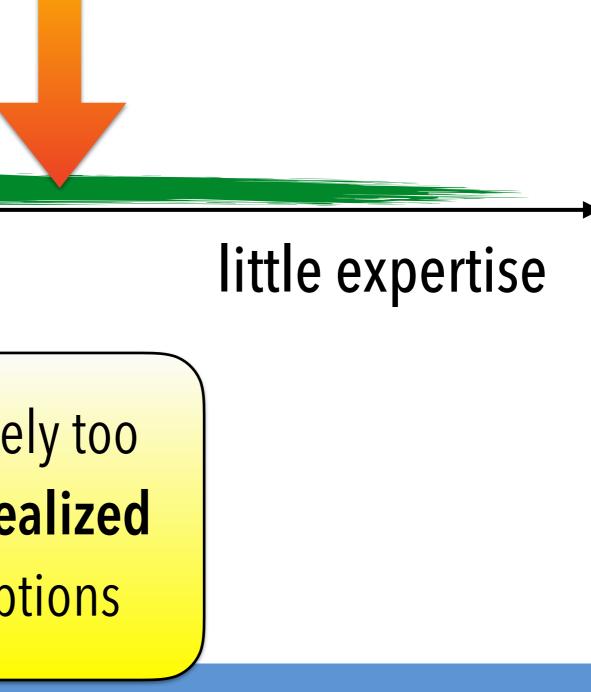
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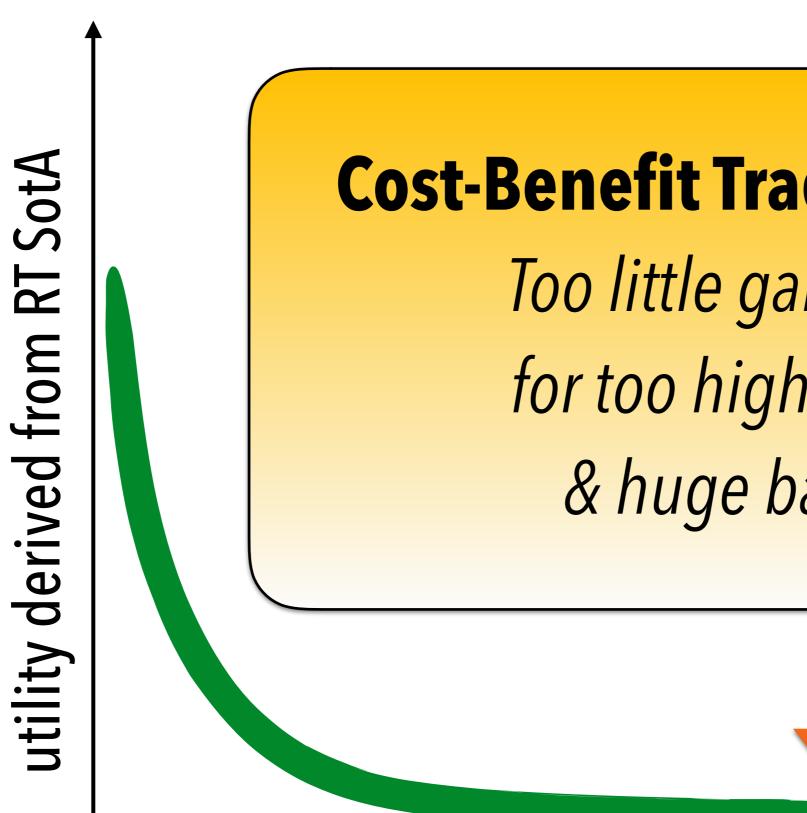
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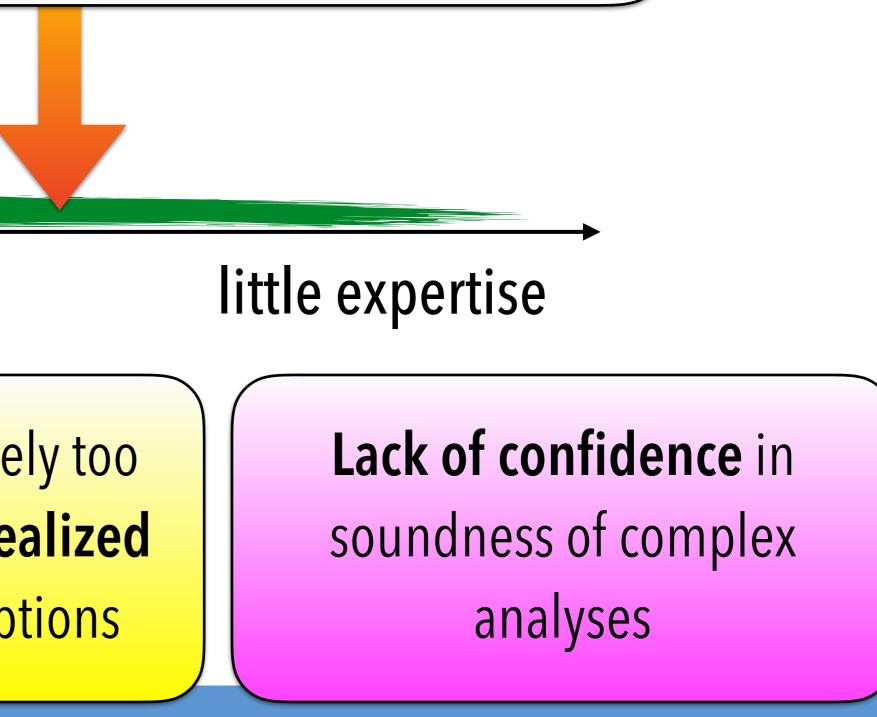
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Theory-Oriented Real-time Operating System

A radically different, practical foundation for temporally sound cyber-physical systems





European Research Council Established by the European Commission

Theory-Oriented Real-time Operating System

provably free of timing errors (with high confidence)

A radically different, practical foundation for temporally sound cyber-physical systems

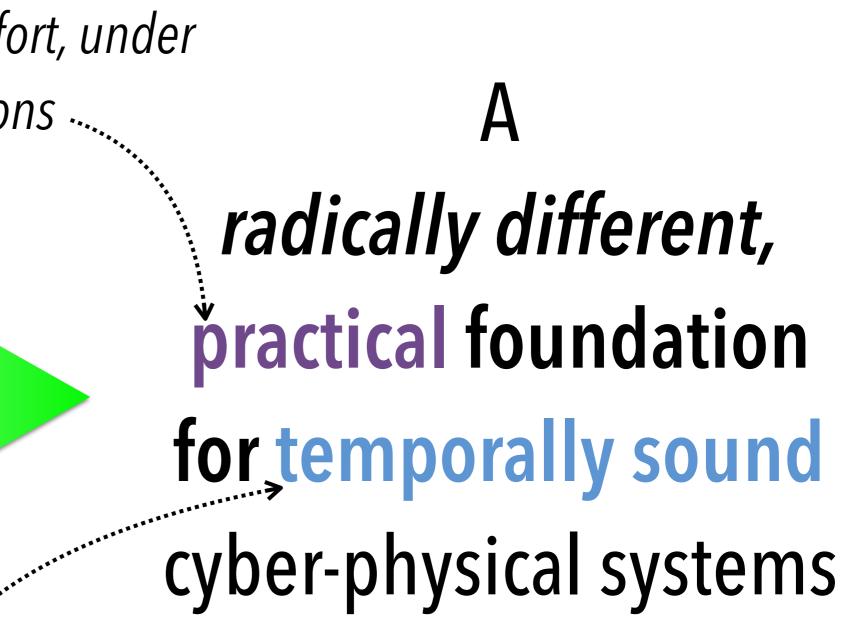




European Research Council Established by the European Commission with **affordable** effort, under **realistic** assumptions

Theory-Oriented Real-time Operating System

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European Research Council Established by the European Commission *theory-first approach*: intersection of multiprocessor real-time scheduling theory and RTOS design

with **affordable** effort, under **realistic** assumptions

Theory-Oriented Real-time Operating System

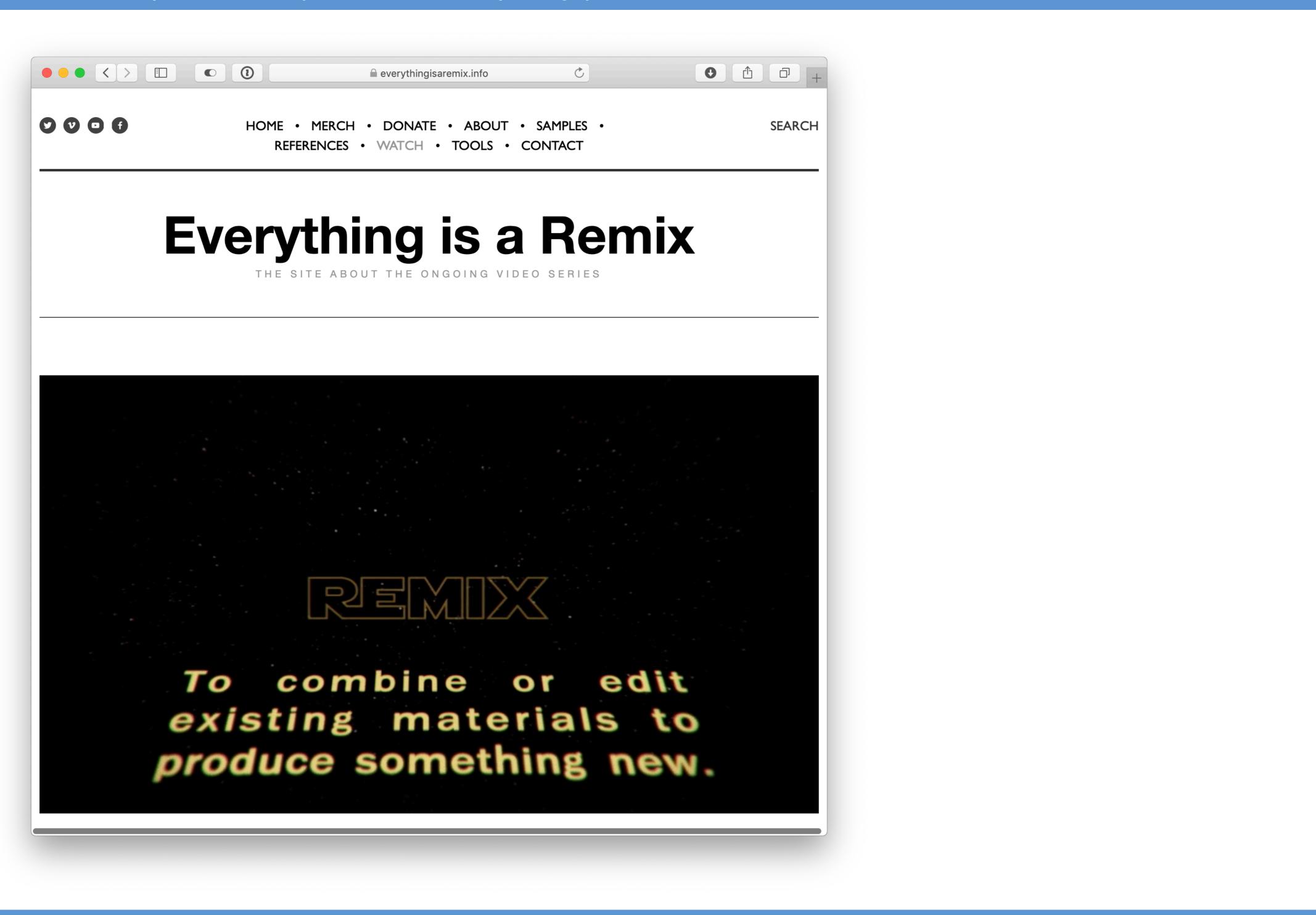
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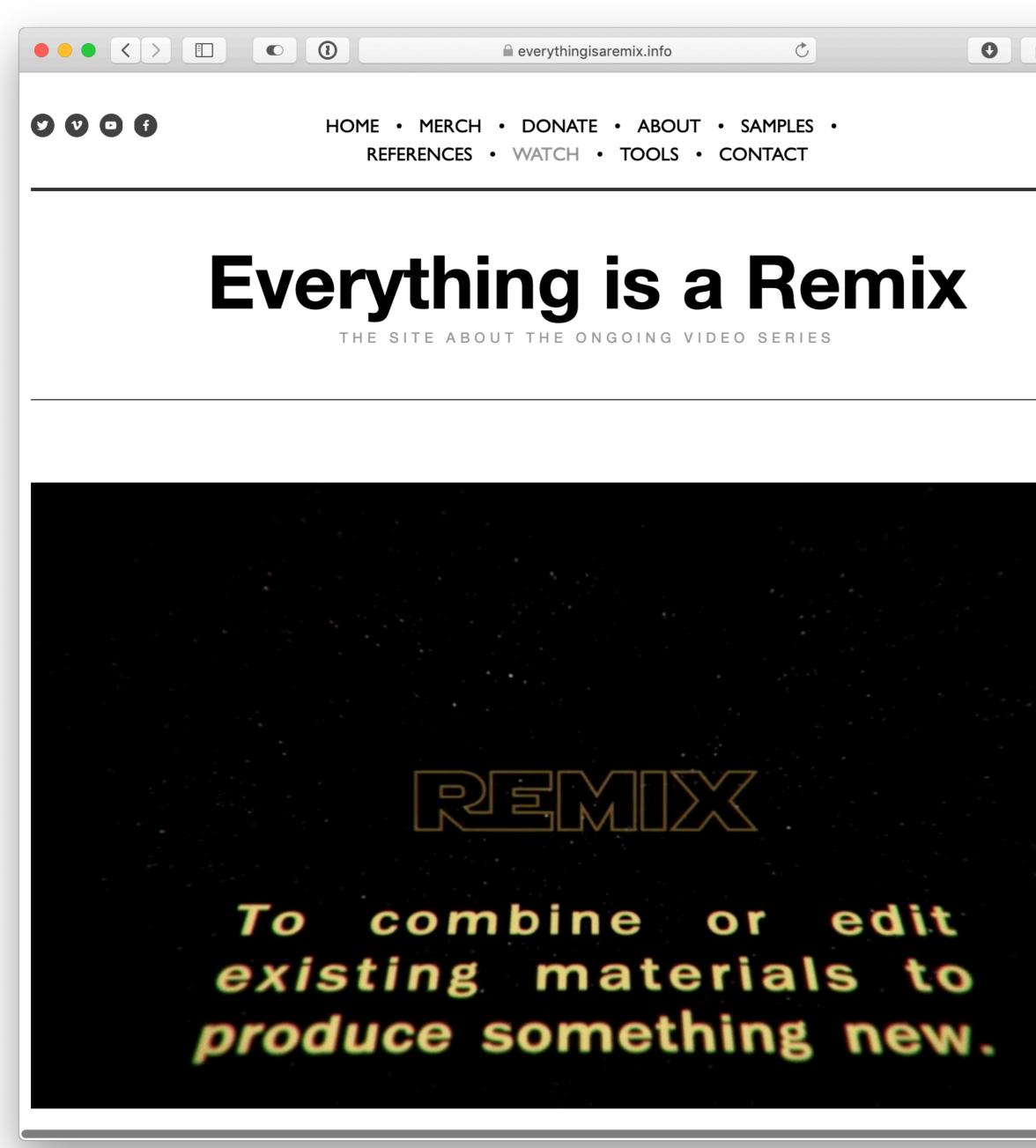


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Five Decades of RT Literature → A rich foundation!

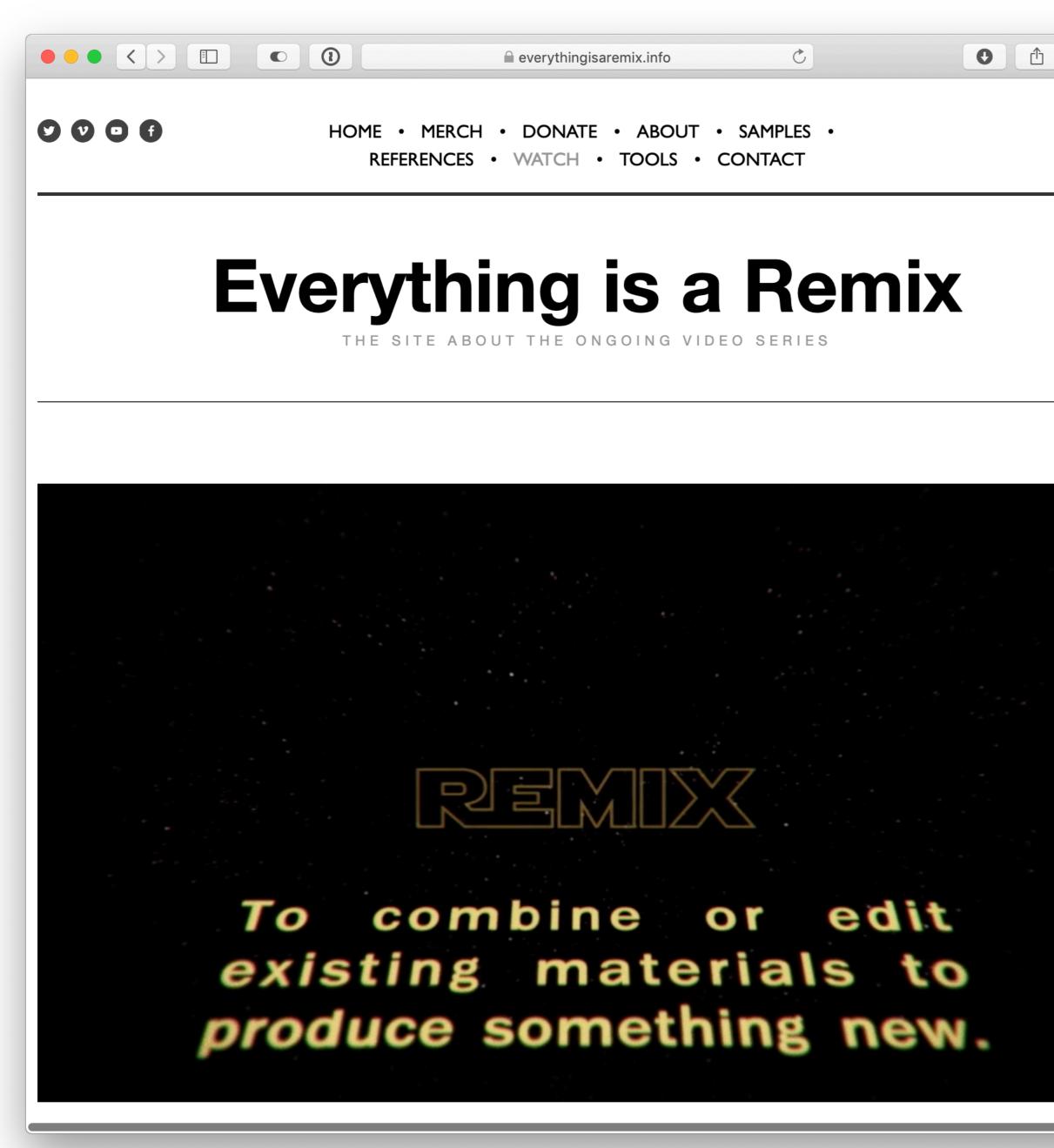


SEARCH

Five Decades of RT Literature → A rich foundation!

No Need for Completely New Inventions

→ Many great techniques that work to choose from



) ⁰ +

SEARCH

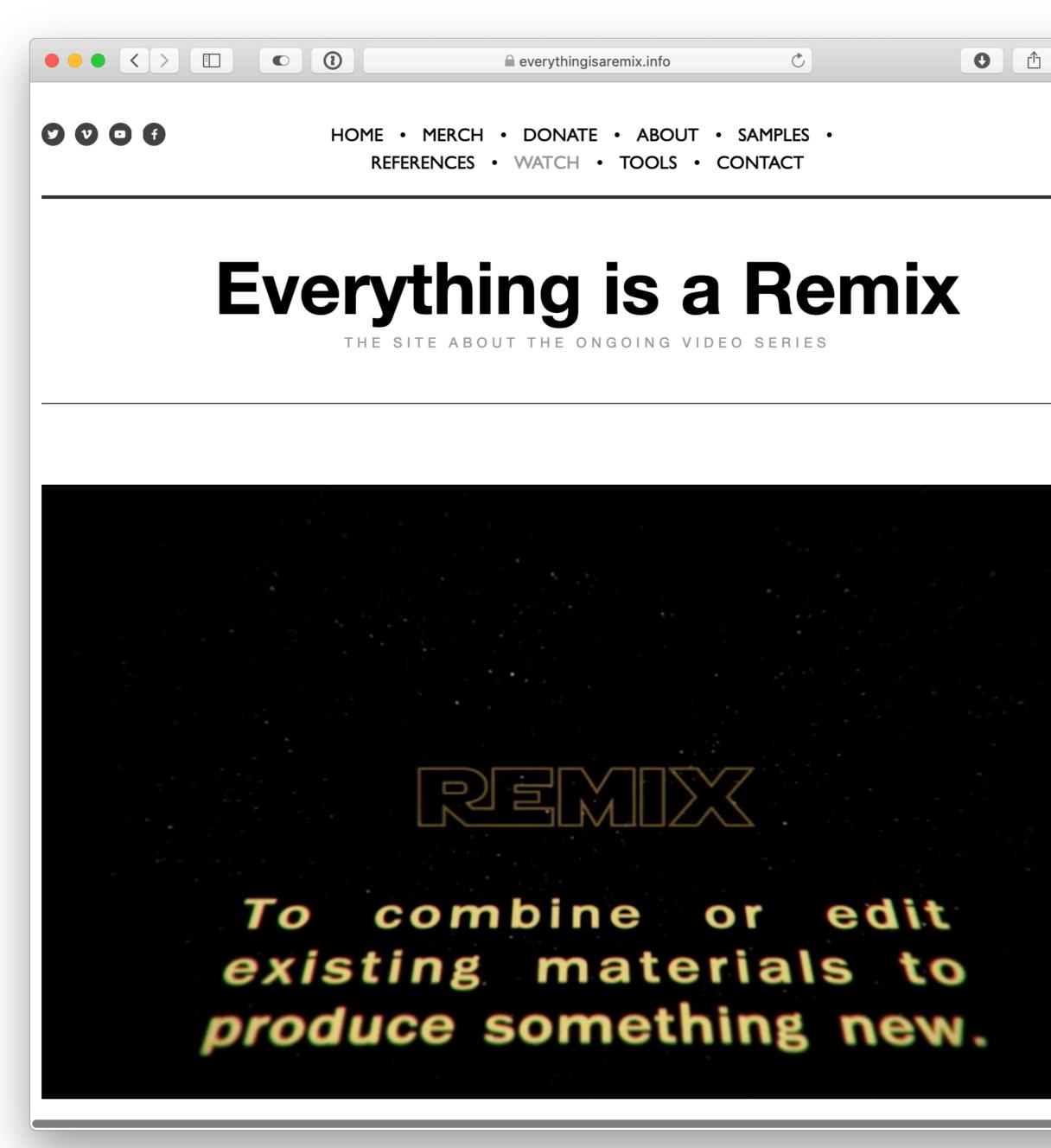
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No Need for Completely New Inventions

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The Challenge

→ Select and combine just the right ideas in just the right way, and remove the rest



SEARCH

D

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The Promise

→ More than the sum of its parts

THE FIVE TOROS PRINCIPLES

theory-oriented RTOS design: all provided abstractions must be (1)**temporally sound** (\rightarrow any composition is analyzable)

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- (1) theory-oriented RTOS design: all provided abstractions must be temporally sound (→ any composition is analyzable)
- (2) **declarative OS abstractions**: automatically checkable timing and resourceallocation **goals** (→ *domain experts do not need to understand scheduling*)
- (3) **temporal reflection**: *transparently* & *continuously* self-assess temporal correctness and **proactively adapt** when guarantees can no longer be given

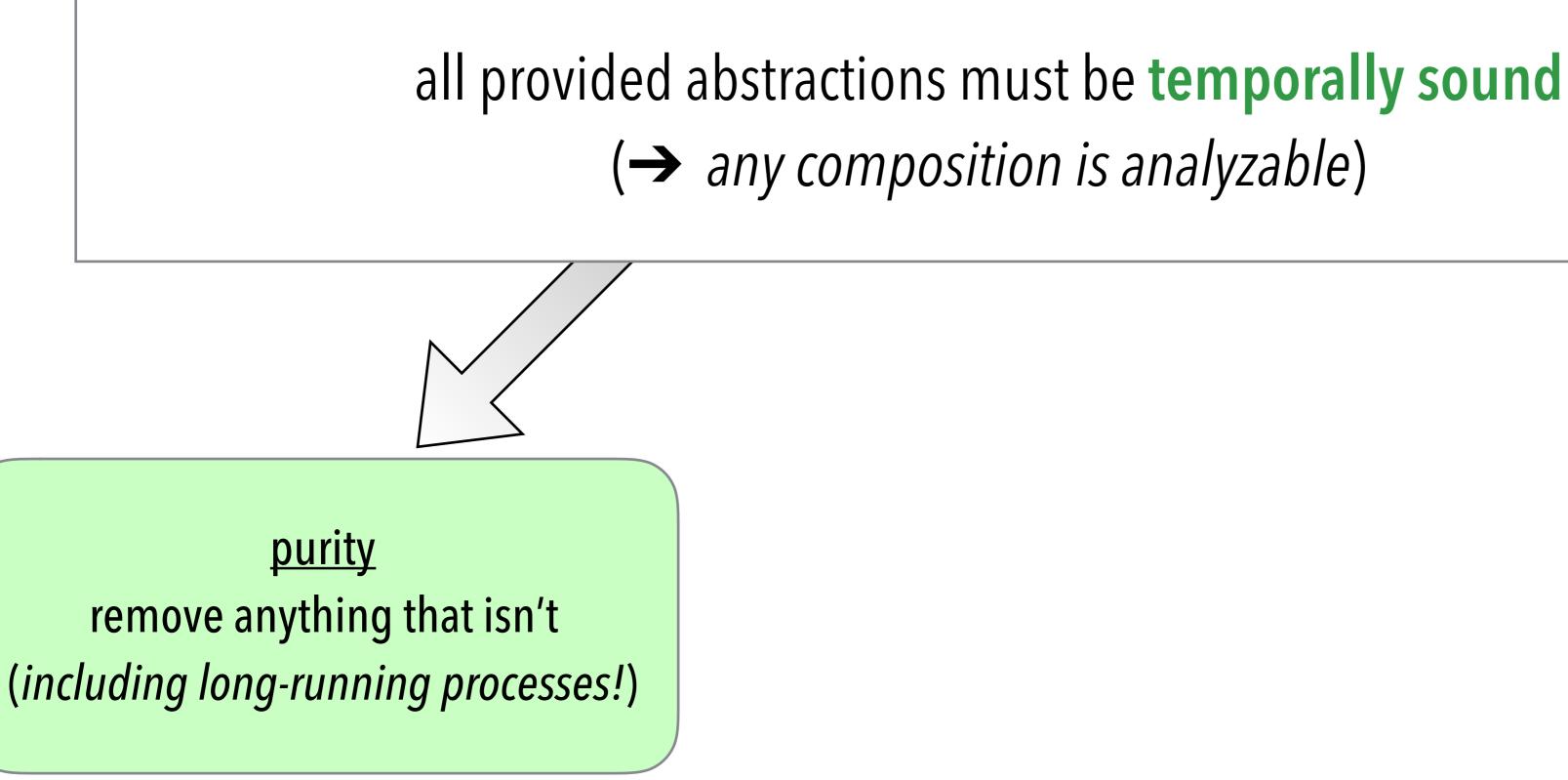
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- (4) **structured uncertainty management**: provide first-class, sound abstractions to manage uncertainty due to **below-worst-case provisioning**
- (5) trustworthy analysis: verify analysis soundness with machine-checked proofs using the Coq proof assistant [not discussed today]

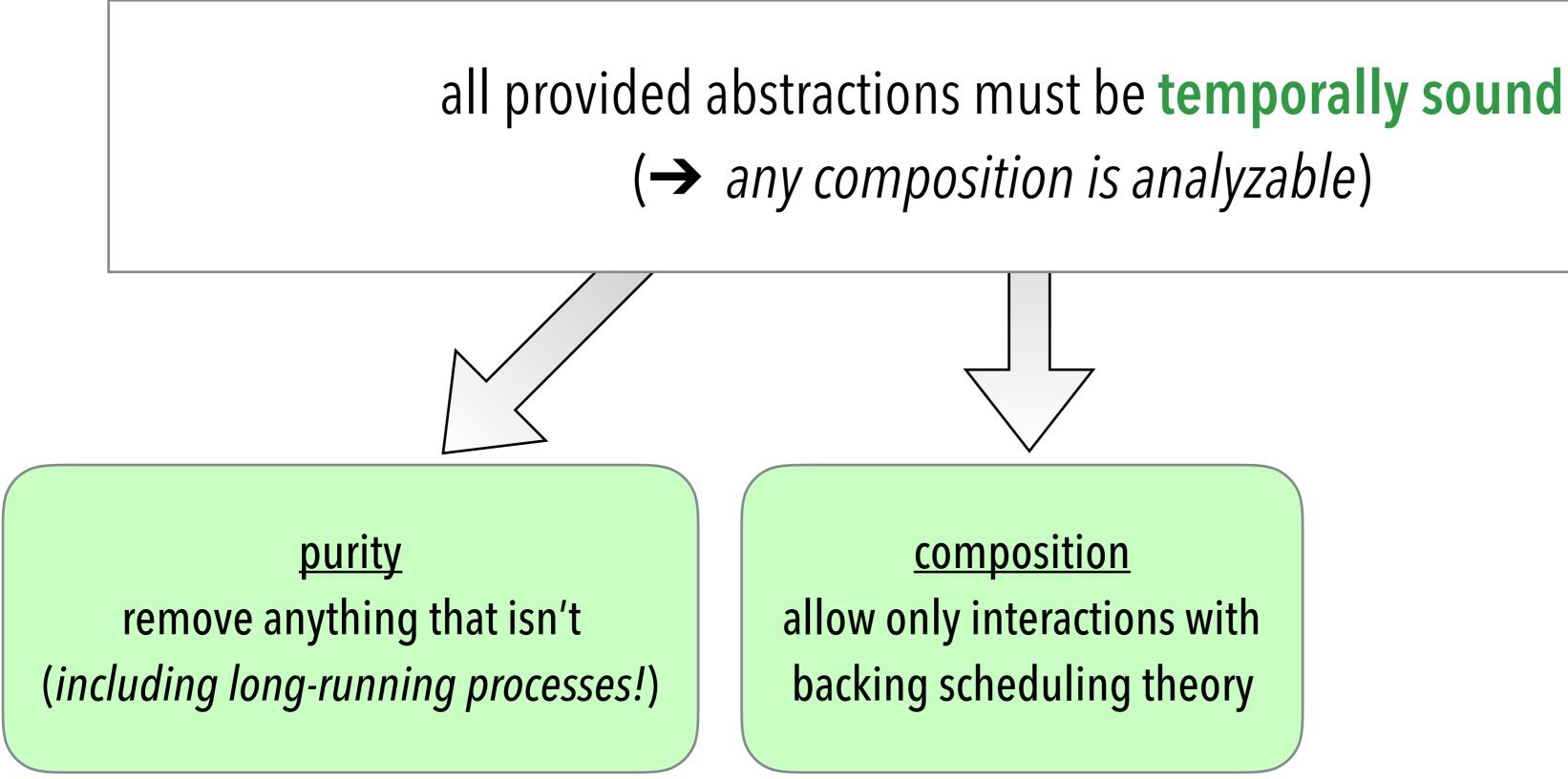
(1) THEORY-ORIENTED OS DESIGN

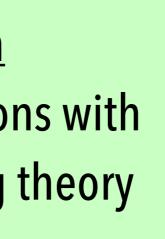
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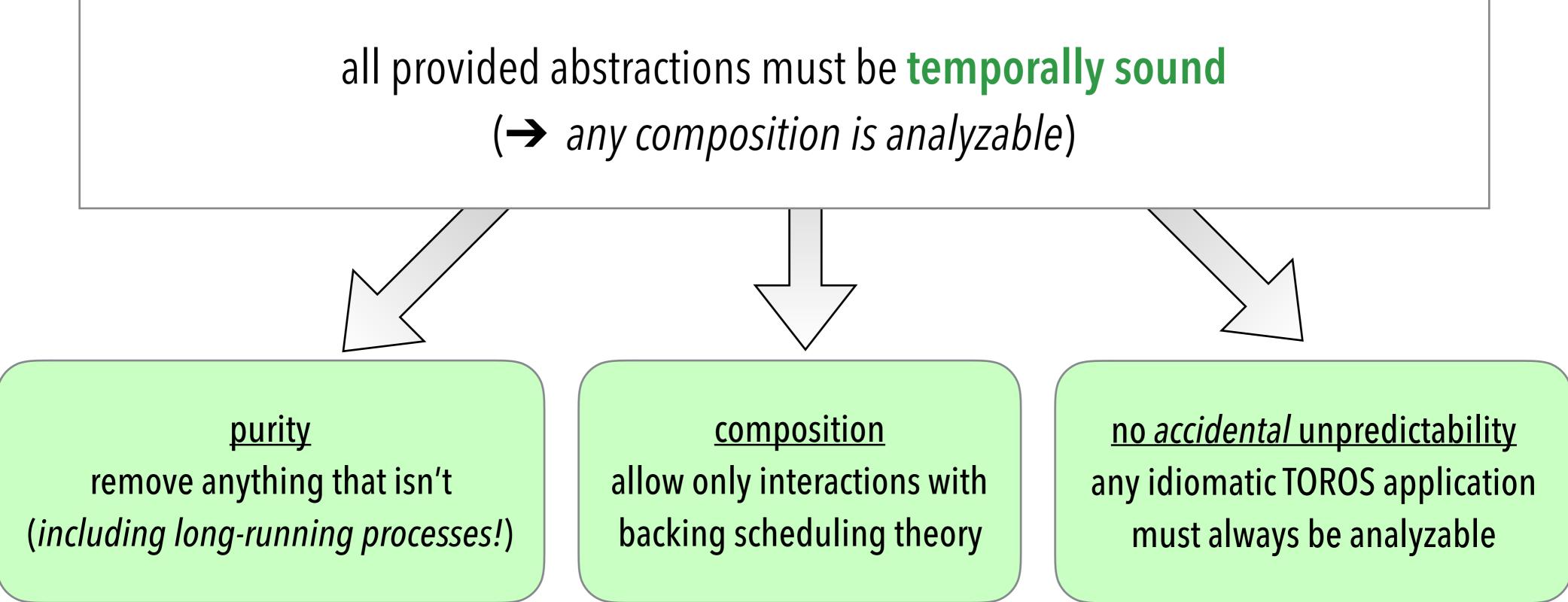


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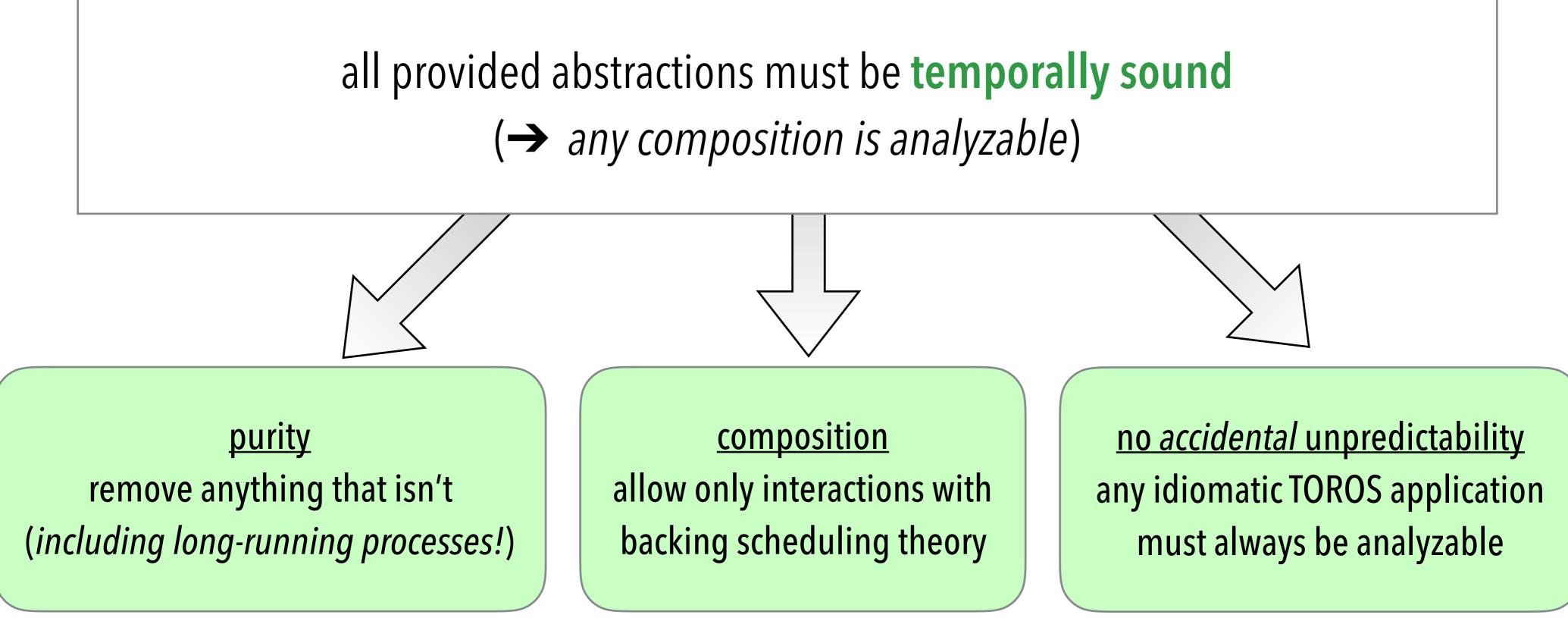




(1) THEORY-ORIENTED OS DESIGN



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Challenges

- → Can one still build practical applications with reasonable effort on top of such a minimal, unconventional foundation?
- \rightarrow Massive engineering effort to get the system off the ground...

(2) DECLARATIVE, HIGH-LEVEL OS ABSTRACTIONS

automatically checkable timing and resource-allocation **goals** (→ *domain experts do not need to understand real-time theory*)

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MPI-SWS

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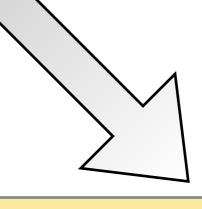
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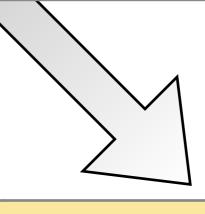
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Challenges

- → Need one-size-fits-all scheduling and synchronization policies
- → Automatically map specified goals to efficient parameter choices



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hronization policies ent parameter choices

BASIC ABSTRACTIONS IN TOROS



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temporal isolation

<u>Guaranteed Processor</u> <u>Share (GPS)</u>

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 text + heap + bss +
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- \rightarrow passive = not necessarily inherited by a thread
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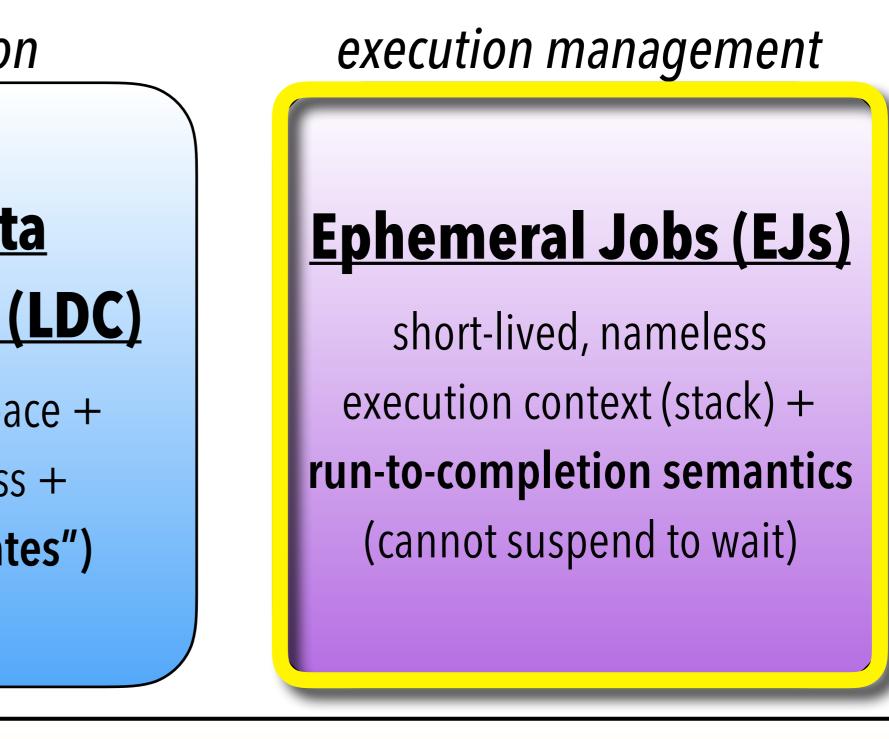
Ephemeral Jobs (EJs)

- \rightarrow like callbacks or event handlers in flight
- \rightarrow always start at some entry point
- \rightarrow cannot wait
- \rightarrow cannot be referenced

BASIC ABSTRACTIONS IN TOROS

temporal isolation spatial isolation Logic & Data **Guaranteed Processor Compartment (LDC) Share (GPS)** passive address space + Fraction of a core (%) text + heap + bss + max. scheduling latency entry points ("gates")

Only two ways for EJs to interact asynchronous_invoke(LDC::entry_point, GPS) → fork synchronous_invoke(LDC::entry_point, GPS) → call-return semantics



EXECUTION & PROGRAMMING MODEL

Hardware





EXECUTION & PROGRAMMING MODEL

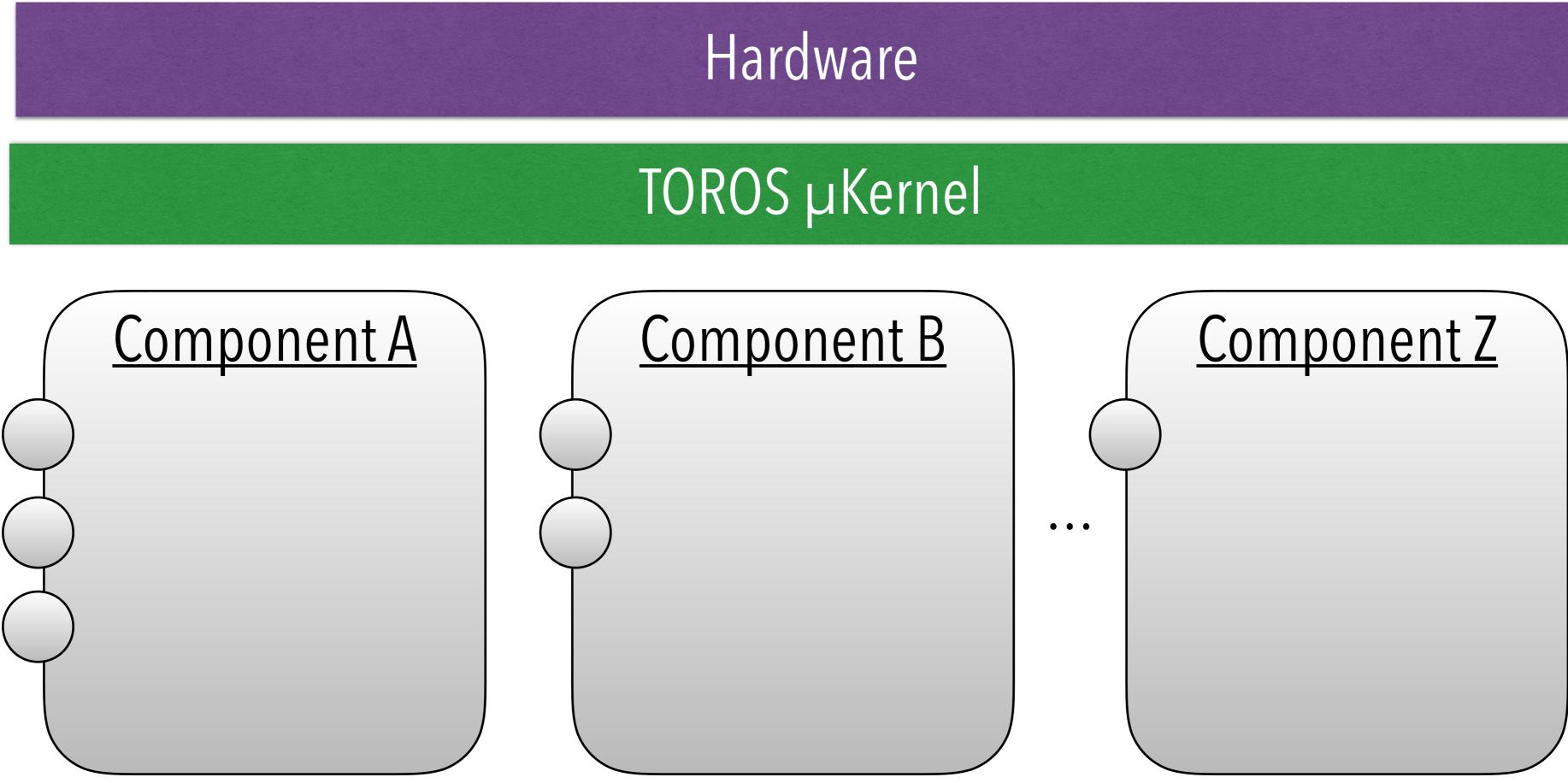




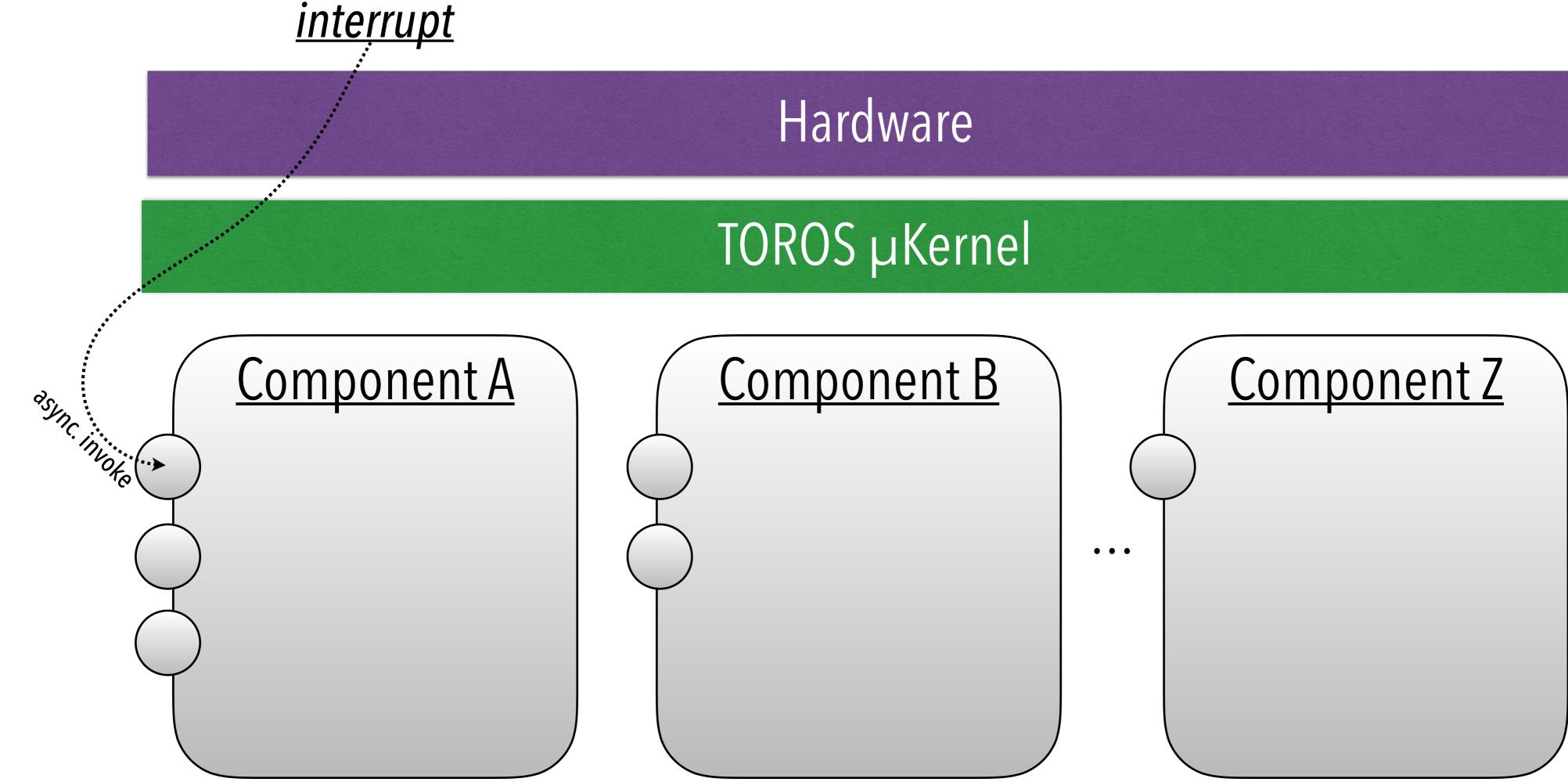


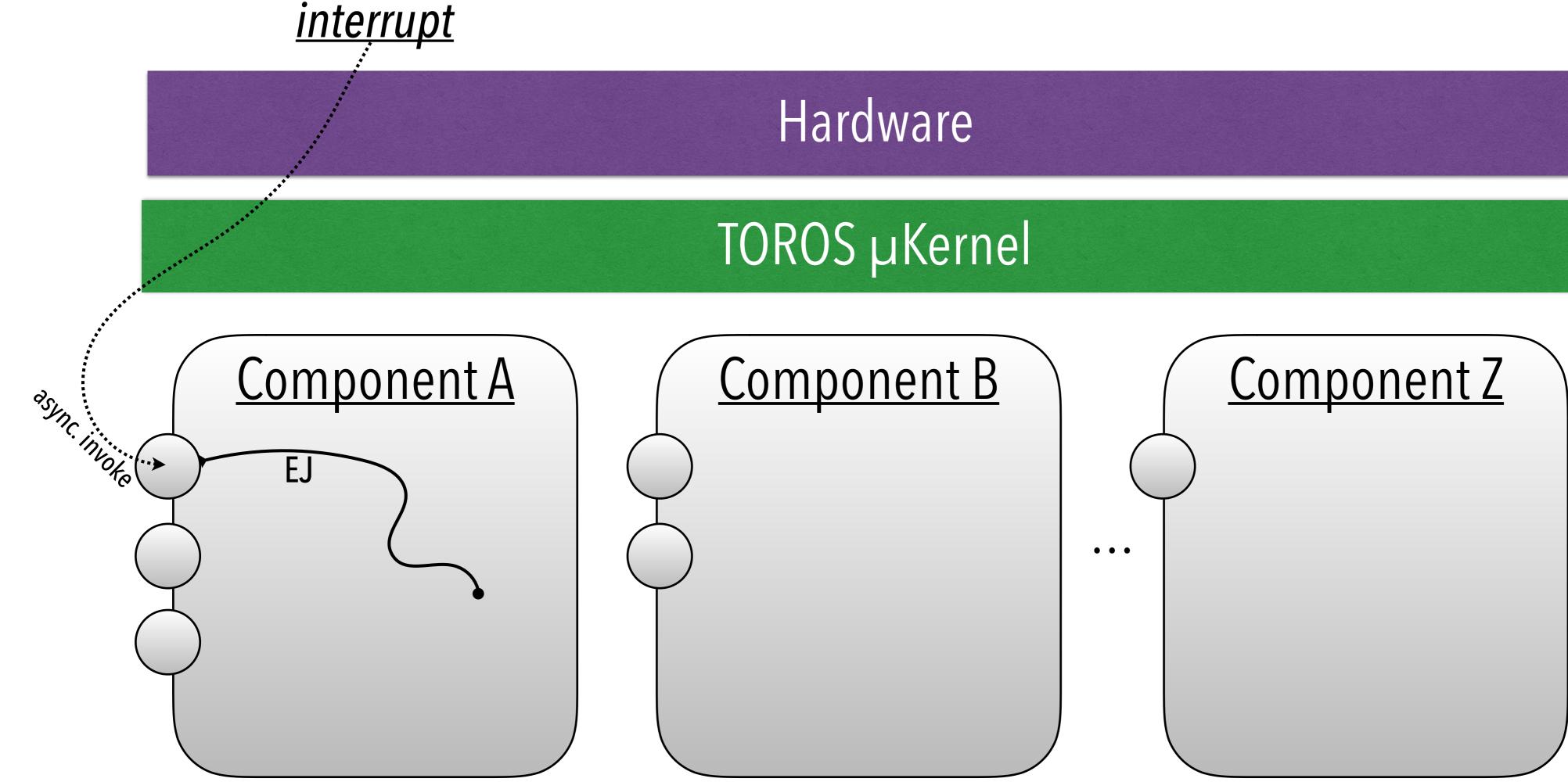
B. Brandenburg

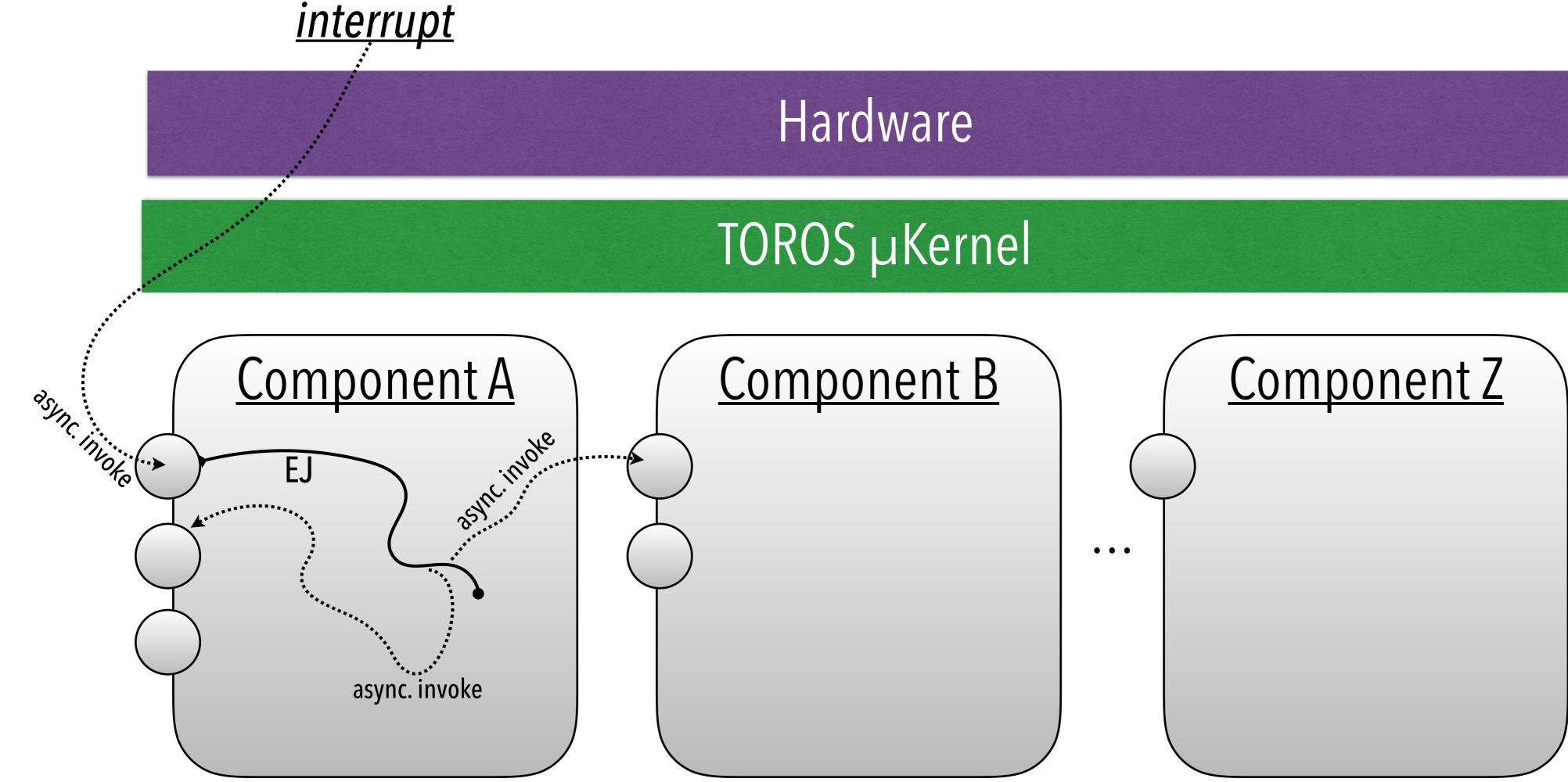
EXECUTION & PROGRAMMING MODEL

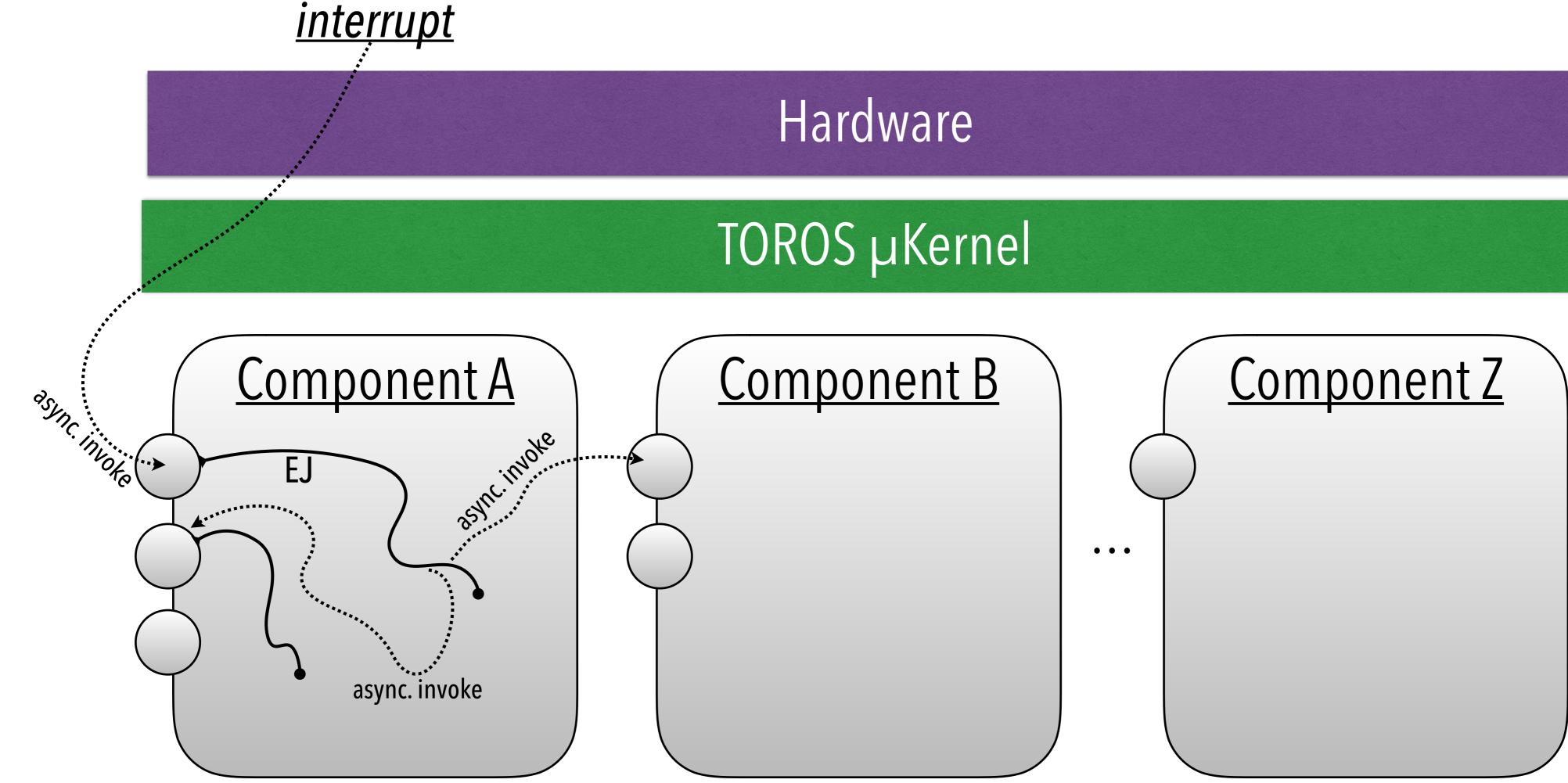


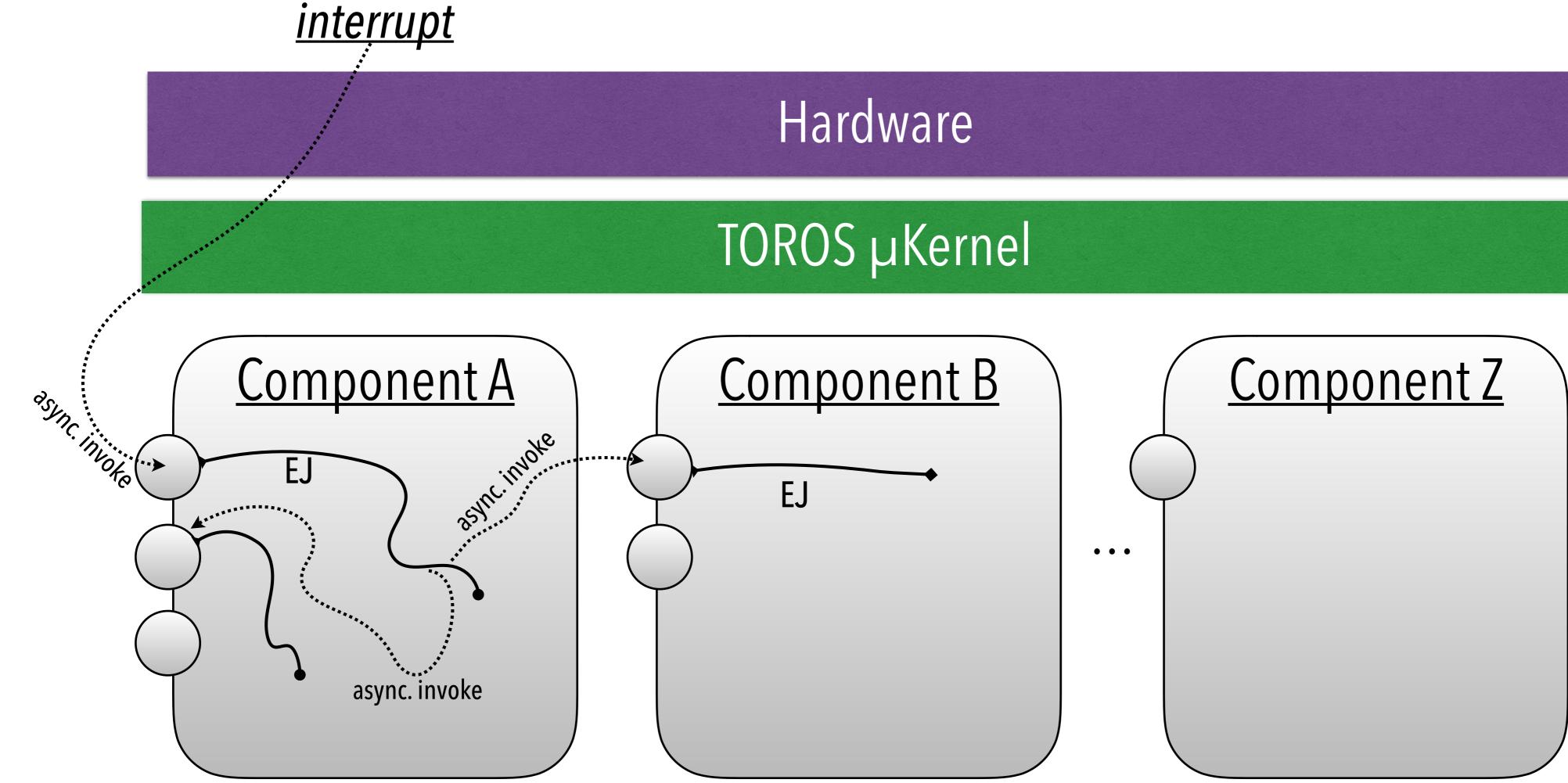
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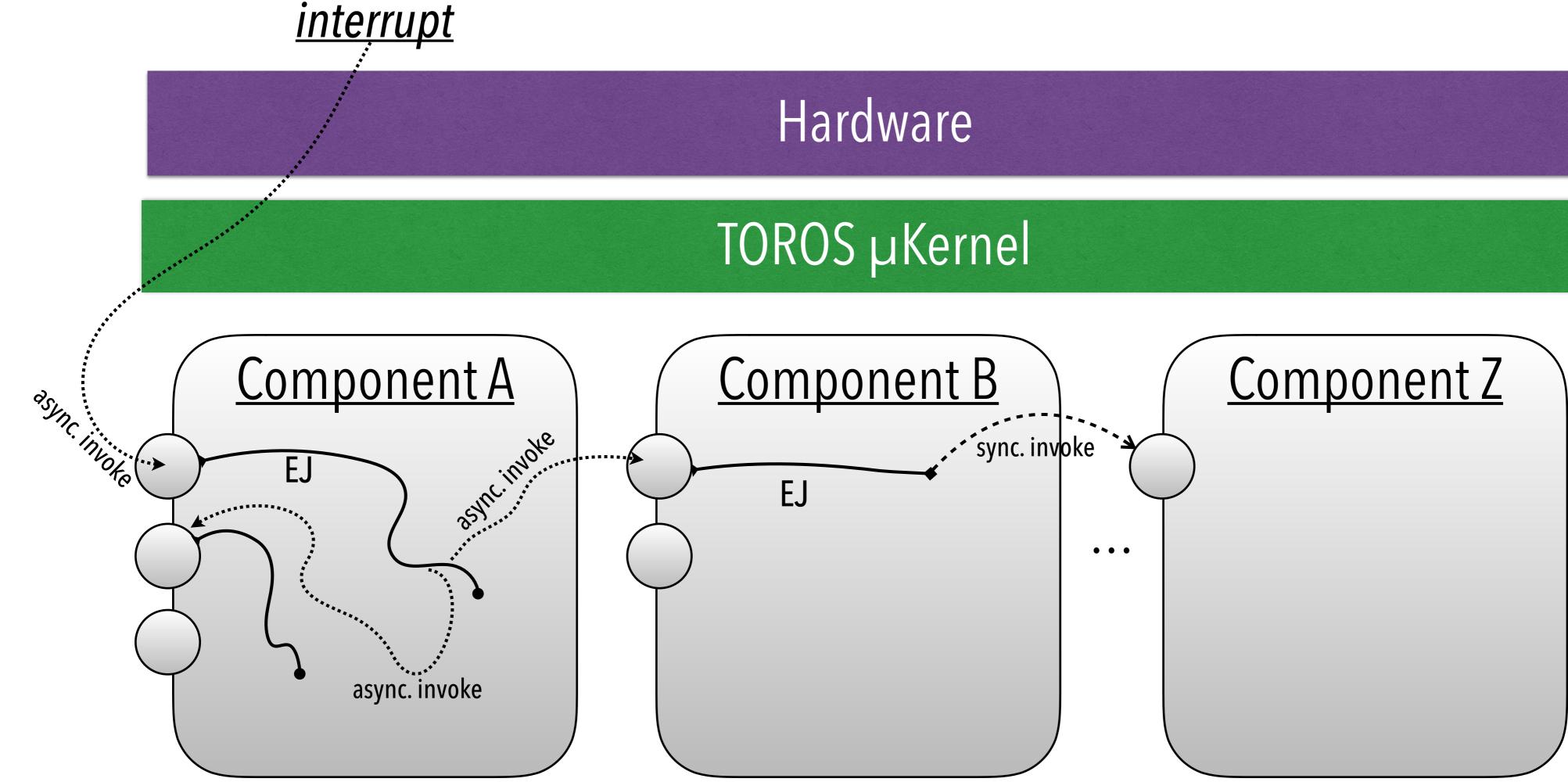


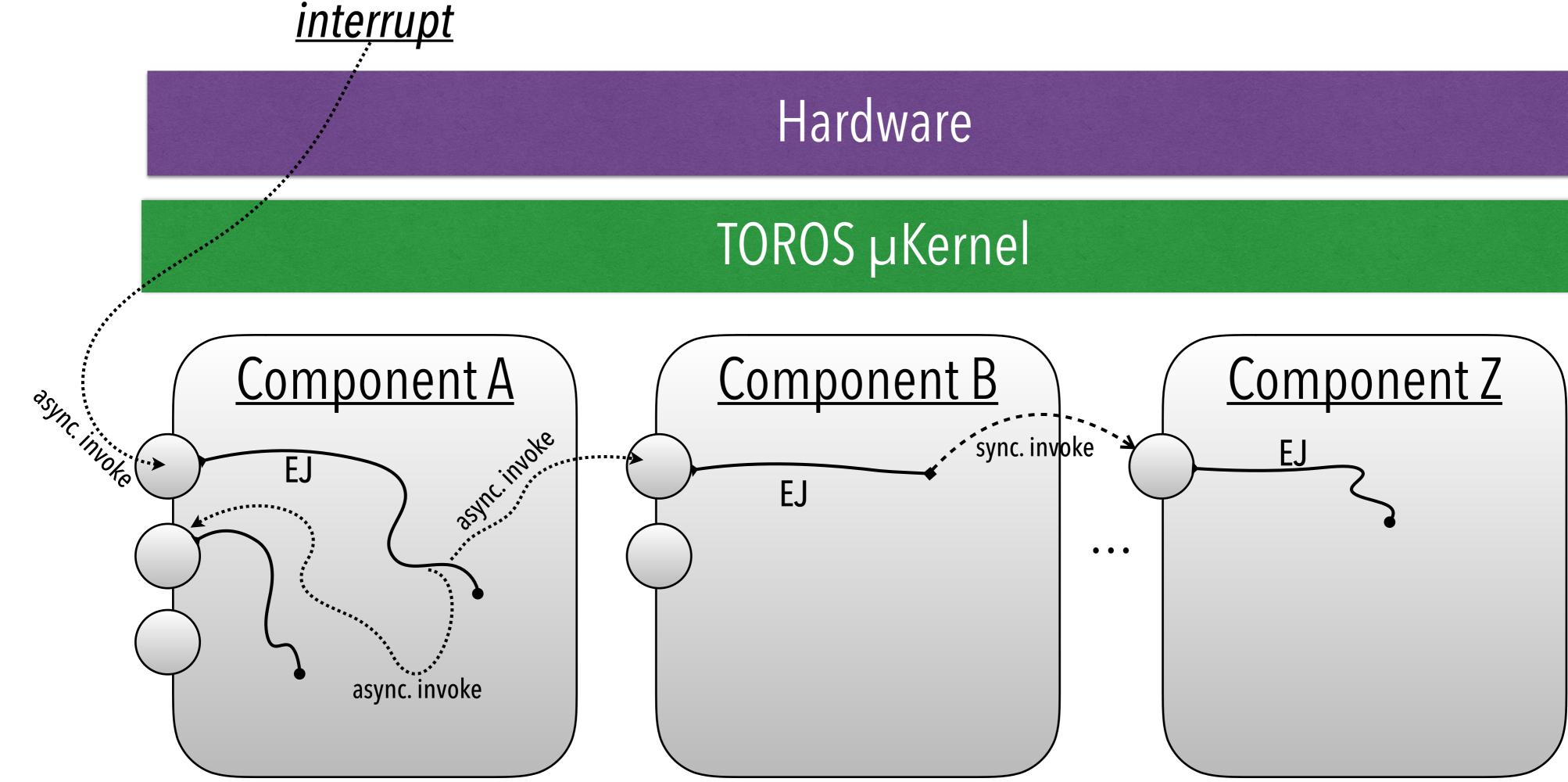


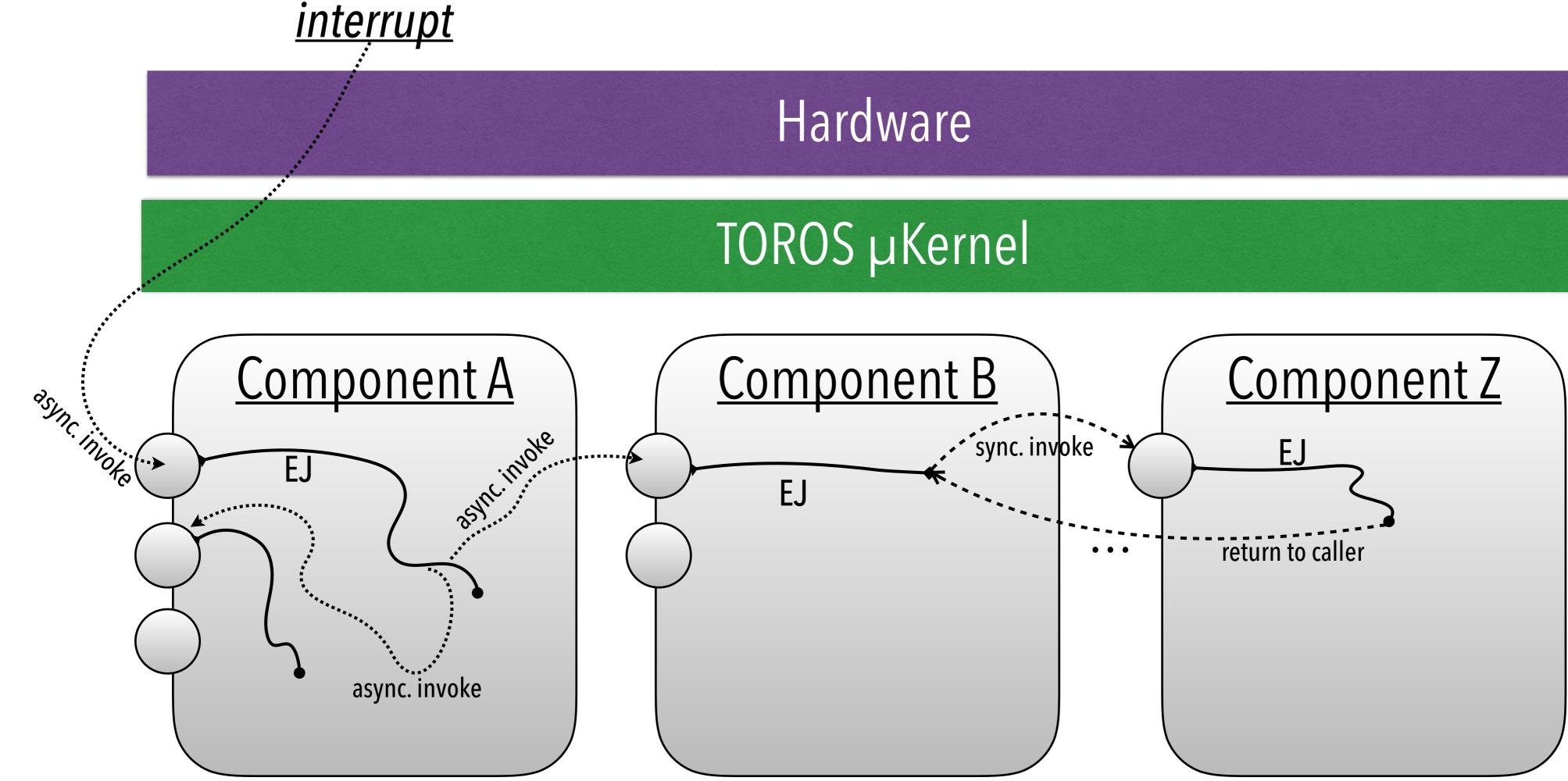


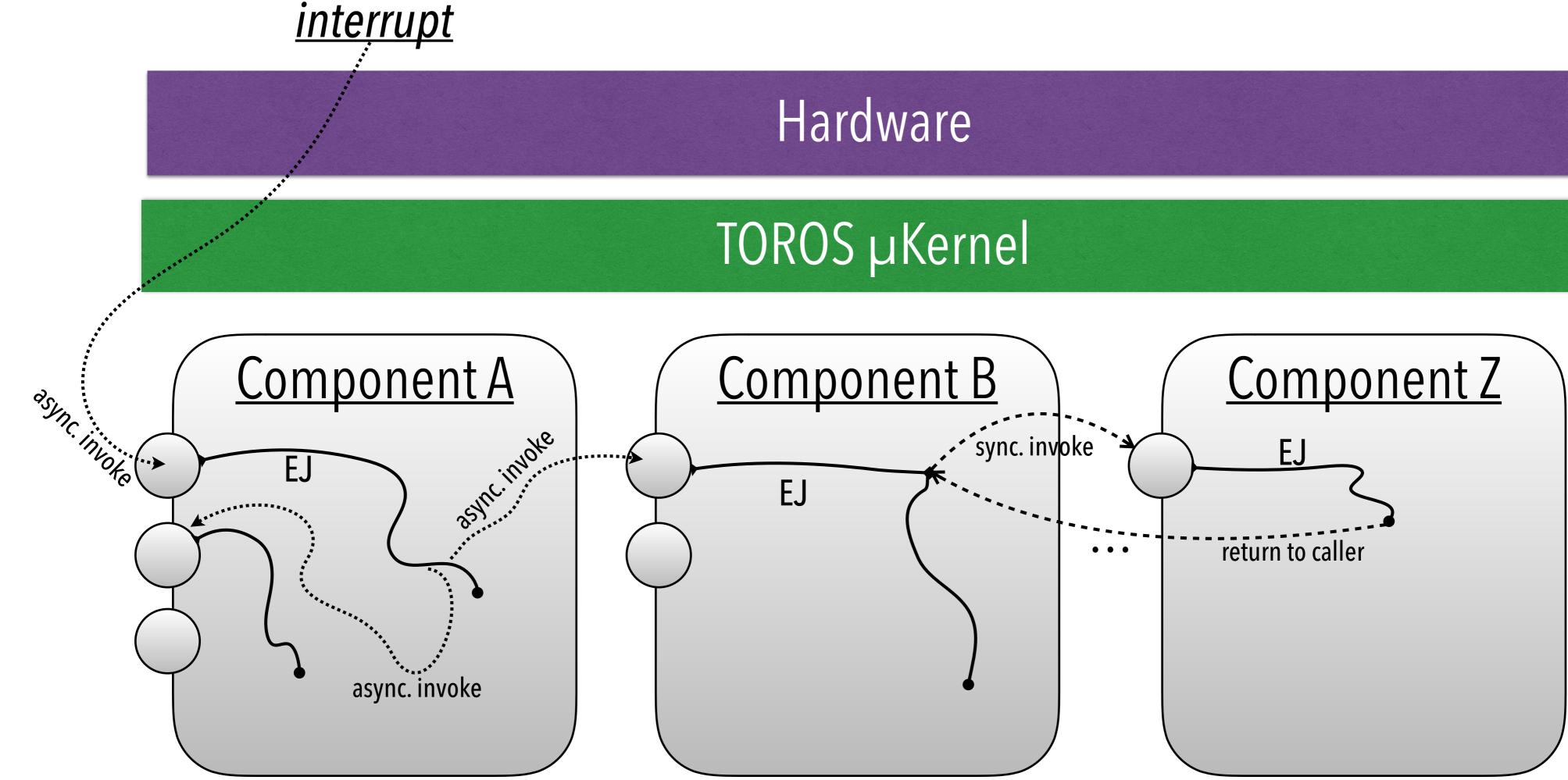




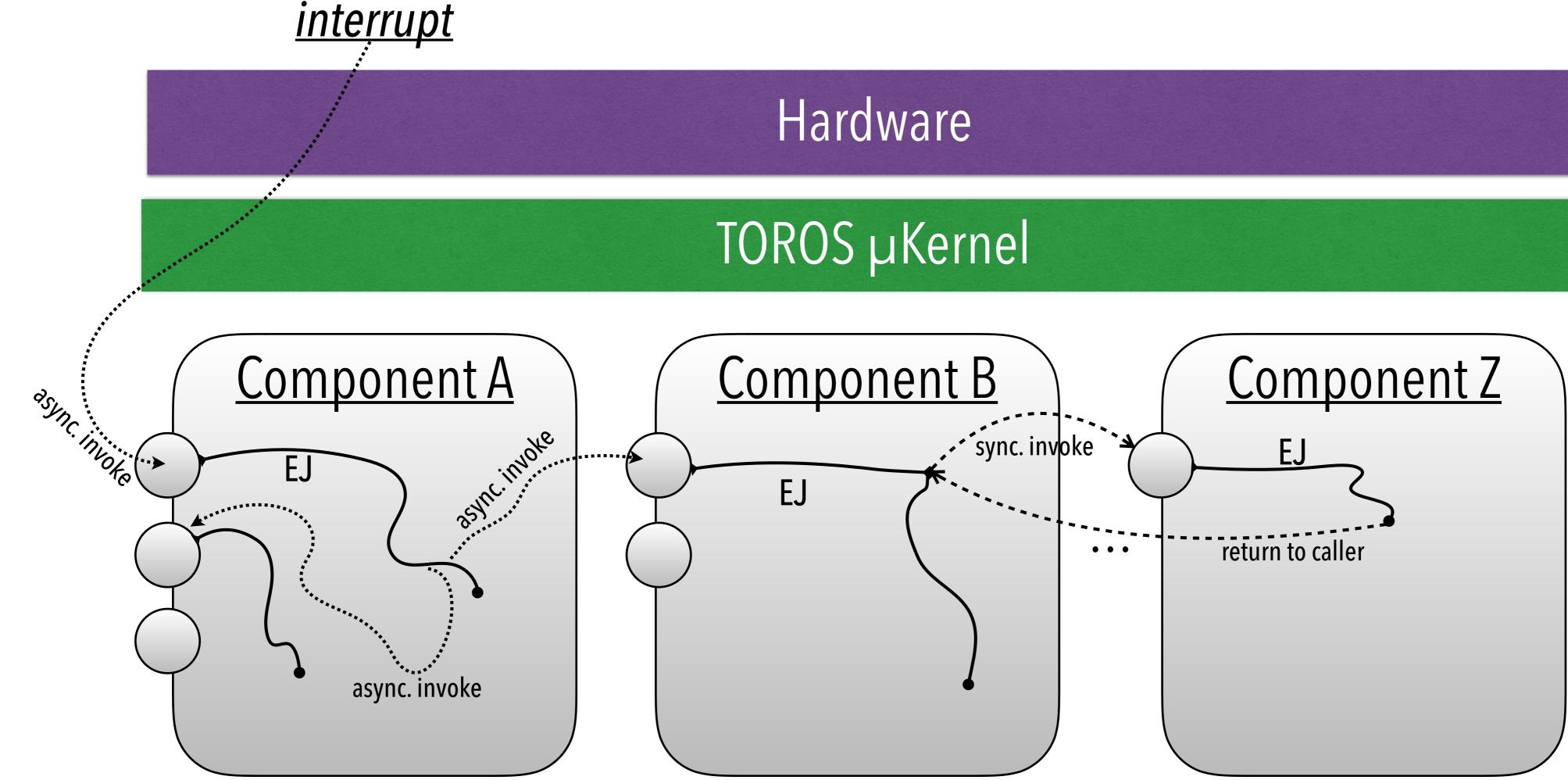








EXECUTION & PROGRAMMING MODEL



event-driven / continuation-based / actor-like programming model

policy freedom

VS

opinionated design

freedom from choice

policy freedom

Aim for maximal flexibility: under no circumstance hardcode any policy into the OS.

The application developer knows best.

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L4 & Composite

Linux, RTEMS, FreeRTOS...

VS

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(3) TEMPORAL REFLECTION

transparently & continuously self-assess temporal correctness and proactively adapt when guarantees can no longer be given

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Approach

- → always-on lightweight tracing of entire system
- → trigger incremental re-analysis whenever inputs to analysis change
- → invoke application-provided adaptation handler if timing goals cannot be guaranteed

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- tracing runtime overheads
- tracing space overheads
- analysis runtime

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an B

cloud offloading of analysis

transparently & continuously self-assess temporal correctness and proactively adapt when guarantees can no longer be given

Do not measure WCETs!

We can trace **percentiles** (< 100) and **(non-)correlations**

B. Brandenburg

with high & quantifiable confidence in bounded space.

Approach	Ch
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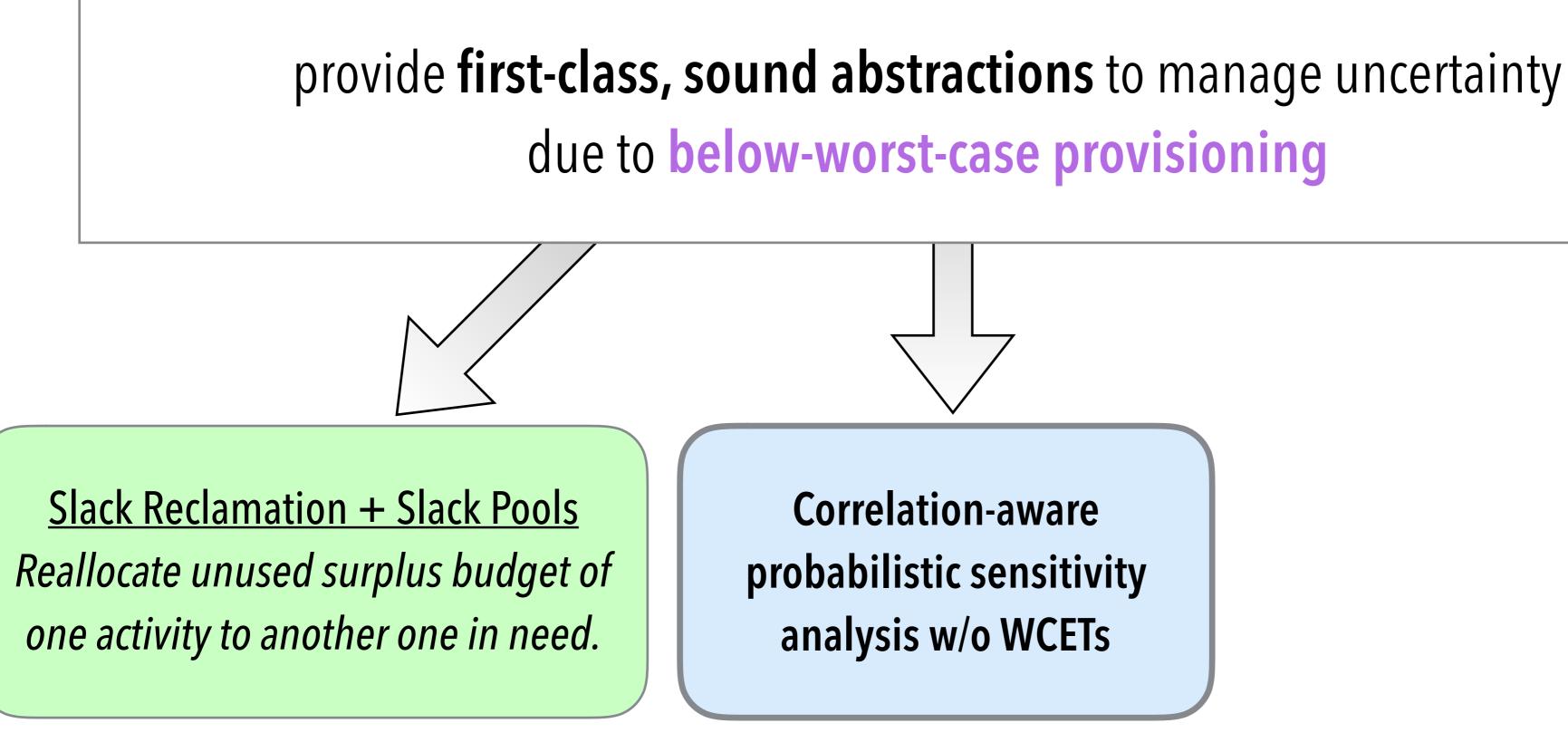
(4) STRUCTURED UNCERTAINTY MANAGEMENT

provide **first-class, sound abstractions** to manage uncertainty due to **below-worst-case provisioning**

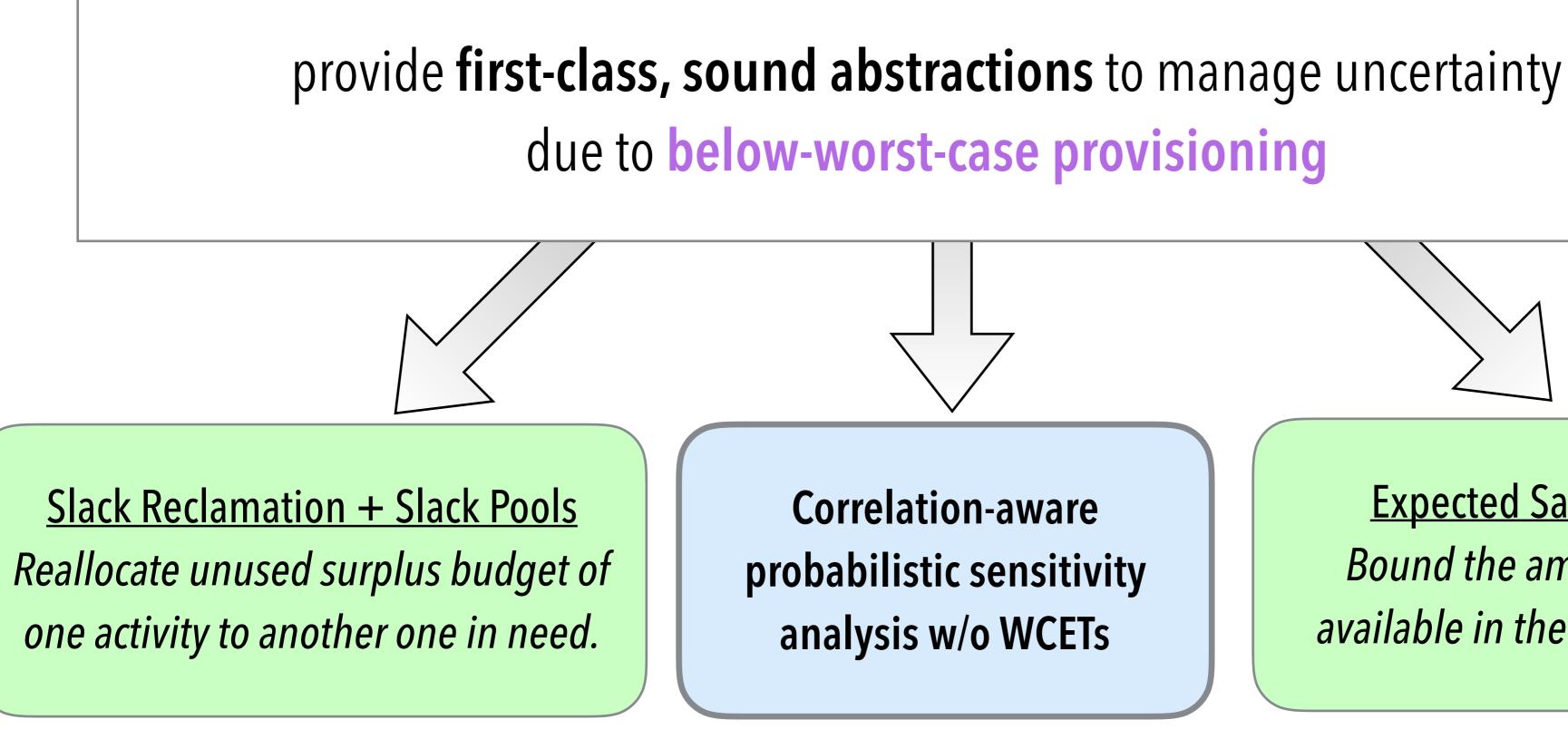
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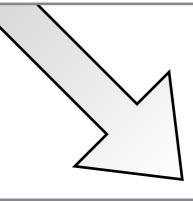
provide **first-class, sound abstractions** to manage uncertainty due to below-worst-case provisioning Slack Reclamation + Slack Pools *Reallocate unused surplus budget of* one activity to another one in need.

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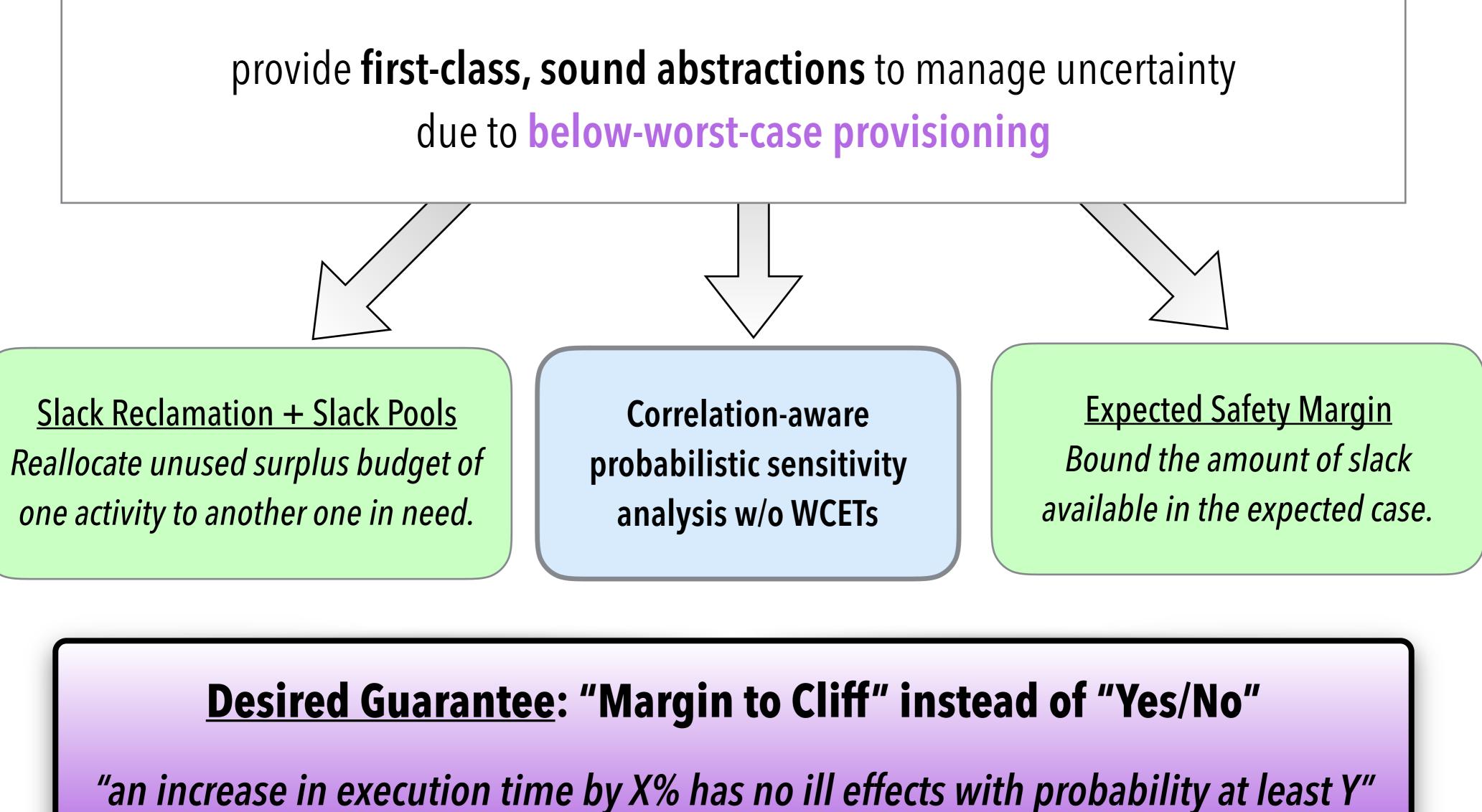
(4) STRUCTURED UNCERTAINTY MANAGEMENT





Expected Safety Margin Bound the amount of slack available in the expected case.

(4) STRUCTURED UNCERTAINTY MANAGEMENT



MPI-SWS

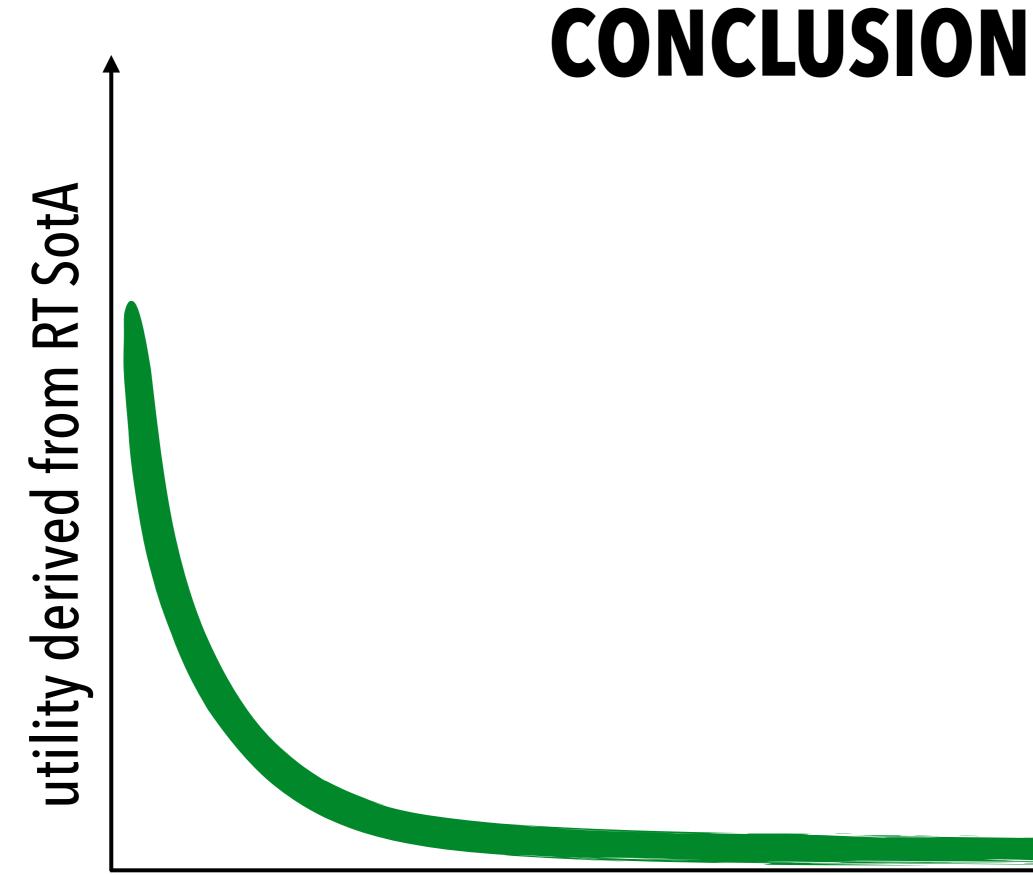
CONCLUSION





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much expertise

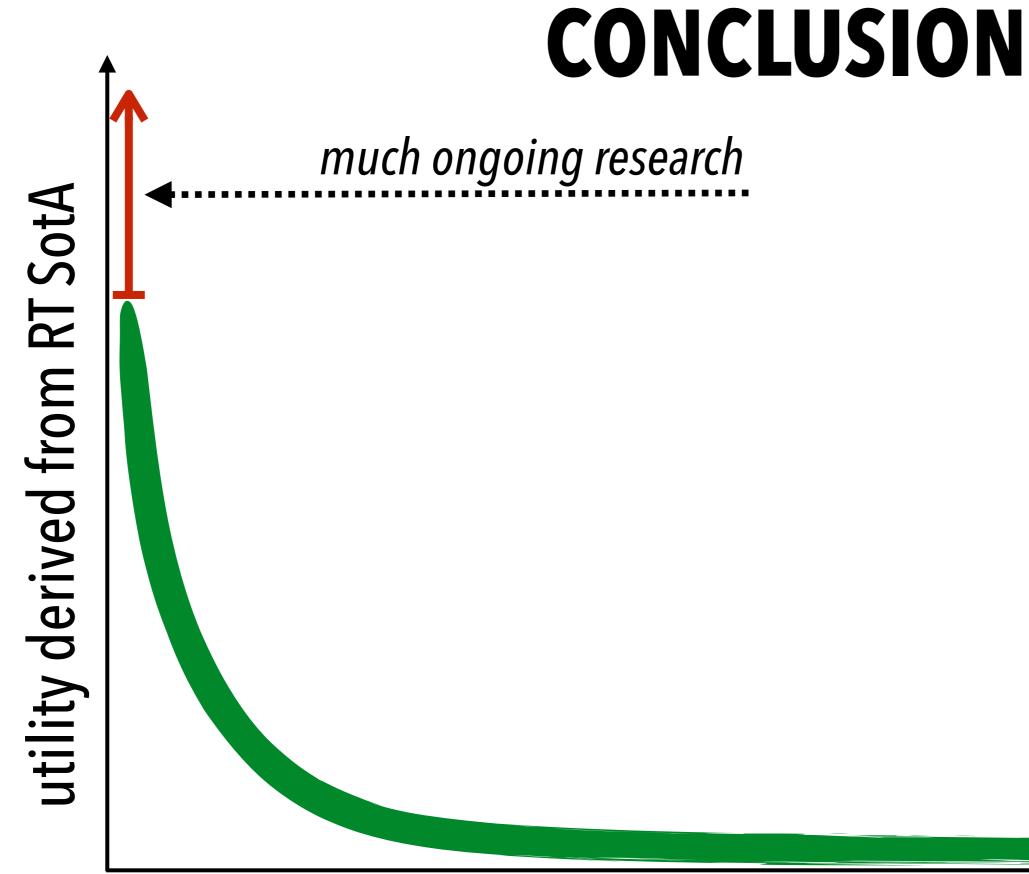
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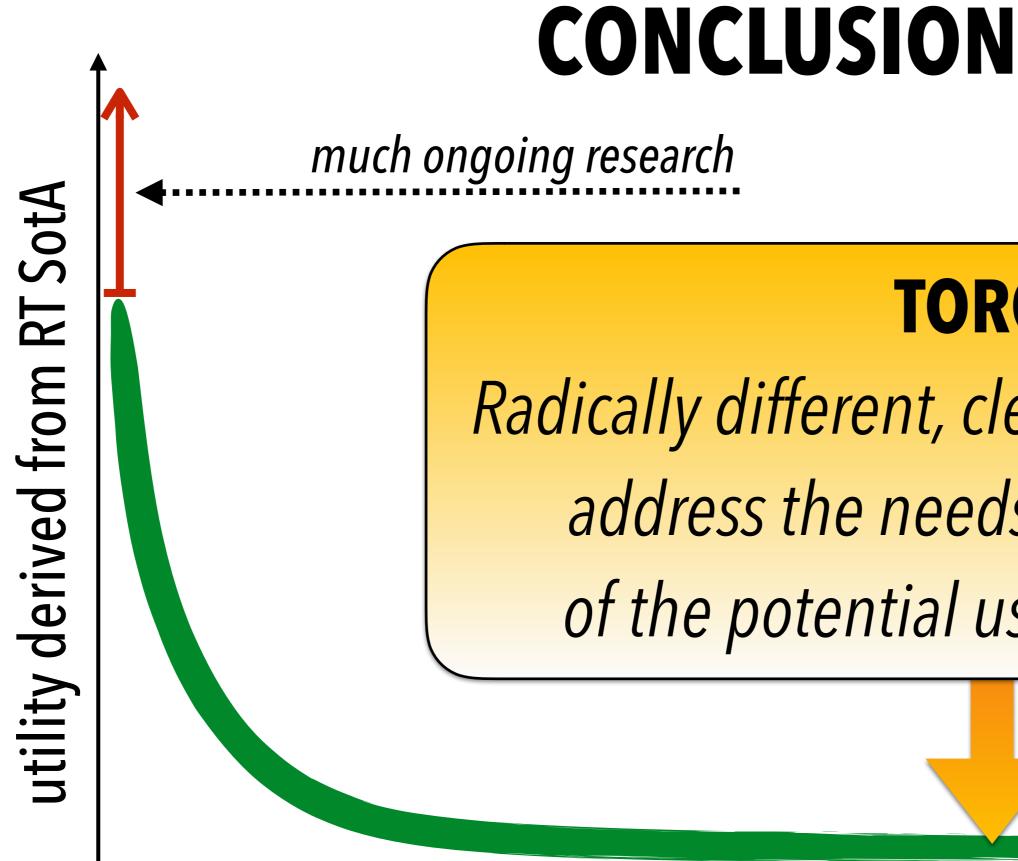
little expertise





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TOROS

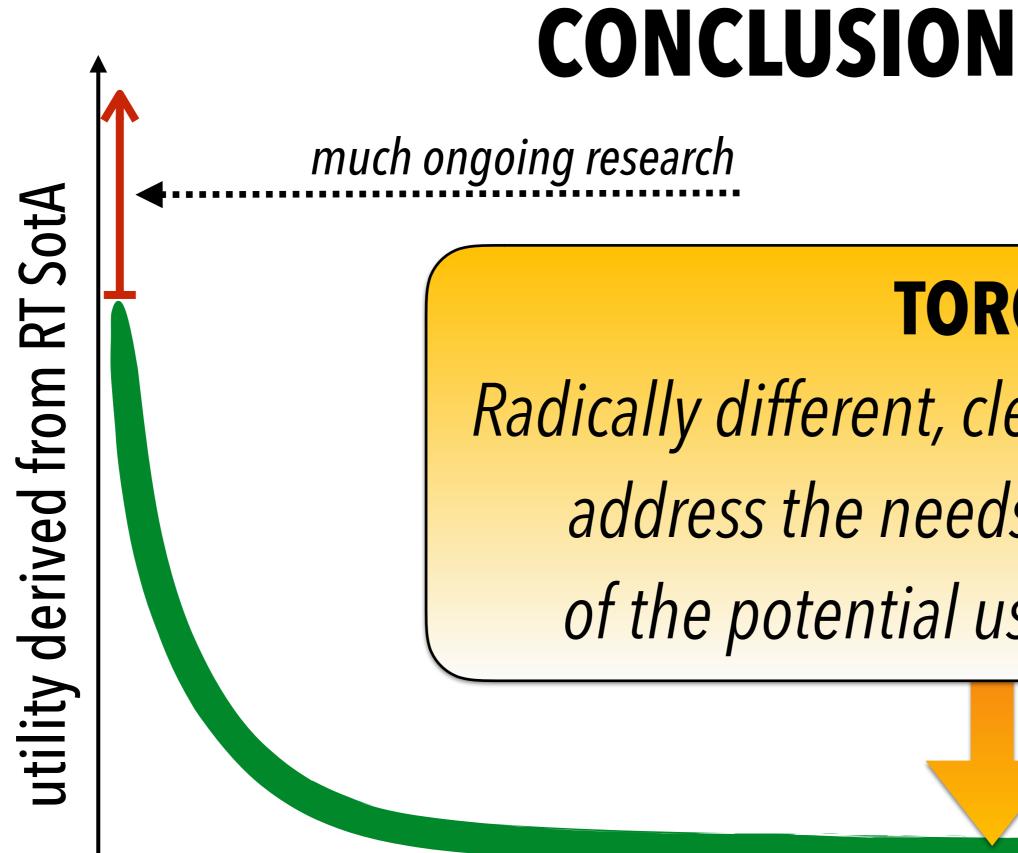
Radically different, clean-slate attempt to address the needs of the "fat tail" of the potential users population.

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much expertise

The Five TOROS Principles

- **Theory-Oriented RTOS Design**
- 2. Declarative OS Abstractions
- **3.** Temporal Reflection
- 4. Structured Uncertainty Management
- 5. Trustworthy Analysis

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TOROS

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