

# DimSum

## A Decentralized Approach to Multi-language Semantics and Verification

**Michael Sammler**, *Simon Spies, Youngju Song, Emanuele D’Osualdo, Robert Krebbers, Deepak Garg, and Derek Dreyer*

POPL’23

January 18, 2023

**How can we reason modularly  
about multi-language programs?**

# Example

```
void main() {  
    char x[3]; x[0] = 1; x[1] = 2; // x = {1, 2, *}  
    memmove(x + 1, x + 0, 2); // x = {1, 1, 2}  
    print(x[1]); print(x[2]);  
}
```

# Example

System call



```
void main() {  
    char x[3]; x[0] = 1; x[1] = 2; // x = {1, 2, *}  
    memmove(x + 1, x + 0, 2); // x = {1, 1, 2}  
    print(x[1]); print(x[2]);  
}
```

# Example

System call



```
void main() {  
    char x[3]; x[0] = 1; x[1] = 2; // x = {1, 2, *}  
    memmove(x + 1, x + 0, 2); // x = {1, 1, 2}  
    print(x[1]); print(x[2]);  
}
```

Library **print**

```
print : mov x8, PRINT; syscall; ret
```

# Example

System call

```
void main() {  
    char x[3]; x[0] = 1; x[1] = 2; // x = {1, 2, *}  
    memmove(x + 1, x + 0, 2); // x = {1, 1, 2}  
    print(x[1]); print(x[2]);  
}
```

## Key aspects

#1 Extend **C** with system calls via **print** library

Library **print**

```
print : mov x8, PRINT; syscall; ret
```

# Example

## Key aspects

### #1 Extend **C** with system calls via **print** library

```
void main() {  
    char x[3]; x[0] = 1; x[1] = 2;    // x = {1, 2, *}  
    memmove(x + 1, x + 0, 2);        // x = {1, 1, 2}  
    print(x[1]); print(x[2]);  
}
```

#### Library **memmove**

```
void memmove(char *d, char *s, int n) {  
    if (locl(d, s)) { return memcpy(d, s, n, 1); }  
    else { return memcpy(d+n-1, s+n-1, n, -1); } }  
void memcpy(char *d, char *s, int n, int o) {  
    if (0 < n) { *d = *s; memcpy(d+o, s+o, n-1, o) } }
```

#### Library **print**

```
print : mov x8, PRINT; syscall; ret
```

# Example

## Key aspects

#1 Extend **C** with system calls via **print** library

```
void main() {  
    char x[3]; x[0] = 1; x[1] = 2;    // x = {1, 2, *}  
    memmove(x + 1, x + 0, 2);        // x = {1, 1, 2}  
    print(x[1]); print(x[2]);  
}
```

### Address comparison

#### Library **memmove**

```
void memmove(char *d, char *s, int n) {  
    if (locl(d, s)) { return memcpy(d, s, n, 1); }  
    else { return memcpy(d+n-1, s+n-1, n, -1); } }  
void memcpy(char *d, char *s, int n, int o) {  
    if (0 < n) { *d = *s; memcpy(d+o, s+o, n-1, o) } }
```

#### Library **print**

```
print : mov x8, PRINT; syscall; ret
```



# Example

## Key aspects

#1 Extend **C** with system calls via **print** library

```
void main() {  
    char x[3]; x[0] = 1; x[1] = 2;    // x = {1, 2, *}  
    memmove(x + 1, x + 0, 2);        // x = {1, 1, 2}  
    print(x[1]); print(x[2]);  
}
```

### Address comparison

Library **memmove**

```
void memmove(char *d, char *s, int n) {  
    if (locl(d, s)) { return memcpy(d, s, n, 1); }  
    else { return memcpy(d+n-1, s+n-1, n, -1); } }  
void memcpy(char *d, char *s, int n, int o) {  
    if (0 < n) { *d = *s; memcpy(d+o, s+o, n-1, o) } }
```

Library **locl**

```
locl : sle x0, x0, x1; ret
```

Library **print**

```
print : mov x8, PRINT; syscall; ret
```

# Example

## Key aspects

#1 Extend **C** with system calls via **print** library

#2 Use **Asm**'s concrete memory model to provide address comparison to **C** via **locle**

```
void main() {  
    char x[3]; x[0] = 1; x[1] = 2;    // x = {1, 2, *}  
    memmove(x + 1, x + 0, 2);        // x = {1, 1, 2}  
    print(x[1]); print(x[2]);  
}
```

### Address comparison

Library **memmove**

```
void memmove(char *d, char *s, int n) {  
    if (locle(d, s)) { return memcpy(d, s, n, 1); }  
    else { return memcpy(d+n-1, s+n-1, n, -1); } }  
void memcpy(char *d, char *s, int n, int o) {  
    if (0 < n) { *d = *s; memcpy(d+o, s+o, n-1, o) } }
```

Library **locle**

```
locle : sle x0, x0, x1; ret
```

Library **print**

```
print : mov x8, PRINT; syscall; ret
```

# Example

## Key aspects

#1 Extend **C** with system calls via **print** library

#2 Use **Asm**'s concrete memory model to provide address comparison to **C** via **locle**

#3 Reason about **memcpy** independent of **Asm**

```
void main() {  
    char x[3]; x[0] = 1; x[1] = 2;    // x = {1, 2, *}  
    memmove(x + 1, x + 0, 2);        // x = {1, 1, 2}  
    print(x[1]); print(x[2]);  
}
```

### Library **memmove**

```
void memmove(char *d, char *s, int n) {  
    if (locle(d, s)) { return memcpy(d, s, n, 1); }  
    else { return memcpy(d+n-1, s+n-1, n, -1); } }  
void memcpy(char *d, char *s, int n, int o) {  
    if (0 < n) { *d = *s; memcpy(d+o, s+o, n-1, o) } }
```

### Library **locle**

```
locle : sle x0, x0, x1; ret
```

### Library **print**

```
print : mov x8, PRINT; syscall; ret
```

# Existing approaches

# Existing approaches

## 1. Pilsner [Neis et al. 2015], ...

*All target-level (**Asm**) code is specified using source-level (**C**) code.*

# Existing approaches

## 1. Pilsner [Neis et al. 2015], ...

*All target-level (**Asm**) code is specified using source-level (**C**) code.*



Fixes the source language as specification language:  
disallows **print** and **locle**

# Existing approaches

## 1. Pilsner [Neis et al. 2015], ...

*All target-level (**Asm**) code is specified using source-level (**C**) code.*

## 2. CompCert-based approaches

[Stewart et al. 2015, ...]

*Link all languages via a common interaction protocol.*



Fixes the source language as specification language:  
disallows **print** and **locle**

# Existing approaches

## 1. Pilsner [Neis et al. 2015], ...

*All target-level (**Asm**) code is specified using source-level (**C**) code.*

## 2. CompCert-based approaches

[Stewart et al. 2015, ...]

*Link all languages via a common interaction protocol.*



Fixes the source language as specification language: disallows **print** and **locle**

Fixes (abstract) memory model: disallows **locle**



# Existing approaches

## 1. Pilsner [Neis et al. 2015], ...

*All target-level (**Asm**) code is specified using source-level (**C**) code.*

## 2. CompCert-based approaches [Stewart et al. 2015, ...]

*Link all languages via a common interaction protocol.*

## 3. Syntactic multi-languages [Ahmed and Blume 2011, ...]

*Embed all languages into one large multi-language.*



Fixes the source language as specification language: disallows **print** and **locle**

Fixes (abstract) memory model: disallows **locle**

# Existing approaches

## 1. Pilsner [Neis et al. 2015], ...

*All target-level (**Asm**) code is specified using source-level (**C**) code.*

## 2. CompCert-based approaches [Stewart et al. 2015, ...]

*Link all languages via a common interaction protocol.*

## 3. Syntactic multi-languages [Ahmed and Blume 2011, ...]

*Embed all languages into one large multi-language.*



Fixes the source language as specification language: disallows **print** and **locle**

Fixes (abstract) memory model: disallows **locle**

Fixes the set of languages: requires reasoning about **Asm** context for **memcpy**

# Desiderata for multi-language reasoning

# Desiderata for multi-language reasoning

## **#1 No fixed source language as specification language**

Can link with target code that is not representable in the source

# Desiderata for multi-language reasoning

## **#1 No fixed source language as specification language**

Can link with target code that is not representable in the source

## **#2 No fixed memory model**

Supports languages with different memory models

# Desiderata for multi-language reasoning

## **#1 No fixed source language as specification language**

Can link with target code that is not representable in the source

## **#2 No fixed memory model**

Supports languages with different memory models

## **#3 No fixed set of languages**

Allows language-local reasoning

# Desiderata for multi-language reasoning

## **#1 No fixed source language as specification language**

Can link with target code that is not representable in the source

## **#2 No fixed memory model**

Supports languages with different memory models

## **#3 No fixed set of languages**

Allows language-local reasoning

## **#4 No fixed notion of linking**

# Desiderata for multi-language reasoning

## #1 No fixed source language as specification language

Can link with target code that is not representable in the source

## #2 No fixed memory model

Supports languages with different memory models

## #3 No fixed set of languages

Allows language-local reasoning



# DimSum

## #4 No fixed notion of linking





# DimSum

## ***Decentralized* Multi-language Reasoning**

#1 No fixed source / spec. language

#2 No fixed memory model

#3 No fixed set of languages

#4 No fixed notion of linking



# DimSum

*Decentralized* Multi-language Reasoning

#1 No fixed source / spec. language

#2 No fixed memory model

#3 No fixed set of languages

#4 No fixed notion of linking



# DimSum

↓

## ***Decentralized* Multi-language Reasoning**

Combining ideas from  
*process algebra, Kripke relations, angelic non-determinism, ...*

#1 No fixed source / spec. language

#2 No fixed memory model

#3 No fixed set of languages

#4 No fixed notion of linking



# DimSum

↓

## ***Decentralized Multi-language Reasoning***

Combining ideas from  
*process algebra, Kripke relations, angelic non-determinism, ...*

### **Instantiations**

**Rec:** *C-like language*

**Asm:** *assembly language*

**Spec:** *specification language*

#1 No fixed source / spec. language

#2 No fixed memory model

#3 No fixed set of languages

#4 No fixed notion of linking



# DimSum

## *Decentralized* Multi-language Reasoning

Combining ideas from  
*process algebra, Kripke relations, angelic non-determinism, ...*

### Instantiations

**Rec**: *C-like language*

**Asm**: *assembly language*

**Spec**: *specification language*

### Evaluation

$\downarrow R$ : Compiler from *Rec* to *Asm*

**locl**: pointer comparison

$\oplus_{\text{coro}}$ : coroutines

#1 No fixed source / spec. language

#2 No fixed memory model

#3 No fixed set of languages

#4 No fixed notion of linking



# DimSum



## *Decentralized* Multi-language Reasoning

Combining ideas from  
*process algebra, Kripke relations, angelic non-determinism, ...*

### Instantiations

**Rec**: *C-like language*

**Asm**: *assembly language*

**Spec**: *specification language*

### Evaluation

$\downarrow R$  : Compiler from *Rec* to *Asm*

**locl** : pointer comparison

$\oplus_{\text{coro}}$  : coroutines

#1 No fixed source / spec. language

#2 No fixed memory model

#3 No fixed set of languages

#4 No fixed notion of linking



# DimSum



## *Decentralized* Multi-language Reasoning

Combining ideas from  
*process algebra, Kripke relations, angelic non-determinism, ...*

### Instantiations

**Rec**: *C-like language*

**Asm**: *assembly language*

**Spec**: *specification language*

### Evaluation

$\downarrow R$  : Compiler from *Rec* to *Asm*

**locle** : pointer comparison

$\oplus_{\text{coro}}$  : coroutines

# Example: onetwo

```
void main() {  
    char x[3]; x[0] = 1; x[1] = 2;    // x = {1, 2, *}  
    memmove(x + 1, x + 0, 2);        // x = {1, 1, 2}  
    print(x[1]); print(x[2]);  
}
```

## Library `memmove`

```
void memmove(char *d, char *s, int n) {  
    if (loclc(d, s)) { return memcpy(d, s, n, 1); }  
    else { return memcpy(d+n-1, s+n-1, n, -1); } }  
void memcpy(char *d, char *s, int n, int o) {  
    if (0 < n) { *d = *s; memcpy(d+o, s+o, n-1, o) } }
```

## Library `loclc`

```
loclc : sle x0, x0, x1; ret
```

## Library `print`

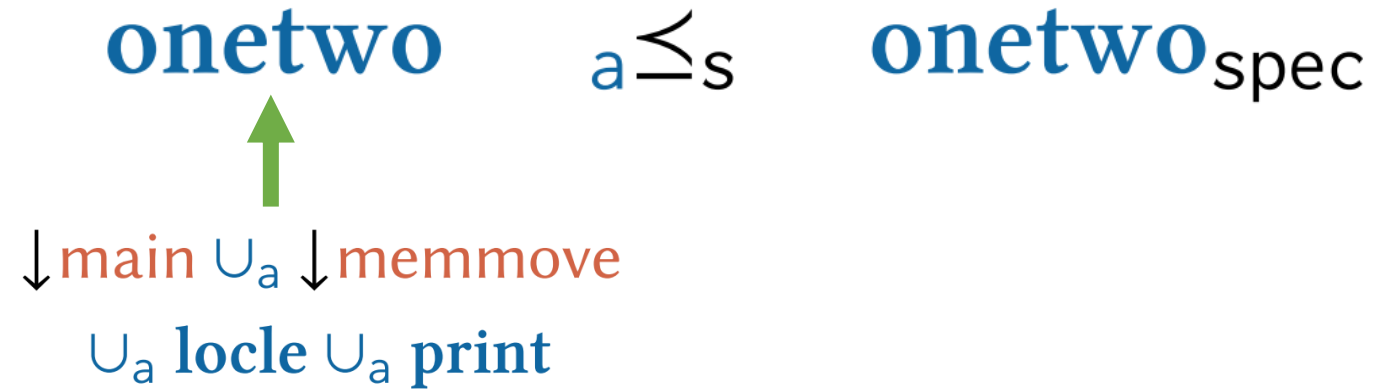
```
print : mov x8, PRINT; syscall; ret
```



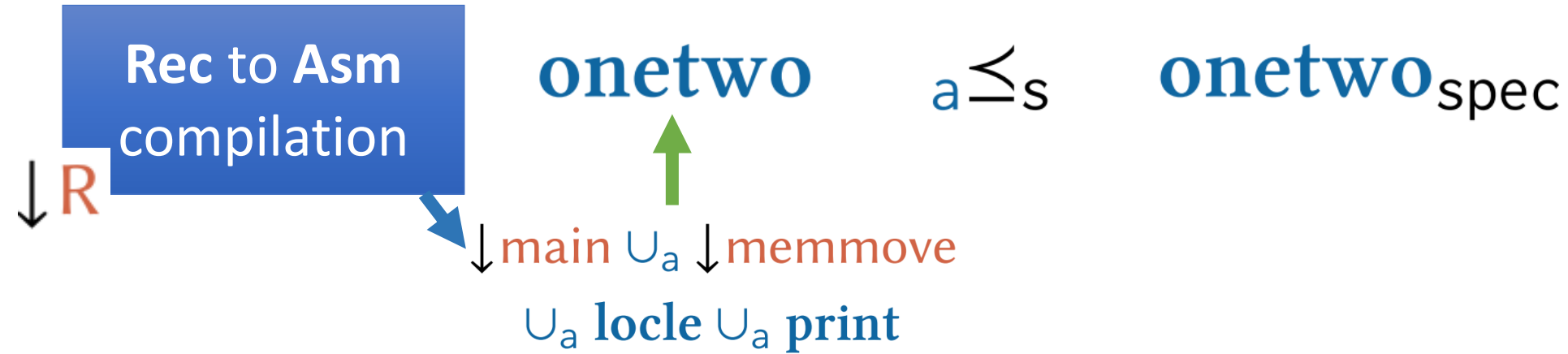
# Specification

**onetwo**  $\sqsubseteq_s$  **onetwo**<sub>spec</sub>

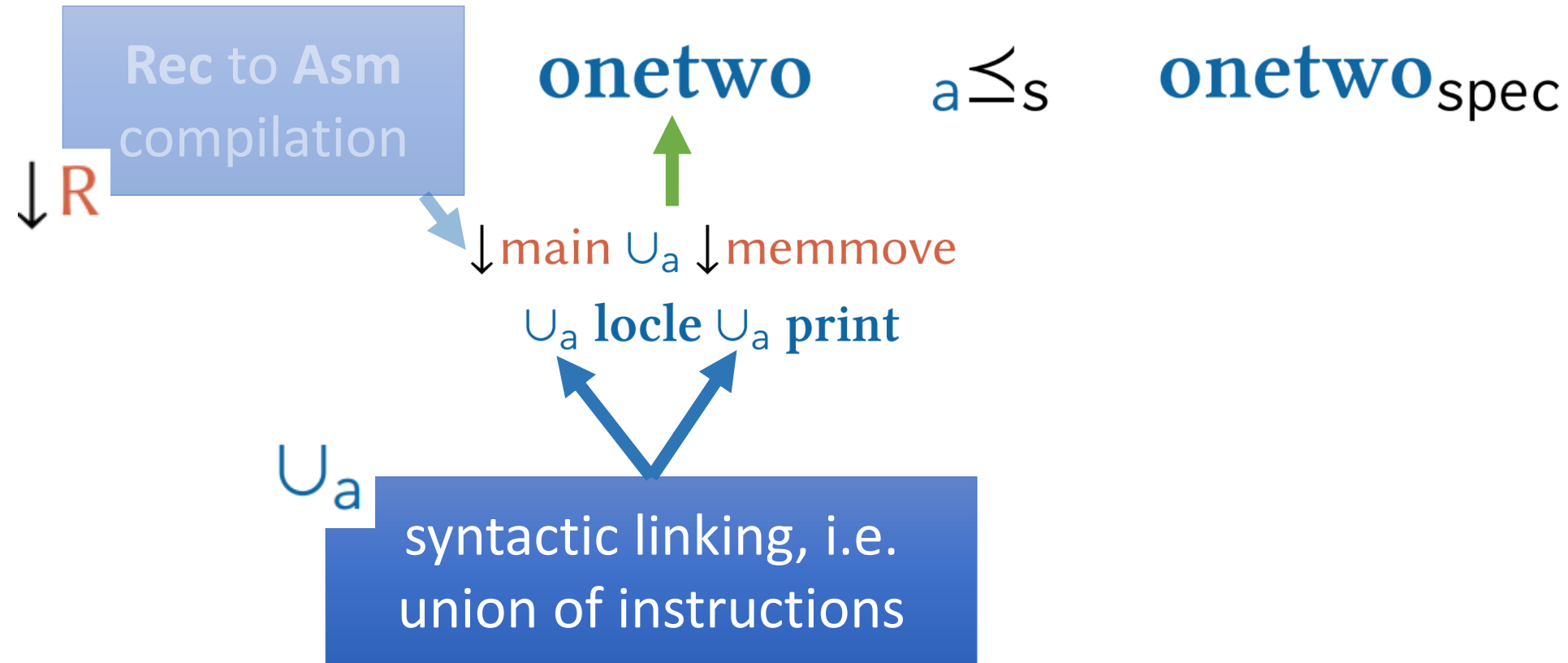
# Specification



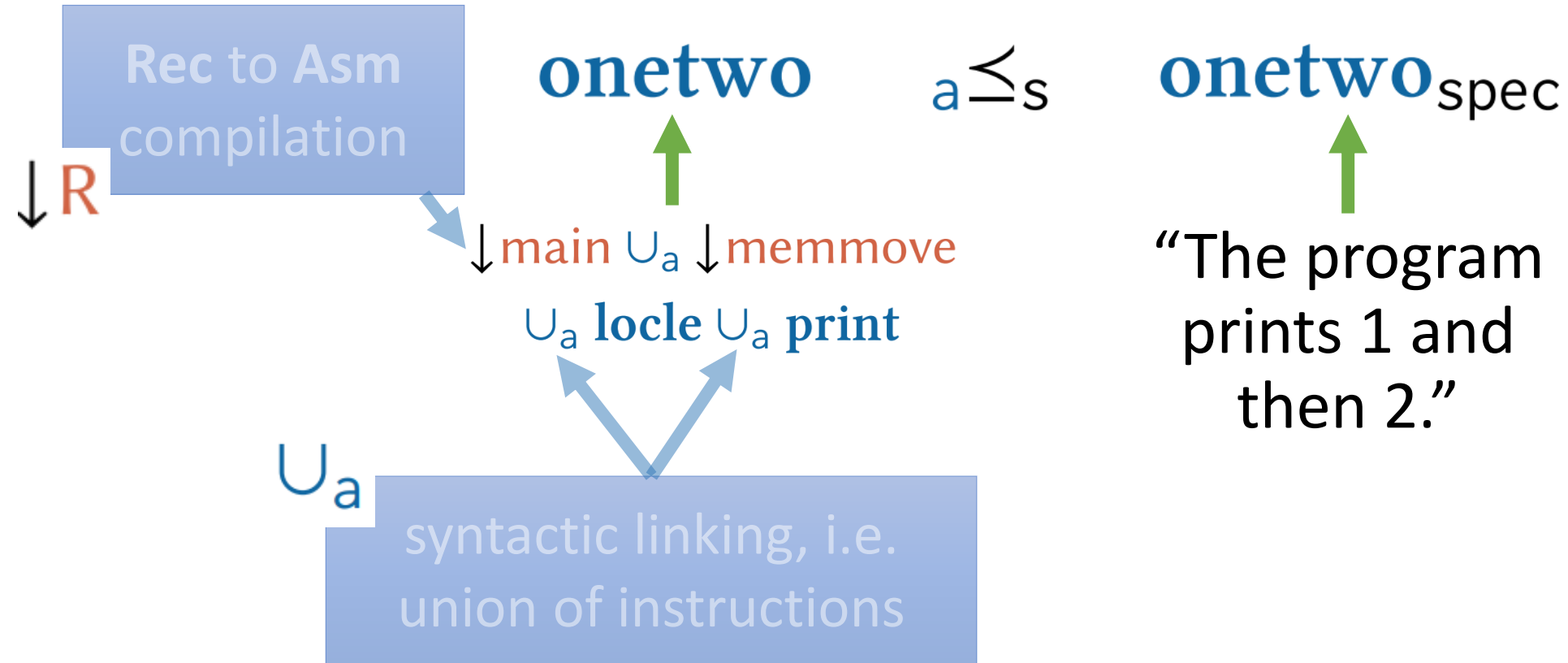
# Specification



# Specification



# Specification



# Specification



How to define  
refinement *in a*  
*decentralized*  
*fashion?*

**onetwo**  $\text{a} \preceq_s$  **onetwo**<sub>spec</sub>

*defined as*



# Specification



How to define  
refinement *in a  
decentralized  
fashion?*

**onetwo**  $\preceq_s$  **onetwo**<sub>spec</sub>

*defined as*

$\llbracket \mathbf{onetwo} \rrbracket_a \preceq \mathbf{onetwo}_{\text{spec}}$



Use *labeled  
transition  
systems* as  
semantic domain  
with *interaction  
via events!*

# Specification



How to define refinement *in a decentralized fashion?*

$$\mathbf{onetwo} \quad a \preceq_s \quad \mathbf{onetwo}_{\text{spec}}$$

*defined as*

$$\llbracket \mathbf{onetwo} \rrbracket_a \preceq \mathbf{onetwo}_{\text{spec}}$$



syntactic program to semantic LTS (i.e., module)



Use *labeled transition systems* as semantic domain with *interaction via events!*



# Specification



How to define refinement *in a decentralized fashion?*

$$\mathbf{onetwo} \quad a \preceq_s \mathbf{onetwo}_{\text{spec}}$$

*defined as*

$$\llbracket \mathbf{onetwo} \rrbracket_a \preceq \mathbf{onetwo}_{\text{spec}}$$

“are in simulation”



Use *labeled transition systems* as semantic domain with *interaction via events!*

# Proof outline

$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\
 &\preceq \llbracket \downarrow \text{main} \rrbracket_a \quad \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \quad \oplus_a \llbracket \text{locle} \rrbracket_a \quad \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \quad \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \quad \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \llbracket \text{main}_{\text{spec}} \rrbracket_{r \Rightarrow a} \quad \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \text{onetwo}_{\text{spec}}
 \end{aligned}$$

# Proof outline

$\llbracket \text{onetwo} \rrbracket_a = \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a$

1. Modularize proof via semantic linking  $\oplus_a$
2. Translate between languages via semantic wrapping  $\llbracket \cdot \rrbracket_{r \Rightarrow a}$
3. Language-local verification in **Rec**

$\leq \text{onetwo}_{\text{spec}}$

# Proof outline

$\llbracket \text{onetwo} \rrbracket_a = \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a$

1. Modularize proof via semantic linking  $\oplus_a$
2. Translate between languages via semantic wrapping  $\llbracket \cdot \rrbracket_{r \Rightarrow a}$
3. Language-local verification in **Rec**

$\leq \text{onetwo}_{\text{spec}}$

# Syntactic vs. semantic linking

$$\begin{aligned} \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \rrbracket_a & \cup_a \downarrow \text{memmove} & & \cup_a \text{locle} & & \cup_a \text{print} \rrbracket_a \\ &\preceq \llbracket \downarrow \text{main} \rrbracket_a & \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a & & \oplus_a \llbracket \text{locle} \rrbracket_a & & \oplus_a \llbracket \text{print} \rrbracket_a \end{aligned}$$

# Syntactic vs. semantic linking

$$\llbracket \text{onetwo} \rrbracket_a = \llbracket \downarrow \text{main} \rrbracket_a \\ \preceq \llbracket \downarrow \text{main} \rrbracket_a$$

$$\bigcup_a \downarrow \text{memmove} \\ \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a$$

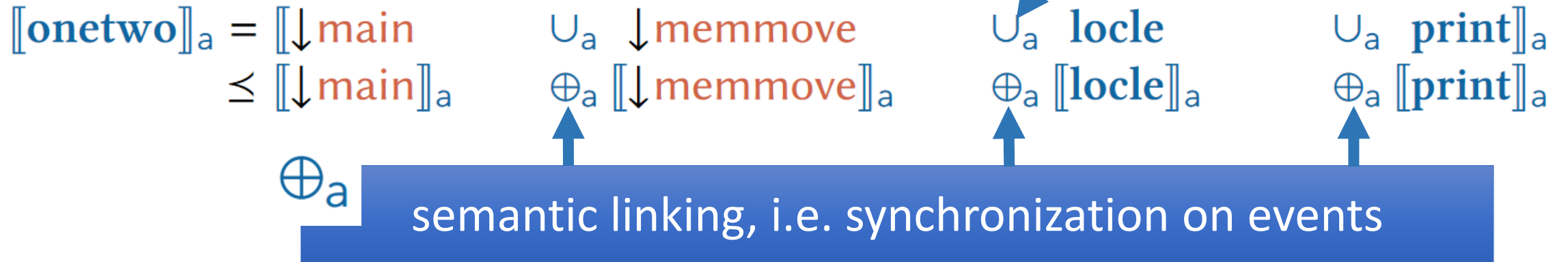
$$\bigcup_a \text{locle} \\ \oplus_a \llbracket \text{locle} \rrbracket_a$$

$$\bigcup_a \text{print} \\ \oplus_a \llbracket \text{print} \rrbracket_a$$

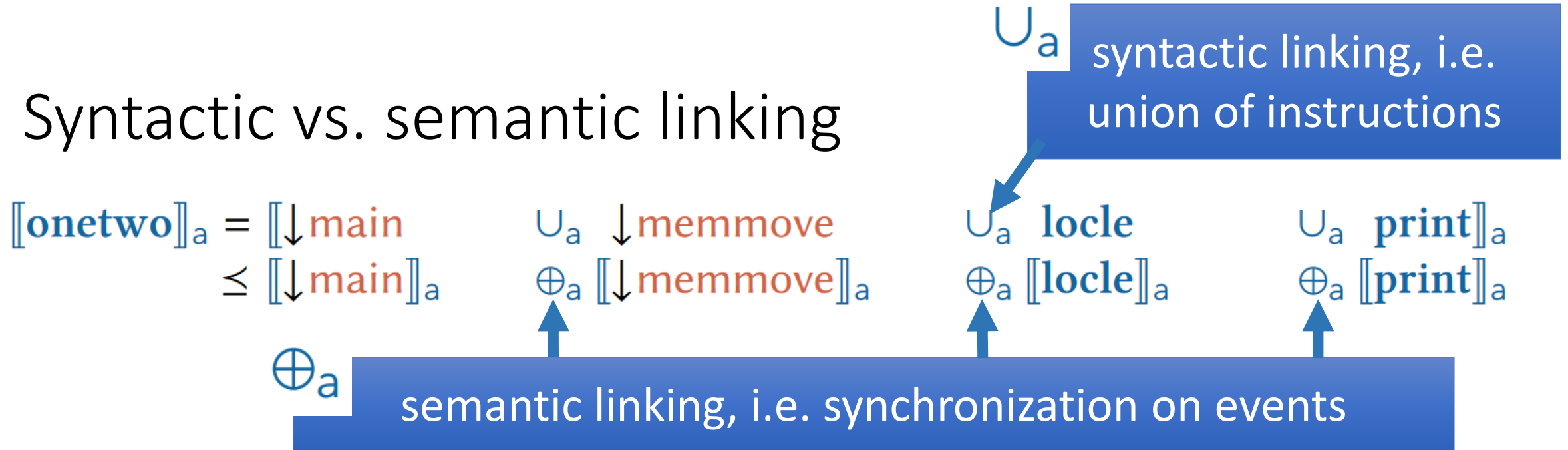
$\bigcup_a$  syntactic linking, i.e. union of instructions



# Syntactic vs. semantic linking



# Syntactic vs. semantic linking

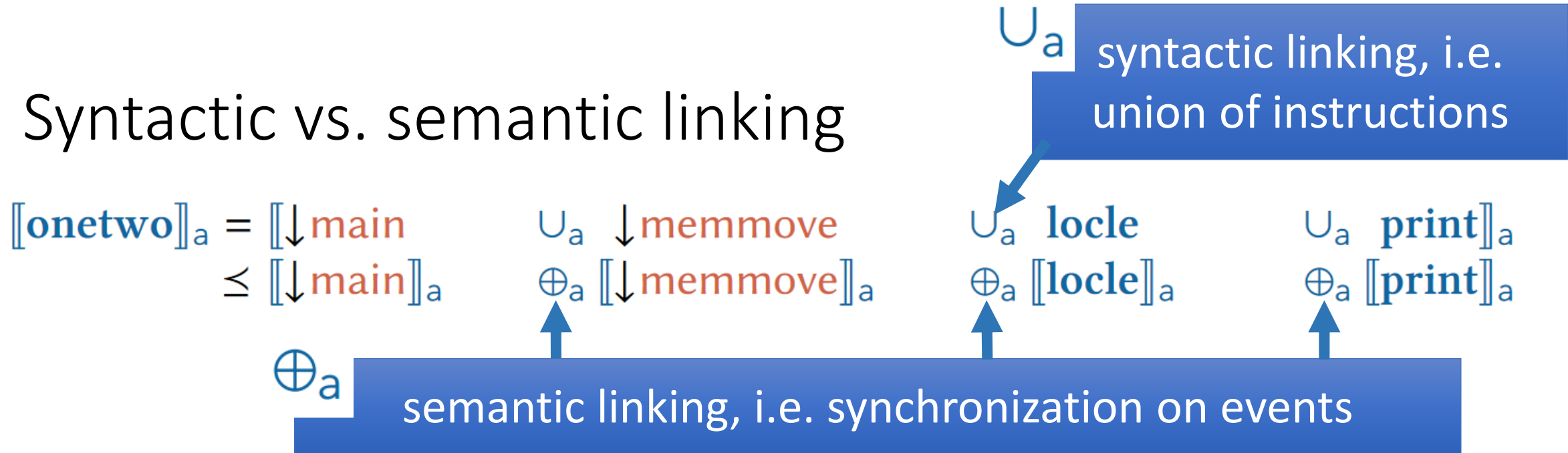


$\oplus_a$  matches outgoing **Jump!** with incoming **Jump?**

Events  $\ni e ::= \text{Jump!}(r, m) \mid \text{Jump?}(r, m)$   
 $\mid \text{Syscall!}(v_1, v_2, m) \mid \text{SyscallRet?}(v, m)$



# Syntactic vs. semantic linking



Key property: Horizontal compositionality

ASM-LINK-HORIZONTAL

$$\frac{M_1 \leq M'_1 \quad M_2 \leq M'_2}{M_1 \oplus_a M_2 \leq M'_1 \oplus_a M'_2}$$

$\oplus_a$  enables *modular reasoning* using  $\leq$ .

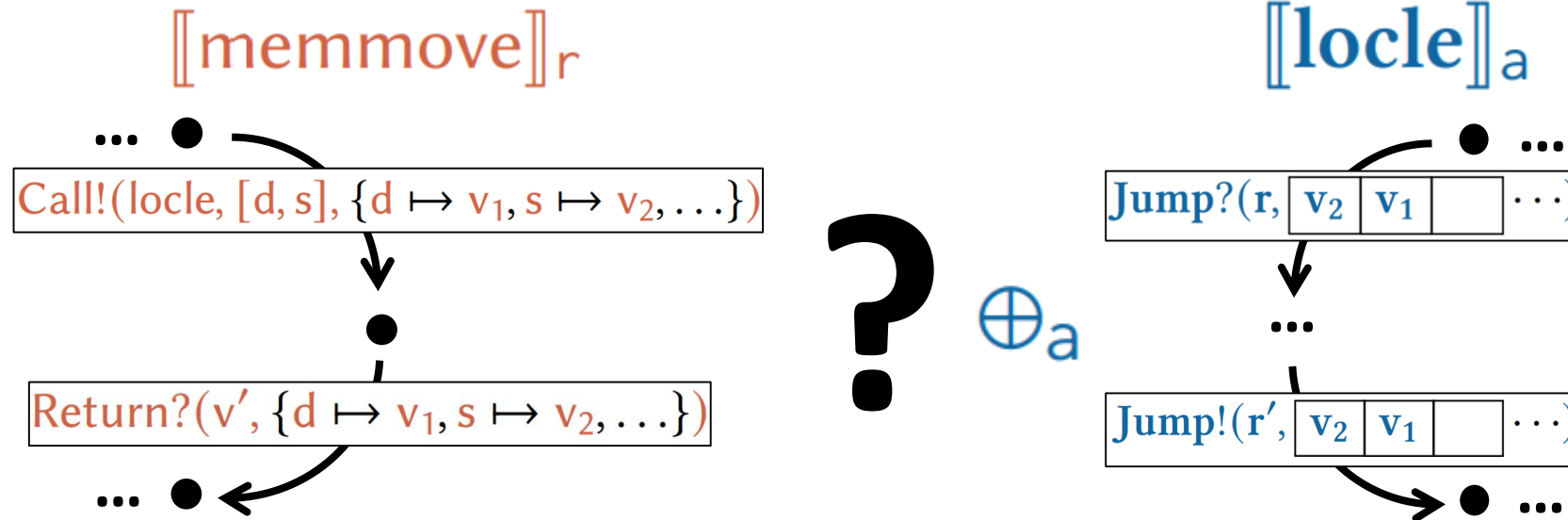
# Proof outline

$\llbracket \text{onetwo} \rrbracket_a = \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a$

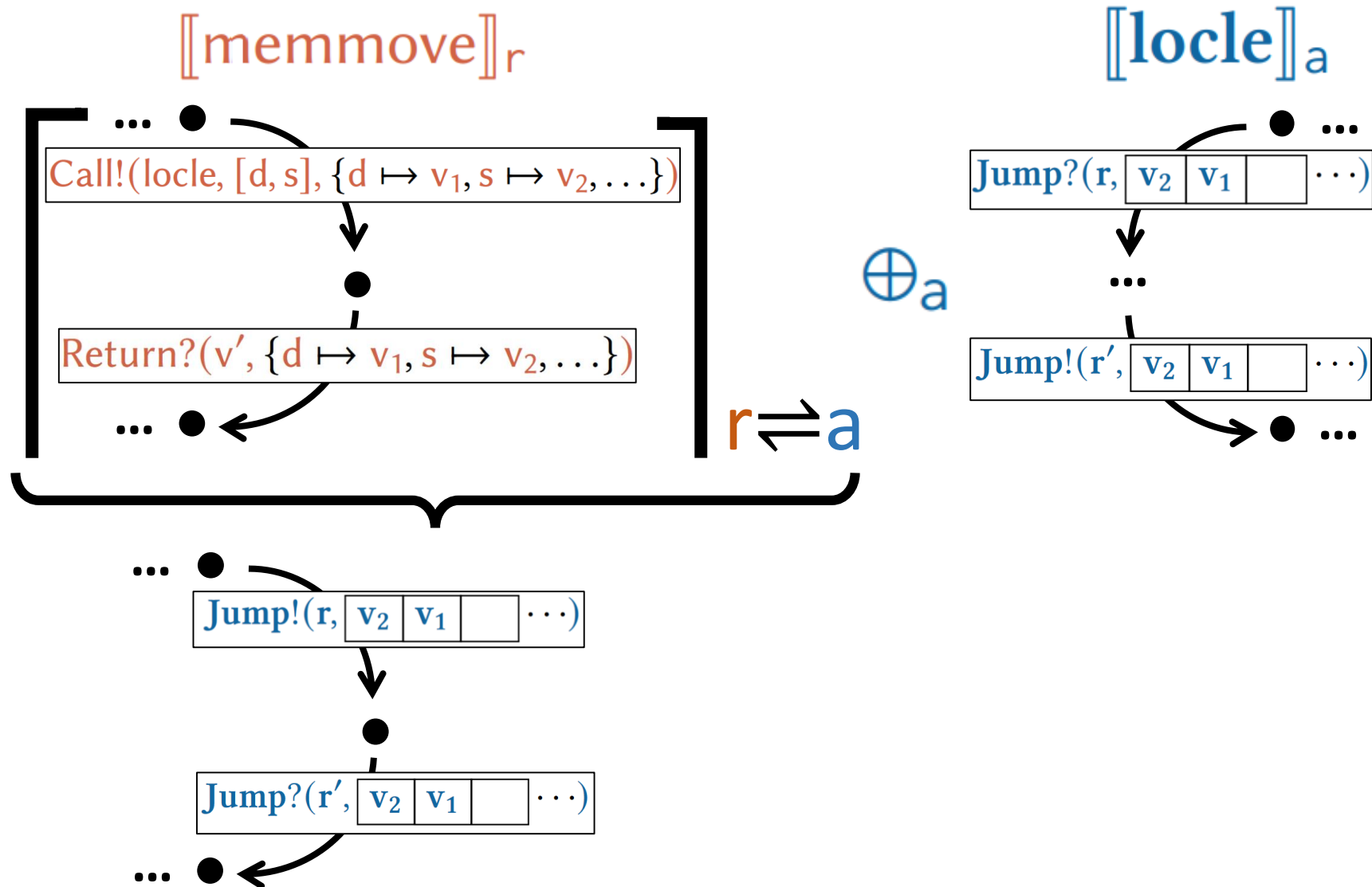
1. Modularize proof via semantic linking  $\oplus_a$
2. Translate between languages via semantic wrapping  $\llbracket \cdot \rrbracket_{r \Rightarrow a}$
3. Language-local verification in **Rec**

$\leq \text{onetwo}_{\text{spec}}$

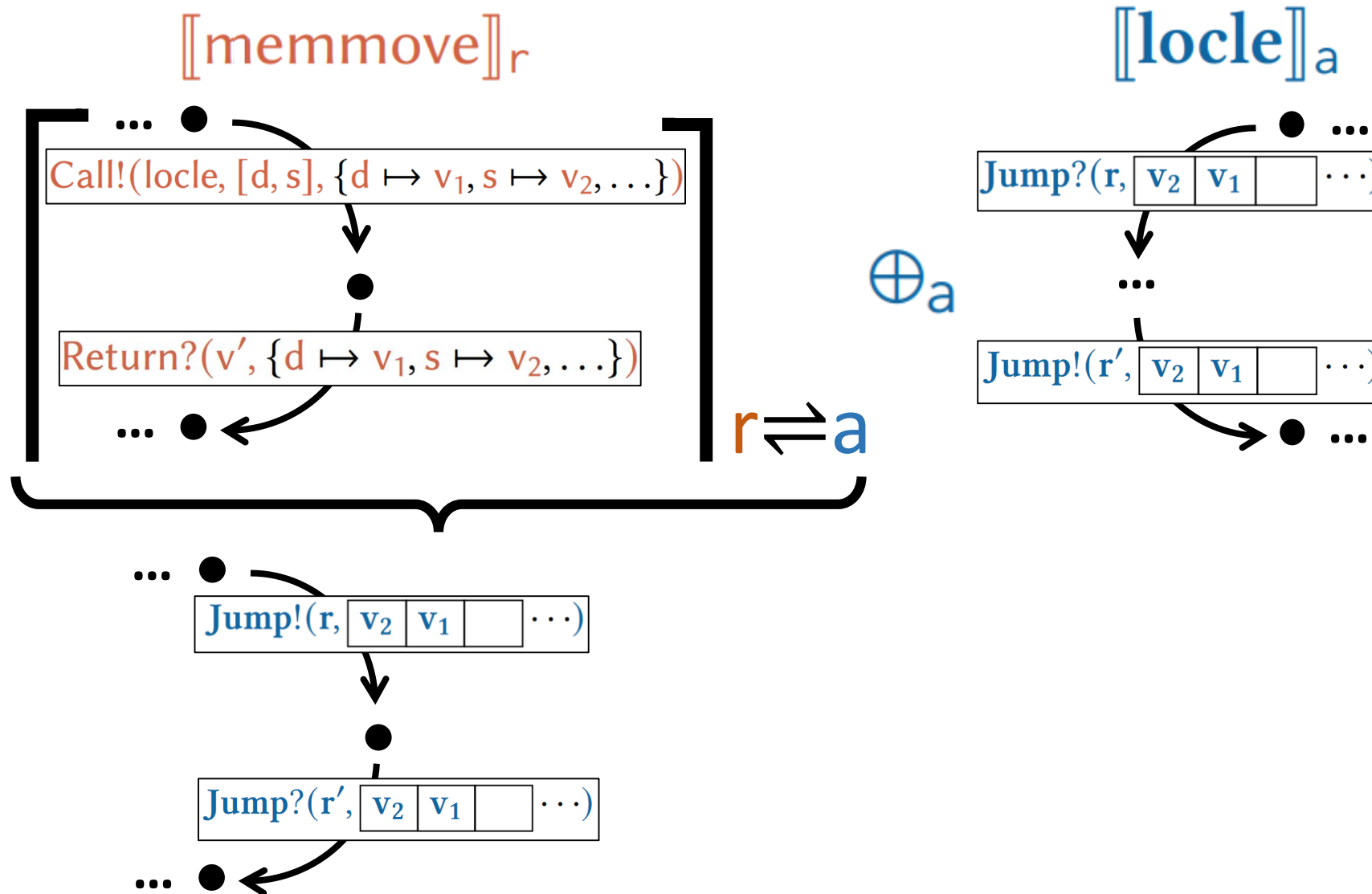
# Translating between languages: wrapper $\llbracket \cdot \rrbracket_{r \Rightarrow a}$



# Translating between languages: wrapper $\llbracket \cdot \rrbracket_{r \rightleftharpoons a}$



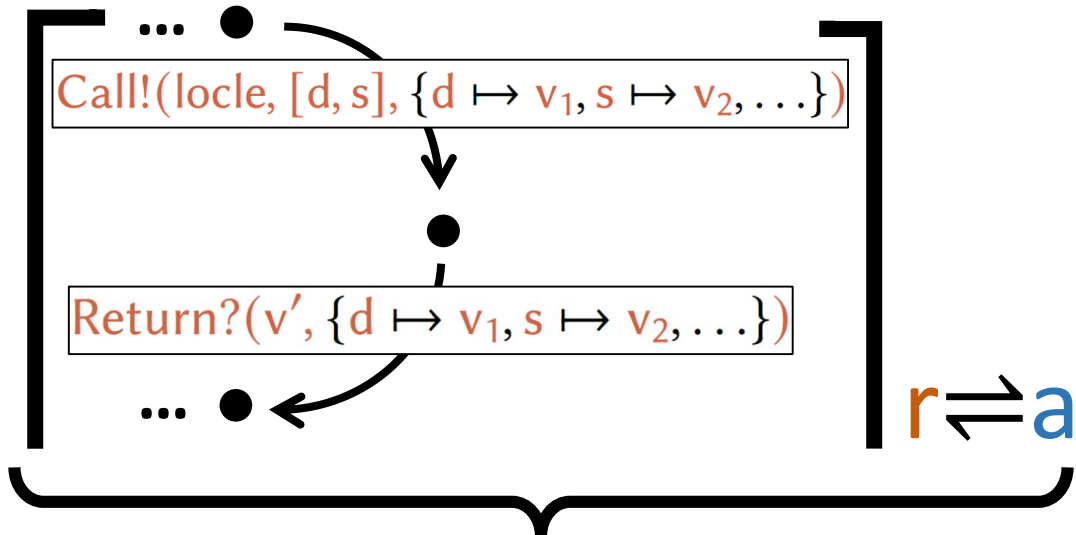
# Translating between languages: wrapper $\llbracket \cdot \rrbracket_{r \rightleftharpoons a}$



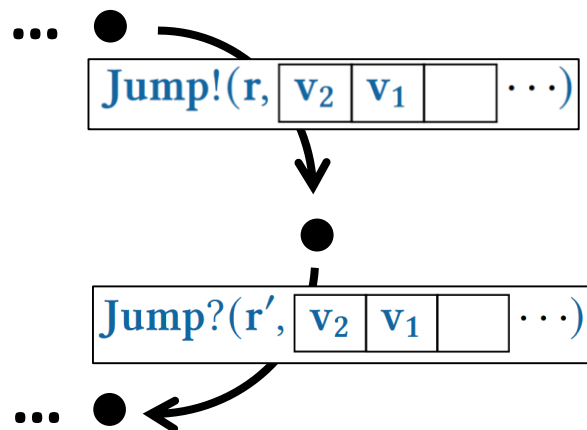
**Desideratum #2:**  
 $\llbracket \cdot \rrbracket_{r \rightleftharpoons a}$  enables interoperation between language and memory models

# Translating between languages: wrapper $\llbracket \cdot \rrbracket_{r \rightleftharpoons a}$

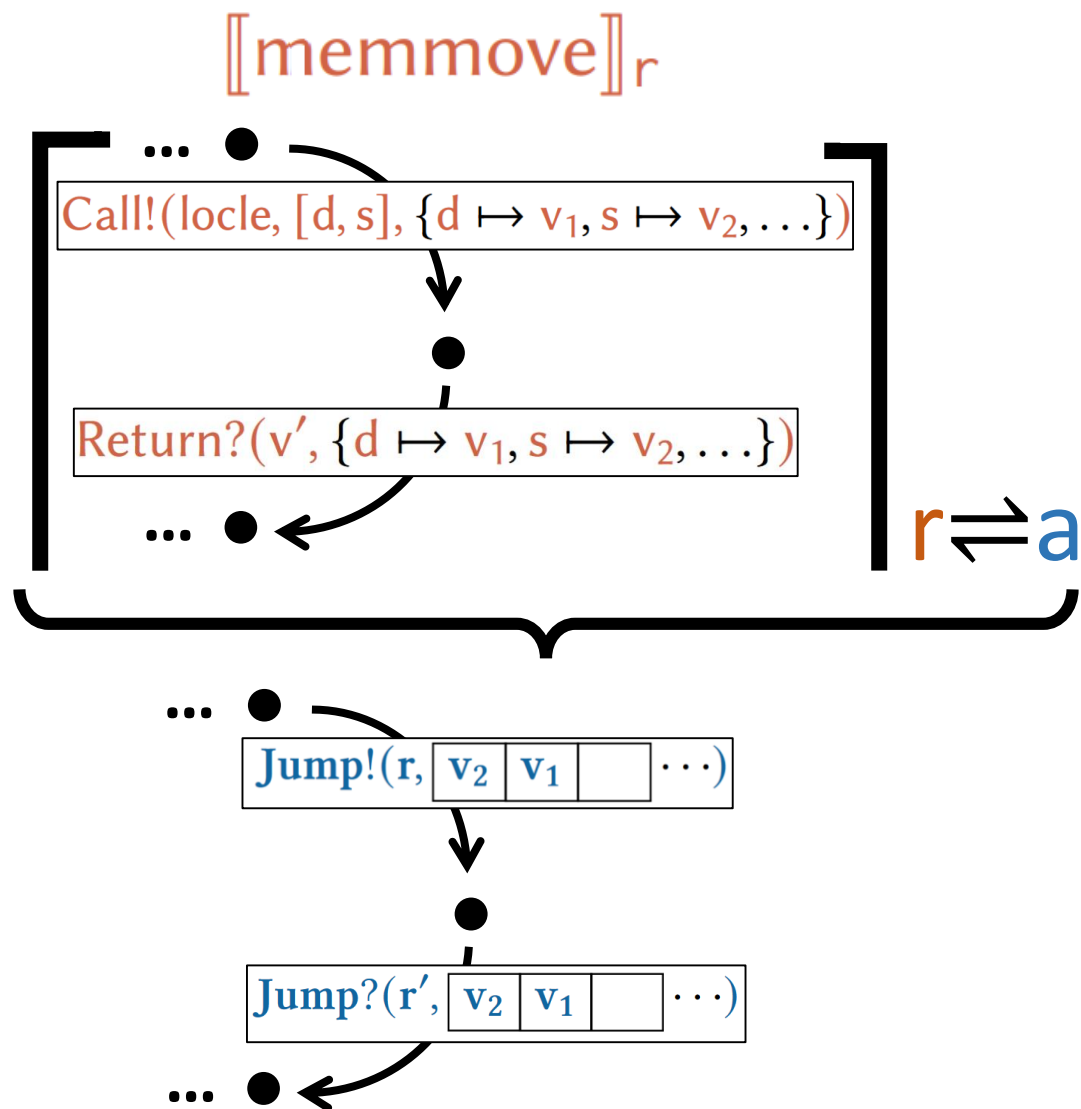
$\llbracket \text{memmove} \rrbracket_r$



**Key technical idea:**  
rely-guarantee protocol via  
*demonic* and *angelic*  
non-determinism



# Translating between languages: wrapper $\llbracket \cdot \rrbracket_{r \Rightarrow a}$



**Key technical idea:**  
rely-guarantee protocol via  
*demonic* and *angelic*  
non-determinism

inspired by *Conditional Contextual Refinement*  
[Song et al., POPL'23]



# Compiler correctness

$$\begin{aligned} \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\ &\sqsubseteq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\ &\sqsubseteq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Leftrightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Leftrightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \end{aligned}$$



# Compiler correctness

$$\begin{aligned} \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\ &\sqsubseteq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\ &\sqsubseteq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \end{aligned}$$

COMPILER-CORRECT

$$\llbracket \downarrow R \rrbracket_a \sqsubseteq \llbracket \llbracket R \rrbracket_r \rrbracket_{r \Rightarrow a}$$

# Compiler correctness

$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\
 &\sqsubseteq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\sqsubseteq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a
 \end{aligned}$$

syntactically translated

COMPILER-CORRECT

$$\llbracket \downarrow R \rrbracket_a \sqsubseteq \llbracket \llbracket R \rrbracket_r \rrbracket_{r \Rightarrow a}$$

# Compiler correctness

$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\
 &\sqsubseteq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\sqsubseteq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a
 \end{aligned}$$

syntactically translated

COMPILER-CORRECT

$$\llbracket \downarrow R \rrbracket_a \sqsubseteq \llbracket \llbracket R \rrbracket_r \rrbracket_{r \Rightarrow a}$$

semantically translated

# Abstracting **Asm** to **Rec** transition system

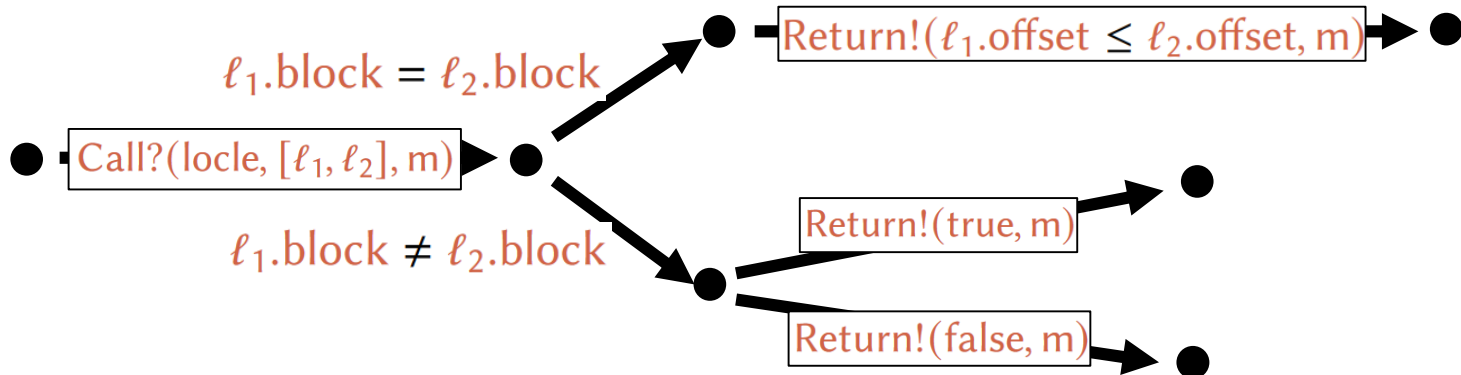
$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\
 &\preceq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_{r \Rightarrow a}
 \end{aligned}$$

LOCLE-CORRECT  $\llbracket \text{locle} \rrbracket_a \preceq \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a}$

# Abstracting **Asm** to **Rec** transition system

$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\
 &\preceq \llbracket \downarrow \text{main} \rrbracket_a \quad \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \quad \oplus_a \llbracket \text{locle} \rrbracket_a \quad \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \quad \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \quad \oplus_a \llbracket \text{locle} \rrbracket_a \quad \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \quad \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \quad \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \quad \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_{r \Rightarrow a}
 \end{aligned}$$

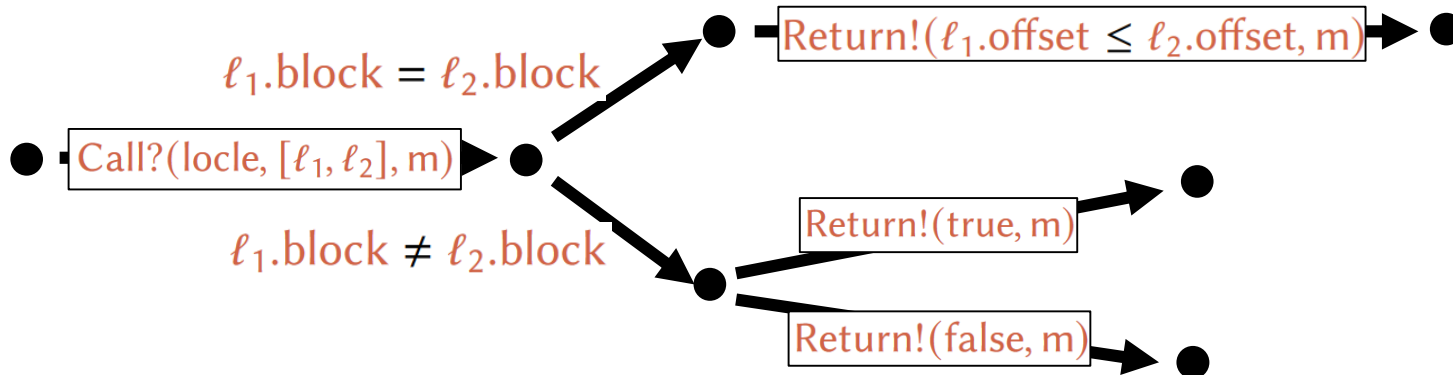
LOCLE-CORRECT  $\llbracket \text{locle} \rrbracket_a \preceq \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a}$



# Abstracting **Asm** to **Rec** transition system

$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\
 &\preceq \llbracket \downarrow \text{main} \rrbracket_a \quad \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \quad \oplus_a \llbracket \text{locle} \rrbracket_a \quad \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \quad \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \quad \oplus_a \llbracket \text{locle} \rrbracket_a \quad \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \quad \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \quad \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \quad \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_{r \Rightarrow a}
 \end{aligned}$$


LOCLE-CORRECT  $\llbracket \text{locle} \rrbracket_a \preceq \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a}$



**Desideratum #1**  
 No syntactic **Rec**  
 program required!

# Proof outline

$\llbracket \text{onetwo} \rrbracket_a = \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a$

1. Modularize proof via semantic linking  $\oplus_a$
2. Translate between languages via semantic wrapping  $\llbracket \cdot \rrbracket_{r \Rightarrow a}$
-  3. Language-local verification in **Rec**

$\leq \text{onetwo}_{\text{spec}}$

# Bundling **Rec** modules

$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\
 &\preceq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}}
 \end{aligned}$$



# Bundling **Rec** modules

$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\
 &\preceq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_{r \Rightarrow a} \\
 &\preceq \llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_{r \Rightarrow a}
 \end{aligned}$$

REC-TO-ASM-LINK

$$\llbracket M_1 \rrbracket_{r \Rightarrow a} \oplus_a \llbracket M_2 \rrbracket_{r \Rightarrow a} \preceq \llbracket M_1 \oplus_r M_2 \rrbracket_{r \Rightarrow a}$$

# Rec-level reasoning

$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\
 &\preceq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \llbracket \text{main}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}}
 \end{aligned}$$

# Rec-level reasoning

$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\
 &\preceq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \llbracket \text{main}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}}
 \end{aligned}$$

$$\llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \text{locle}_{\text{spec}} \preceq \text{main}_{\text{spec}}$$

# Rec-level reasoning

**Desideratum #3:**  
 Language-local  
 reasoning  
 (independent of **Asm**)

$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \cup_a \downarrow \text{print} \rrbracket_a \\
 &\preceq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{locle} \rrbracket_a \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main}_{\text{spec}} \rrbracket_{r \Rightarrow a} \rrbracket_a \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_a
 \end{aligned}$$

$$\llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \preceq \llbracket \llbracket \text{main}_{\text{spec}} \rrbracket_{r \Rightarrow a} \rrbracket_a$$

# Rec-level reasoning

**Desideratum #3:**  
 Language-local  
 reasoning  
 (independent of **Asm**)

via

REC-WRAPPER-COMPAT

$$\frac{M \leq M'}{\begin{array}{c} \llbracket M \rrbracket_{r \Rightarrow a} \leq \llbracket M' \rrbracket_{r \Rightarrow a} \\ \oplus_a \llbracket \text{print} \rrbracket_a \\ \oplus_a \llbracket \text{print} \rrbracket_a \\ \oplus_a \text{print}_{\text{spec}} \\ \oplus_a \text{print}_{\text{spec}} \\ \oplus_a \text{print}_{\text{spec}} \end{array}}$$

$$\begin{array}{l} \llbracket \text{onetwo} \rrbracket_a = \llbracket \downarrow \text{main} \rrbracket_a \cup_a \llbracket \downarrow \text{main} \rrbracket_a \\ \preceq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{main} \rrbracket_a \\ \preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{locle} \rrbracket_a \rrbracket_a \\ \preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{locle}_{\text{spec}} \rrbracket_r \rrbracket_{r \Rightarrow a} \\ \preceq \llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \\ \preceq \llbracket \text{main}_{\text{spec}} \rrbracket_{r \Rightarrow a} \end{array}$$

$$\llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \text{locle}_{\text{spec}} \leq \text{main}_{\text{spec}}$$

# Reasoning with specifications

$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\
 &\preceq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \llbracket \text{main}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \text{onetwo}_{\text{spec}}
 \end{aligned}$$

# Complete verification

$$\begin{aligned}
 \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \quad \cup_a \downarrow \text{memmove} \quad \cup_a \text{locle} \quad \cup_a \text{print} \rrbracket_a \\
 &\preceq \llbracket \downarrow \text{main} \rrbracket_a \quad \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \quad \oplus_a \llbracket \text{locle} \rrbracket_a \quad \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \quad \oplus_a \llbracket \text{print} \rrbracket_a \\
 &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \text{locle}_{\text{spec}} \rrbracket_{r \Rightarrow a} \quad \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \llbracket \text{main}_{\text{spec}} \rrbracket_{r \Rightarrow a} \quad \oplus_a \text{print}_{\text{spec}} \\
 &\preceq \text{onetwo}_{\text{spec}}
 \end{aligned}$$

# Recap: Desiderata for multi-language reasoning



# Recap: Desiderata for multi-language reasoning

## **#1 No fixed source language as specification language**

Labeled transition systems as semantic domain

# Recap: Desiderata for multi-language reasoning

## #1 No fixed source language as specification language

Labeled transition systems as semantic domain

## #2 No fixed memory model

Wrappers like  $[\cdot]_{r \rightleftharpoons a}$  translate between memory models

# Recap: Desiderata for multi-language reasoning

## #1 No fixed source language as specification language

Labeled transition systems as semantic domain

## #2 No fixed memory model

Wrappers like  $\llbracket \cdot \rrbracket_{r \Rightarrow a}$  translate between memory models

## #3 No fixed set of languages

Language-local reasoning via compatibility with refinement

# Recap: Desiderata for multi-language reasoning

## #1 No fixed source language as specification language

Labeled transition systems as semantic domain

## #2 No fixed memory model

Wrappers like  $\lceil \cdot \rceil_{r \Rightarrow a}$  translate between memory models

## #3 No fixed set of languages

Language-local reasoning via compatibility with refinement

## #4 No fixed notion of linking

DimSum allows defining custom linking operators

# Recap: Desiderata for multi-language reasoning

## #1 No fixed source language as specification language

Labeled transition systems as semantic domain



# DimSum

## #2 No fixed memory model

Wrappers like  $\lceil \cdot \rceil_{r \Rightarrow a}$  translate between memory models

<https://plv.mpi-sws.org/dimsum>

## #3 No fixed set of languages

Language-local reasoning via compatibility with refinement

## #4 No fixed notion of linking

DimSum allows defining custom linking operators

# In the paper Questions?

Wrappers via demonic and angelic non-determinism

$$\llbracket \cdot \rrbracket_{r \Rightarrow a}$$

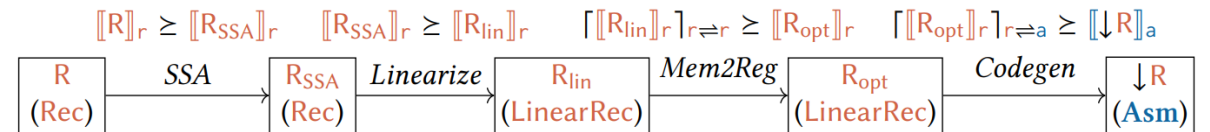
Language-generic combinators

$$\begin{array}{ll} M_1 \times M_2 & M_1 \setminus M_2 \\ \llbracket M \rrbracket_X & M_1 \oplus_X M_2 \end{array}$$

Coroutines

$$\llbracket \text{main} \rrbracket_r \oplus_{\text{coro}} \llbracket \text{stream} \rrbracket_r$$

Verification of  $\Downarrow R$



Operational semantics for dual non-determinism  
 $\rightarrow \in \mathcal{P}(S \times \text{option}(E) \times \mathcal{P}(S))$ .

#1 No fixed source / spec. language

#2 No fixed memory model

#3 No fixed set of languages

#4 No fixed notion of linking



# DimSum

# Questions?

<https://plv.mpi-sws.org/dimsum>

## *Decentralized Multi-language Reasoning*



Combining ideas from  
*process algebra, wrappers, Kripke relations, and angelic non-determinism*

Instantiations

**Rec**: *C-like language*

**Asm**: *assembly language*

**Spec**: *specification language*

Evaluation

$\downarrow R$  : Compiler from *Rec* to *Asm*

**locl** : pointer comparison

$\oplus_{\text{coro}}$  : coroutines

Backup slides



# What next?

Other language features

*e.g. closures, concurrency, ...*

# What next?

Other language features

*e.g. closures, concurrency, ...*

Combine with existing  
verification tools

*e.g. RefinedC, Islaris, ...*

# What next?

Other language features

*e.g. closures, concurrency, ...*

Combine with existing  
verification tools

*e.g. RefinedC, Islaris, ...*

Investigate meta-level properties

*e.g. boundary cancellation, ...*

# What next?

Other language features

*e.g. closures, concurrency, ...*

Combine with existing  
verification tools

*e.g. RefinedC, Islaris, ...*

Investigate meta-level properties

*e.g. boundary cancellation, ...*

...

# Events

## Asm

Events  $\ni e ::= \text{Jump!}(r, m) \mid \text{Jump?}(r, m)$   
 $\mid \text{Syscall!}(v_1, v_2, m) \mid \text{SyscallRet?}(v, m)$

## Rec

Events  $\ni e ::= \text{Call!}(f, \bar{v}, m) \mid \text{Call?}(f, \bar{v}, m)$   
 $\mid \text{Return!}(\bar{v}, m) \mid \text{Return?}(\bar{v}, m)$

# Events

unstructured jumps vs. call and return

**Asm**

Events  $\ni e ::= \text{Jump!}(r, m) \mid \text{Jump?}(r, m)$   
 $\mid \text{Syscall!}(v_1, v_2, m) \mid \text{SyscallRet?}(v, m)$

**Rec**

Events  $\ni e ::= \text{Call!}(f, \bar{v}, m) \mid \text{Call?}(f, \bar{v}, m)$   
 $\mid \text{Return!}(\bar{v}, m) \mid \text{Return?}(\bar{v}, m)$

# Events

unstructured jumps vs. call and return

**Asm**

Events  $\ni e ::= \text{Jump!}(r, m) \mid \text{Jump?}(r, m)$   
 $\mid \text{Syscall!}(v_1, v_2, m) \mid \text{SyscallRet?}(v, m)$

system  
calls

**Rec**

Events  $\ni e ::= \text{Call!}(f, \bar{v}, m) \mid \text{Call?}(f, \bar{v}, m)$   
 $\mid \text{Return!}(\bar{v}, m) \mid \text{Return?}(\bar{v}, m)$

# Events

unstructured jumps vs. call and return

**Asm**

Events  $\ni e ::= \text{Jump}!(r, m) \mid \text{Jump}?(r, m)$   
 $\mid \text{Syscall}!(v_1, v_2, m) \mid \text{SyscallRet}?(v, m)$

Val  $\ni v \triangleq \mathbb{Z}$

**Rec**

Events  $\ni e ::= \text{Call}!(f, \bar{v}, m) \mid \text{Call}?(f, \bar{v}, m)$   
 $\mid \text{Return}!(\bar{v}, m) \mid \text{Return}?(v, m)$

Val  $\ni v ::= z : \mathbb{Z} \mid b : \mathbb{B} \mid \ell : \text{Loc}$

system  
calls



# Events

unstructured jumps vs. call and return

Asm

Events  $\ni e ::= \text{Jump}!(r, m) \mid \text{Jump}?(r, m)$

$\mid \text{Syscall}!(v_1, v_2, m) \mid \text{SyscallRet}?(v, m)$

system calls

Rec

Events  $\ni e ::= \text{Call}!(f, \bar{v}, m) \mid \text{Call}?(f, \bar{v}, m)$

$\mid \text{Return}!(\bar{v}, m) \mid \text{Return}?( \bar{v}, m)$

unstructured vs. structured values

Val  $\ni v \triangleq \mathbb{Z}$

Val  $\ni v ::= z : \mathbb{Z} \mid b : \mathbb{B} \mid \ell : \text{Loc}$

# Events

unstructured jumps vs. call and return

Asm

Events  $\ni e ::= \text{Jump}!(r, m) \mid \text{Jump}?(r, m)$

$\mid \text{Syscall}!(v_1, v_2, m) \mid \text{SyscallRet}?(v, m)$

system calls

Rec

Events  $\ni e ::= \text{Call}!(f, \bar{v}, m) \mid \text{Call}?(f, \bar{v}, m)$

$\mid \text{Return}!(\bar{v}, m) \mid \text{Return}?(v, m)$

Val  $\ni v \triangleq \mathbb{Z}$

unstructured vs. structured values

Val  $\ni v ::= z : \mathbb{Z} \mid b : \mathbb{B} \mid \ell : \text{Loc}$

Registers  $\ni r \triangleq \text{RegisterName} \rightarrow \text{Val}$

Memory  $\ni m \triangleq \mathbb{Z} \xrightarrow{\text{fin}} \text{Val} \cup \{\#\}$

Memory  $\ni m \triangleq \text{Loc} \xrightarrow{\text{fin}} \text{Val}$

Loc  $\ni \ell ::= \{\text{blockid} : \text{Id}, \text{offset} : \mathbb{Z}\}$

# Events

unstructured jumps vs. call and return

Asm

Events  $\ni e ::= \text{Jump}!(r, m) \mid \text{Jump}?(r, m)$

$\mid \text{Syscall}!(v_1, v_2, m) \mid \text{SyscallRet}?(v, m)$

system calls

Rec

Events  $\ni e ::= \text{Call}!(f, \bar{v}, m) \mid \text{Call}?(f, \bar{v}, m)$

$\mid \text{Return}!(\bar{v}, m) \mid \text{Return}?(v, m)$

unstructured vs. structured values

Val  $\ni v \triangleq \mathbb{Z}$

Val  $\ni v ::= z : \mathbb{Z} \mid b : \mathbb{B} \mid \ell : \text{Loc}$

Registers  $\ni r \triangleq \text{RegisterName} \rightarrow \text{Val}$

Memory  $\ni m \triangleq \mathbb{Z} \xrightarrow{\text{fin}} \text{Val} \cup \{\#\}$

Memory  $\ni m \triangleq \text{Loc} \xrightarrow{\text{fin}} \text{Val}$

Loc  $\ni \ell ::= \{\text{blockid} : \text{Id}, \text{offset} : \mathbb{Z}\}$

flat vs. block-based memory

# Specification

$\text{Spec}(E) \ni p ::=_{\text{coind}} \text{any} \mid \text{vis}(e); p \mid \text{assume}(\phi); p \mid \exists x : T; p(x) \mid \dots$

# Specification

$\text{Spec}(E) \ni p ::=_{\text{coind}} \text{any} \mid \text{vis}(e); p \mid \text{assume}(\phi); p \mid \exists x : T; p(x) \mid \dots$

$\text{onetwo}_{\text{spec}} \triangleq \exists \mathbf{r}, \mathbf{m}_0; \text{vis}(\text{Jump?}(\mathbf{r}, \mathbf{m}_0)); \text{assume}(\mathbf{r}(\text{pc}) = a_{\text{main}} \wedge \text{has\_stack}(\mathbf{r}(\text{sp}), \mathbf{m}_0));$   
 $\exists \mathbf{m}_1; \text{vis}(\text{Syscall!}(\text{PRINT}, 1, \mathbf{m}_1)); \exists \mathbf{m}_2; \text{vis}(\text{SyscallRet?}(*, \mathbf{m}_2)); \text{assume}(\mathbf{m}_2 = \mathbf{m}_1);$   
 $\text{vis}(\text{Syscall!}(\text{PRINT}, 2, *)); \text{vis}(\text{SyscallRet?}(*, *)); \text{any}$

# Specification

$\text{Spec}(E) \ni p ::=_{\text{coind}} \text{any} \mid \text{vis}(e); p \mid \text{assume}(\phi); p \mid \exists x : T; p(x) \mid \dots$

$\approx$  “Assuming the environment calls the main function, ...”

$\text{onetwo}_{\text{spec}} \triangleq \exists \mathbf{r}, \mathbf{m}_0; \text{vis}(\text{Jump?}(\mathbf{r}, \mathbf{m}_0)); \text{assume}(\mathbf{r}(\text{pc}) = a_{\text{main}} \wedge \text{has\_stack}(\mathbf{r}(\text{sp}), \mathbf{m}_0));$   
 $\exists \mathbf{m}_1; \text{vis}(\text{Syscall!}(\text{PRINT}, 1, \mathbf{m}_1)); \exists \mathbf{m}_2; \text{vis}(\text{SyscallRet?}(*, \mathbf{m}_2)); \text{assume}(\mathbf{m}_2 = \mathbf{m}_1);$   
 $\text{vis}(\text{Syscall!}(\text{PRINT}, 2, *)); \text{vis}(\text{SyscallRet?}(*, *)); \text{any}$

# Specification

$\text{Spec}(E) \ni p ::=_{\text{coind}} \text{any} \mid \text{vis}(e); p \mid \text{assume}(\phi); p \mid \exists x : T; p(x) \mid \dots$

$\approx$  “Assuming the environment calls the main function, ...”

$\text{onetwo}_{\text{spec}} \triangleq \exists r, m_0; \text{vis}(\text{Jump?}(r, m_0)); \text{assume}(r(\text{pc}) = a_{\text{main}} \wedge \text{has\_stack}(r(\text{sp}), m_0));$   
 $\quad \exists m_1; \text{vis}(\text{Syscall!}(\text{PRINT}, 1, m_1)); \exists m_2; \text{vis}(\text{SyscallRet?}(*, m_2)); \text{assume}(m_2 = m_1);$   
 $\quad \text{vis}(\text{Syscall!}(\text{PRINT}, 2, *)); \text{vis}(\text{SyscallRet?}(*, *)); \text{any}$

$\approx$  “... the program prints 1 and then 2.”

# Assembly verification $\text{PRINT-CORRECT } \llbracket \text{print} \rrbracket_a \preceq \llbracket \text{print}_{\text{spec}} \rrbracket_s$

Library `print`

```
print : mov x8, PRINT; syscall; ret
```

$\preceq$

$\text{print}_{\text{spec}} \stackrel{\Delta}{=}_{\text{coind}} \exists r, m; \text{vis}(\text{Jump?}(r, m)); \text{assume}(r(\text{pc}) = a_{\text{print}});$   
 $\text{vis}(\text{Syscall!}(\text{PRINT}, r(x0), m)); \exists v, m'; \text{vis}(\text{SyscallRet?}(v, m'));$   
 $\text{vis}(\text{Jump!}(r[\text{pc} \mapsto r(x30)][x0 \mapsto v][x8 \mapsto *], m'));$   $\text{print}_{\text{spec}}$



# Assembly verification $\text{PRINT-CORRECT } \llbracket \text{print} \rrbracket_a \leq \llbracket \text{print}_{\text{spec}} \rrbracket_s$

<p>Library <code>print</code></p> <p><code>print</code> : <code>mov x8, PRINT; syscall; ret</code></p>
--

When the environment jumps to `print`, ...

$\leq$

`print`<sub>spec</sub>  $\stackrel{\Delta_{\text{coind}}}{=} \exists r, m; \text{vis}(\text{Jump?}(r, m)); \text{assume}(r(\text{pc}) = a_{\text{print}});$   
 $\text{vis}(\text{Syscall!}(\text{PRINT}, r(x0), m)); \exists v, m'; \text{vis}(\text{SyscallRet?}(v, m'));$   
 $\text{vis}(\text{Jump!}(r[\text{pc} \mapsto r(x30)][x0 \mapsto v][x8 \mapsto *], m'));$  `print`<sub>spec</sub>

# Assembly verification $\text{PRINT-CORRECT } \llbracket \text{print} \rrbracket_a \leq \llbracket \text{print}_{\text{spec}} \rrbracket_s$

Library `print`

`print` : `mov x8, PRINT; syscall; ret`

When the environment jumps to `print`, ...

$$\text{print}_{\text{spec}} \stackrel{\Delta_{\text{coind}}}{=} \exists r, m; \text{vis}(\text{Jump?}(r, m)); \text{assume}(r(\text{pc}) = a_{\text{print}});$$

$$\text{vis}(\text{Syscall!}(\text{PRINT}, r(x0), m)); \exists v, m'; \text{vis}(\text{SyscallRet?}(v, m'));$$

$$\text{vis}(\text{Jump!}(r[\text{pc} \mapsto r(x30)][x0 \mapsto v][x8 \mapsto *], m')); \text{print}_{\text{spec}}$$

... invoke `PRINT` syscall with argument `r(x0)` ...

# Assembly verification $\text{PRINT-CORRECT } \llbracket \text{print} \rrbracket_a \leq \llbracket \text{print}_{\text{spec}} \rrbracket_s$

Library `print`

`print` : `mov x8, PRINT; syscall; ret`

When the environment jumps to `print`, ...

$$\text{print}_{\text{spec}} \stackrel{\Delta_{\text{coind}}}{=} \exists r, m; \text{vis}(\text{Jump?}(r, m)); \text{assume}(r(\text{pc}) = a_{\text{print}});$$

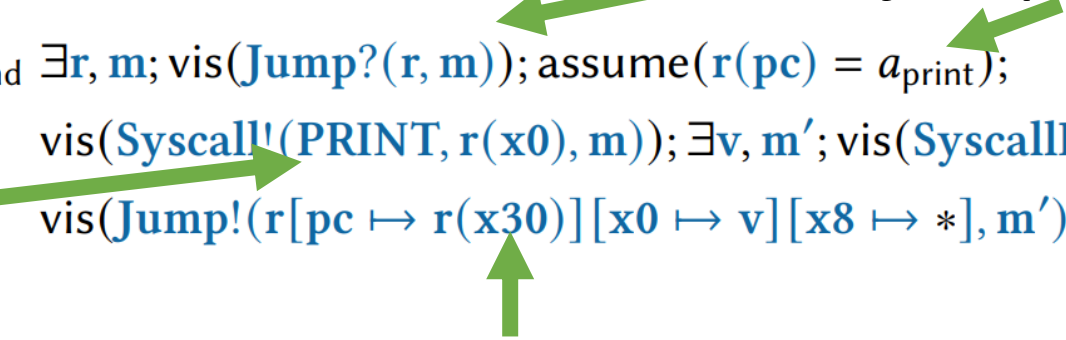
$$\text{vis}(\text{Syscall!}(\text{PRINT}, r(x0), m)); \exists v, m'; \text{vis}(\text{SyscallRet?}(v, m'));$$

$$\text{vis}(\text{Jump!}(r[\text{pc} \mapsto r(x30)][x0 \mapsto v][x8 \mapsto *], m')); \text{print}_{\text{spec}}$$

... invoke `PRINT` syscall with argument `r(x0)` ...

..., and jump to return address `r(x30)`.

$\leq$



Assembly verification    PRINT-CORRECT  $\llbracket \text{print} \rrbracket_a \leq \llbracket \text{print}_{\text{spec}} \rrbracket_s$   
 register with syscall id

```
Library print
print : mov x8, PRINT; syscall; ret
```

When the environment jumps to **print**, ...

$$\text{print}_{\text{spec}} \stackrel{\Delta_{\text{coind}}}{=} \exists r, m; \text{vis}(\text{Jump?}(r, m)); \text{assume}(r(\text{pc}) = a_{\text{print}});$$

$$\text{vis}(\text{Syscall!}(\text{PRINT}, r(x0), m)); \exists v, m'; \text{vis}(\text{SyscallRet?}(v, m'));$$

$$\text{vis}(\text{Jump!}(r[\text{pc} \mapsto r(x30)][x0 \mapsto v][x8 \mapsto *], m')); \text{print}_{\text{spec}}$$

... invoke **PRINT** syscall with argument **r(x0)** ...

..., and jump to return address **r(x30)**.

# Assembly verification

$$\llbracket \text{locle} \rrbracket_a \preceq \lceil \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$$

Library `locle`

```
locle : sle x0, x0, x1; ret
```

# Assembly verification

$$\llbracket \text{locle} \rrbracket_a \preceq \lceil \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

Library `locle`

`locle : sle x0, x0, x1; ret`

# Assembly verification

$$\llbracket \text{locle} \rrbracket_a \preceq \lceil \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

Library <code>locle</code>	<code>locle : sle x0, x0, x1; ret</code>
----------------------------	--

$\preceq$

$$\lceil \text{locle}_{\text{spec}} \stackrel{\Delta}{=}_{\text{coind}} \exists f, \bar{v}, m; \text{vis}(\text{Call?}(f, \bar{v}, m)); \text{assume}(f = \text{locle}); \text{assume}(\bar{v} \text{ is } [\ell_1, \ell_2]); \\ \text{if } \ell_1.\text{blockid} = \ell_2.\text{blockid} \text{ then vis}(\text{Return!}(\ell_1.\text{offset} \leq \ell_2.\text{offset}, m)); \text{locle}_{\text{spec}} \rceil_{r \Rightarrow a} \\ \text{else } \exists b; \text{vis}(\text{Return!}(b, m)); \text{locle}_{\text{spec}} \rceil_{r \Rightarrow a} \rceil_{r \Rightarrow a}$$

# Assembly verification

$$\llbracket \text{locle} \rrbracket_a \leq \lceil \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

Library `locle`

`locle : sle x0, x0, x1; ret`

“When the environment calls `locle` with  $\ell_1$  and  $\ell_2$ , ...  $\leq$

$$\left[ \begin{array}{l} \text{locle}_{\text{spec}} \stackrel{\Delta}{=}_{\text{coind}} \exists f, \bar{v}, m; \text{vis}(\text{Call?}(f, \bar{v}, m)); \text{assume}(f = \text{locle}); \text{assume}(\bar{v} \text{ is } [\ell_1, \ell_2]); \\ \text{if } \ell_1.\text{blockid} = \ell_2.\text{blockid} \text{ then } \text{vis}(\text{Return!}(\ell_1.\text{offset} \leq \ell_2.\text{offset}, m)); \text{locle}_{\text{spec}} \\ \text{else } \exists b; \text{vis}(\text{Return!}(b, m)); \text{locle}_{\text{spec}} \end{array} \right]_{r \Rightarrow a}$$



# Assembly verification

$$\llbracket \text{locle} \rrbracket_a \leq \lceil \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

Library `locle`

`locle : sle x0, x0, x1; ret`

“When the environment calls `locle` with  $\ell_1$  and  $\ell_2$ , ...  $\leq$

$$\left[ \begin{array}{l} \text{locle}_{\text{spec}} \stackrel{\Delta}{=}_{\text{coind}} \exists f, \bar{v}, m; \text{vis}(\text{Call?}(f, \bar{v}, m)); \text{assume}(f = \text{locle}); \text{assume}(\bar{v} \text{ is } [\ell_1, \ell_2]); \\ \text{if } \ell_1.\text{blockid} = \ell_2.\text{blockid} \text{ then vis}(\text{Return!}(\ell_1.\text{offset} \leq \ell_2.\text{offset}, m)); \text{locle}_{\text{spec}} \\ \text{else } \exists b; \text{vis}(\text{Return!}(b, m)); \text{locle}_{\text{spec}} \end{array} \right]_{r \Rightarrow a}$$

# Assembly verification

$$[[\text{locle}]]_a \leq \lceil [[\text{locle}_{\text{spec}}]]_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

```
Library locle  
locle : sle x0, x0, x1; ret
```

“When the environment calls **locle** with  $\ell_1$  and  $\ell_2$ , ...  $\leq$

$$\lceil \text{locle}_{\text{spec}} \stackrel{\Delta}{=}_{\text{coind}} \exists f, \bar{v}, m; \text{vis}(\text{Call?}(f, \bar{v}, m)); \text{assume}(f = \text{locle}); \text{assume}(\bar{v} \text{ is } [\ell_1, \ell_2]); \\ \text{if } \ell_1.\text{blockid} = \ell_2.\text{blockid} \text{ then vis}(\text{Return!}(\ell_1.\text{offset} \leq \ell_2.\text{offset}, m)); \text{locle}_{\text{spec}} \rceil_{r \Rightarrow a} \\ \text{else } \exists b; \text{vis}(\text{Return!}(b, m)); \text{locle}_{\text{spec}}$$

# Assembly verification

$$[[\text{locle}]]_a \leq \lceil [[\text{locle}_{\text{spec}}]]_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

```
Library locle  
locle : sle x0, x0, x1; ret
```

“When the environment calls **locle** with  $\ell_1$  and  $\ell_2$ , ...  $\leq$

$$\lceil \text{locle}_{\text{spec}} \stackrel{\Delta}{=}_{\text{coind}} \exists f, \bar{v}, m; \text{vis}(\text{Call?}(f, \bar{v}, m)); \text{assume}(f = \text{locle}); \text{assume}(\bar{v} \text{ is } [\ell_1, \ell_2]); \\ \text{if } \ell_1.\text{blockid} = \ell_2.\text{blockid} \text{ then vis}(\text{Return!}(\ell_1.\text{offset} \leq \ell_2.\text{offset}, m)); \text{locle}_{\text{spec}} \rceil_{r \Rightarrow a} \\ \text{else } \exists b; \text{vis}(\text{Return!}(b, m)); \text{locle}_{\text{spec}} \rceil_{r \Rightarrow a}$$

# Assembly verification

$$\llbracket \text{locle} \rrbracket_a \leq \lceil \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

Library **locle**

locle : sle x0, x0, x1; ret

“When the environment calls **locle** with  $\ell_1$  and  $\ell_2$ , ...  $\leq$

$$\left[ \begin{array}{l} \text{locle}_{\text{spec}} \stackrel{\Delta}{=}_{\text{coind}} \exists f, \bar{v}, m; \text{vis}(\text{Call?}(f, \bar{v}, m)); \text{assume}(f = \text{locle}); \text{assume}(\bar{v} \text{ is } [\ell_1, \ell_2]); \\ \text{if } \ell_1.\text{blockid} = \ell_2.\text{blockid} \text{ then vis}(\text{Return!}(\ell_1.\text{offset} \leq \ell_2.\text{offset}, m)); \text{locle}_{\text{spec}} \end{array} \right]_{r \Rightarrow a}$$

... if the blocks are equal,  
compare the offsets, ...

# Assembly verification

$$\llbracket \text{locle} \rrbracket_a \leq \lceil \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

```
Library locle  
locle : sle x0, x0, x1; ret
```

“When the environment calls **locle** with  $\ell_1$  and  $\ell_2$ , ...  $\leq$

$$\left[ \begin{array}{l} \text{locle}_{\text{spec}} \stackrel{\Delta}{=}_{\text{coind}} \exists f, \bar{v}, m; \text{vis}(\text{Call?}(f, \bar{v}, m)); \text{assume}(f = \text{locle}); \text{assume}(\bar{v} \text{ is } [\ell_1, \ell_2]); \\ \text{if } \ell_1.\text{blockid} = \ell_2.\text{blockid} \text{ then vis}(\text{Return!}(\ell_1.\text{offset} \leq \ell_2.\text{offset}, m)); \text{locle}_{\text{spec}} \\ \text{else } \exists b; \text{vis}(\text{Return!}(b, m)); \text{locle}_{\text{spec}} \end{array} \right]_{r \Rightarrow a}$$

... if the blocks are equal,  
compare the offsets, ...

# Assembly verification

$$\llbracket \text{locle} \rrbracket_a \leq \lceil \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

```
Library locle  
locle : sle x0, x0, x1; ret
```

“When the environment calls **locle** with  $\ell_1$  and  $\ell_2$ , ...  $\leq$

$$\left[ \begin{array}{l} \text{locle}_{\text{spec}} \stackrel{\Delta}{=}_{\text{coind}} \exists f, \bar{v}, m; \text{vis}(\text{Call?}(f, \bar{v}, m)); \text{assume}(f = \text{locle}); \text{assume}(\bar{v} \text{ is } [\ell_1, \ell_2]); \\ \text{if } \ell_1.\text{blockid} = \ell_2.\text{blockid} \text{ then vis}(\text{Return!}(\ell_1.\text{offset} \leq \ell_2.\text{offset}, m)); \text{locle}_{\text{spec}} \\ \text{else } \exists b; \text{vis}(\text{Return!}(b, m)); \text{locle}_{\text{spec}} \end{array} \right]_{r \Rightarrow a}$$

... if the blocks are equal,  
compare the offsets, ...

# Assembly verification

$$\llbracket \text{locle} \rrbracket_a \leq \lceil \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

```
Library locle  
locle : sle x0, x0, x1; ret
```

“When the environment calls **locle** with  $\ell_1$  and  $\ell_2$ , ...  $\leq$

$$\left[ \begin{array}{l} \text{locle}_{\text{spec}} \stackrel{\Delta}{=}_{\text{coind}} \exists f, \bar{v}, m; \text{vis}(\text{Call?}(f, \bar{v}, m)); \text{assume}(f = \text{locle}); \text{assume}(\bar{v} \text{ is } [\ell_1, \ell_2]); \\ \text{if } \ell_1.\text{blockid} = \ell_2.\text{blockid} \text{ then vis}(\text{Return!}(\ell_1.\text{offset} \leq \ell_2.\text{offset}, m)); \text{locle}_{\text{spec}} \\ \text{else } \exists b; \text{vis}(\text{Return!}(b, m)); \text{locle}_{\text{spec}} \end{array} \right]_{r \Rightarrow a}$$

... if the blocks are equal,  
compare the offsets, ...

... otherwise return a  
non-deterministic Boolean.”

# Assembly verification

$$\llbracket \text{locle} \rrbracket_a \leq \lceil \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

```
Library locle  
locle : sle x0, x0, x1; ret
```

“When the environment calls **locle** with  $\ell_1$  and  $\ell_2$ , ...  $\leq$

$$\left[ \begin{array}{l} \text{locle}_{\text{spec}} \stackrel{\Delta}{=}_{\text{coind}} \exists f, \bar{v}, m; \text{vis}(\text{Call?}(f, \bar{v}, m)); \text{assume}(f = \text{locle}); \text{assume}(\bar{v} \text{ is } [\ell_1, \ell_2]); \\ \text{if } \ell_1.\text{blockid} = \ell_2.\text{blockid} \text{ then vis}(\text{Return!}(\ell_1.\text{offset} \leq \ell_2.\text{offset}, m)); \text{locle}_{\text{spec}} \\ \text{else } \exists b; \text{vis}(\text{Return!}(b, m)); \text{locle}_{\text{spec}} \end{array} \right]_{r \Rightarrow a}$$

... if the blocks are equal,  
compare the offsets, ...

... otherwise return a  
non-deterministic Boolean.”



# Assembly verification

$$\llbracket \text{locle} \rrbracket_a \leq \lceil \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

```
Library locle  
locle : sle x0, x0, x1; ret
```

“When the environment calls **locle** with  $\ell_1$  and  $\ell_2$ , ...  $\leq$

$$\lceil \text{locle}_{\text{spec}} \stackrel{\Delta}{=}_{\text{coind}} \exists f, \bar{v}, m; \text{vis}(\text{Call?}(f, \bar{v}, m)); \text{assume}(f = \text{locle}); \text{assume}(\bar{v} \text{ is } [\ell_1, \ell_2]); \\ \text{if } \ell_1.\text{blockid} = \ell_2.\text{blockid} \text{ then vis}(\text{Return!}(\ell_1.\text{offset} \leq \ell_2.\text{offset}, m)); \text{locle}_{\text{spec}} \rceil_{r \Rightarrow a} \rceil$$

... if the blocks are equal,  
compare the offsets, ...

... otherwise return a  
non-deterministic Boolean.”

# Assembly verification

$$\llbracket \text{locle} \rrbracket_a \leq \lceil \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$$

“set if less or equal”

```
Library locle  
locle : sle x0, x0, x1; ret
```

“When the environment calls **locle** with  $\ell_1$  and  $\ell_2$ , ...  $\leq$

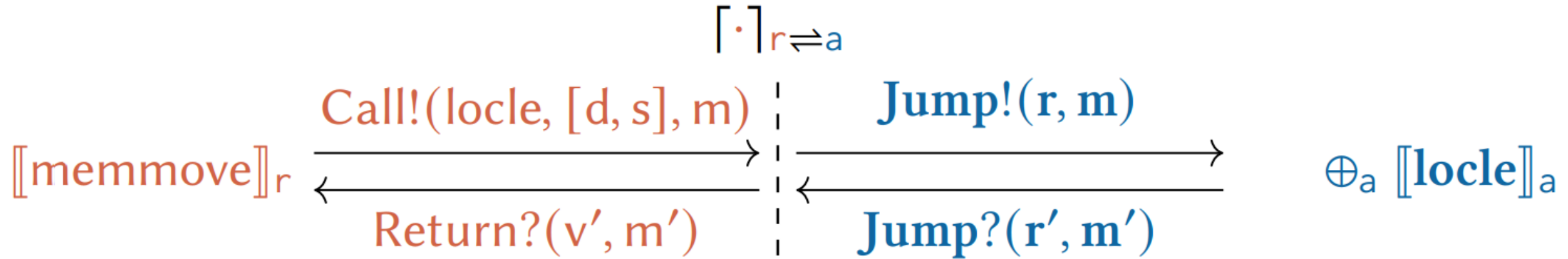
$$\left[ \begin{array}{l} \text{locle}_{\text{spec}} \triangleq_{\text{coind}} \exists f, \bar{v}, m; \text{vis}(\text{Call?}(f, \bar{v}, m)); \text{assume}(f = \text{locle}); \text{assume}(\bar{v} \text{ is } [\ell_1, \ell_2]); \\ \text{if } \ell_1.\text{blockid} = \ell_2.\text{blockid} \text{ then vis}(\text{Return!}(\ell_1.\text{offset} \leq \ell_2.\text{offset}, m)); \text{locle}_{\text{spec}} \\ \text{else } \exists b; \text{vis}(\text{Return!}(b, m)); \text{locle}_{\text{spec}} \end{array} \right]_{r \Rightarrow a}$$

... otherwise return a non-deterministic Boolean.”

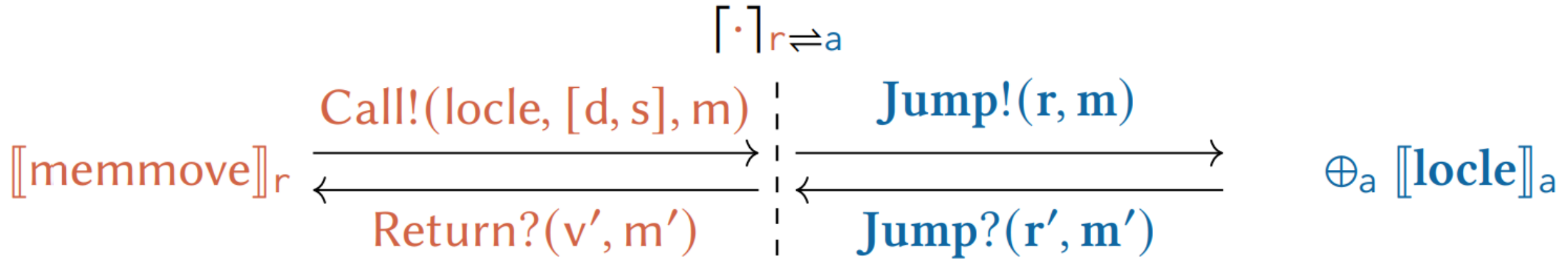
... if the blocks are equal, compare the offsets, ...

No syntactic Rec program required!

$\llbracket \cdot \rrbracket_{r \rightleftharpoons a}$  wrapper



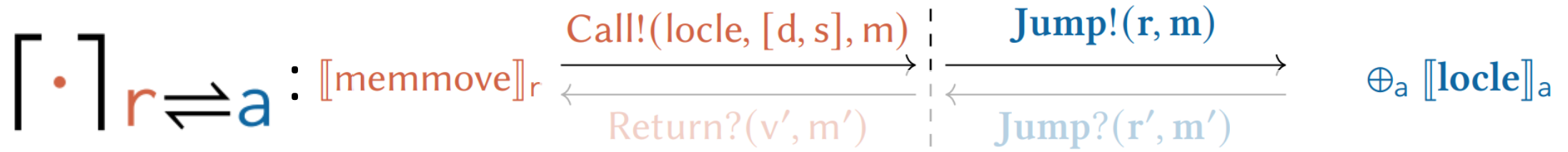
$\llbracket \cdot \rrbracket_{r \rightleftharpoons a}$  wrapper



fn memmove( $d, s, n$ )  $\triangleq$

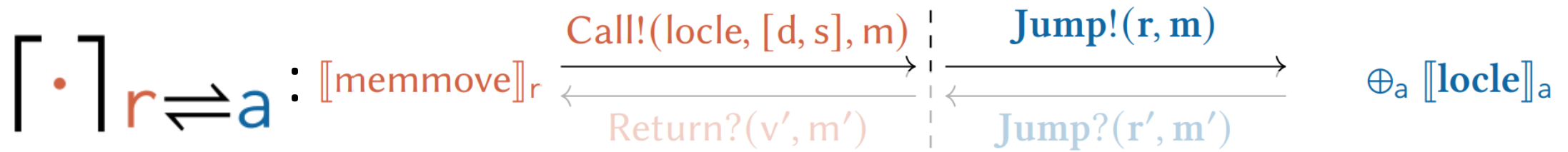
if locle( $d, s$ ) then memcpy( $d, s, n, 1$ ) else memcpy( $d+n-1, s+n-1, n, -1$ )





Translating values:

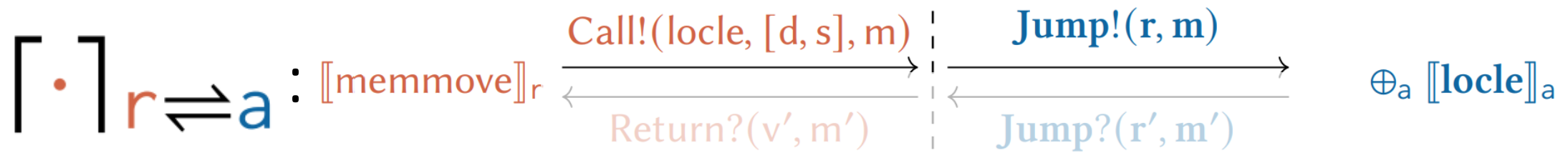
$$z \sim_w z \quad b \sim_w (\text{if } b \text{ then } 1 \text{ else } 0) \quad \ell \sim_w w(\ell.\text{blockid}) + \ell.\text{offset}$$



Translating values:

$$z \sim_w z \quad b \sim_w (\text{if } b \text{ then } 1 \text{ else } 0) \quad \ell \sim_w w(\ell.\text{blockid}) + \ell.\text{offset}$$

Kripke world: map from block ids  
to base addresses

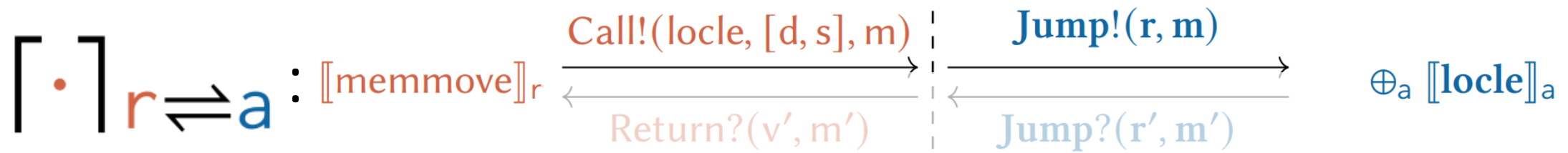


Translating values:

$$z \sim_w z \quad b \sim_w (\text{if } b \text{ then } 1 \text{ else } 0) \quad \ell \sim_w w(\ell.\text{blockid}) + \ell.\text{offset}$$

Kripke world: map from block ids  
to base addresses

$$\text{Call!}(f, \bar{v}, m) \rightarrow_w \text{Jump!}(r, m) \triangleq r(\text{pc}) = a_f \wedge \bar{v} \sim_w r(x0 \dots x8) \wedge m \sim_w m$$



Translating values:

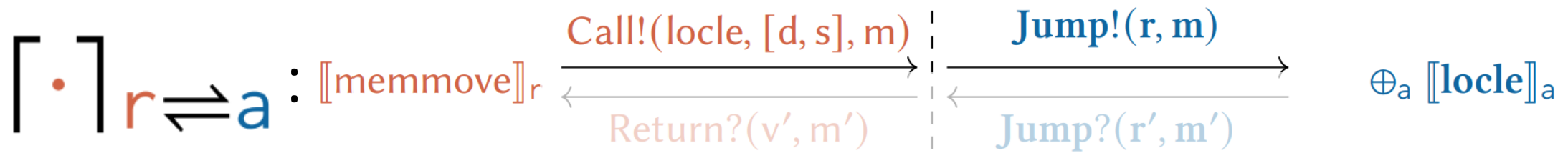
$$z \sim_w z \quad b \sim_w (\text{if } b \text{ then } 1 \text{ else } 0) \quad \ell \sim_w w(\ell.\text{blockid}) + \ell.\text{offset}$$

Kripke world: map from block ids  
to base addresses

$$\text{Call!}(f, \bar{v}, m) \rightarrow_w \text{Jump!}(r, m) \triangleq r(\text{pc}) = a_f \wedge \bar{v} \sim_w r(x0 \dots x8) \wedge m \sim_w m$$

PC contains address  
of the function





Translating values:

$$z \sim_w z \quad b \sim_w (\text{if } b \text{ then } 1 \text{ else } 0) \quad \ell \sim_w w(\ell.\text{blockid}) + \ell.\text{offset}$$

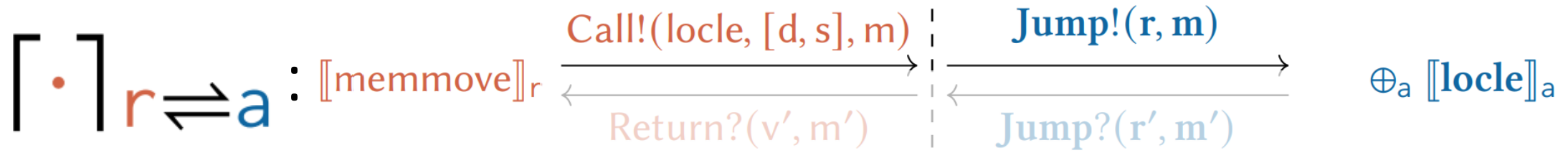
Kripke world: map from block ids to base addresses

Translating events:

$$\text{Call!}(f, \bar{v}, m) \rightarrow_w \text{Jump!}(r, m) \triangleq r(\text{pc}) = a_f \wedge \bar{v} \sim_w r(x0 \dots x8) \wedge m \sim_w m$$

PC contains address of the function

Rec arguments are related to Asm argument registers



Translating values:

$$z \sim_w z \quad b \sim_w (\text{if } b \text{ then } 1 \text{ else } 0) \quad \ell \sim_w w(\ell.\text{blockid}) + \ell.\text{offset}$$

Translating events:

Kripke world: map from block ids to base addresses

$$\text{Call!(f, } \bar{v}, m) \rightarrow_w \text{Jump!(r, m)} \triangleq r(\text{pc}) = a_f \wedge \bar{v} \sim_w r(x0 \dots x8) \wedge m \sim_w m$$

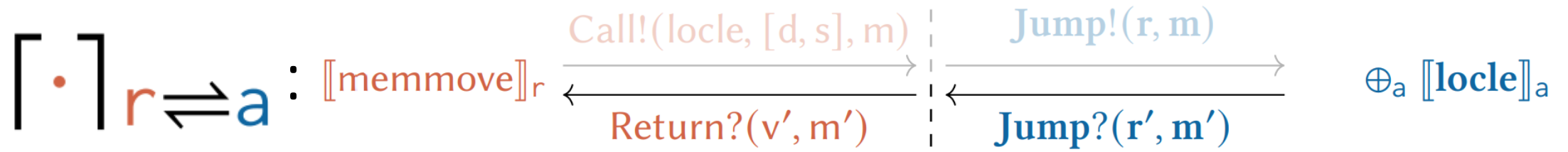
PC contains address of the function

Rec arguments are related to Asm argument registers

memories are related

$$\llbracket \cdot \rrbracket_r \rightrightarrows_a : \llbracket \text{memmove} \rrbracket_r \begin{array}{c} \xrightarrow{\text{Call!}(\text{locle}, [d, s], m)} \\ \xleftarrow{\text{Return?}(v', m')} \end{array} \Big| \begin{array}{c} \xrightarrow{\text{Jump!}(r, m)} \\ \xleftarrow{\text{Jump?}(r', m')} \end{array} \oplus_a \llbracket \text{locle} \rrbracket_a$$

$$\begin{aligned} \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \cup_a \downarrow \text{memmove} \cup_a \text{locle} \cup_a \text{print} \rrbracket_a \\ &\preceq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\ &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\ &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_s \\ &\preceq \llbracket \llbracket \text{main} \rrbracket_r \oplus_r \llbracket \text{memmove} \rrbracket_r \oplus_r \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_s \\ &\preceq \llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_s \\ &\preceq \llbracket \llbracket \text{main}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_s \\ &\preceq \llbracket \text{onetwo}_{\text{spec}} \rrbracket_s \end{aligned}$$



In  $\llbracket \downarrow \text{memmove} \rrbracket_a \preceq \lceil \llbracket \text{memmove} \rrbracket_r \rceil_{r \rightleftarrows a}$ , consider **locle** returning 0:

$$\llbracket \downarrow \text{memmove} \rrbracket_a \xleftarrow{\text{Jump?(r(x0) = 0, \dots, m)}}$$

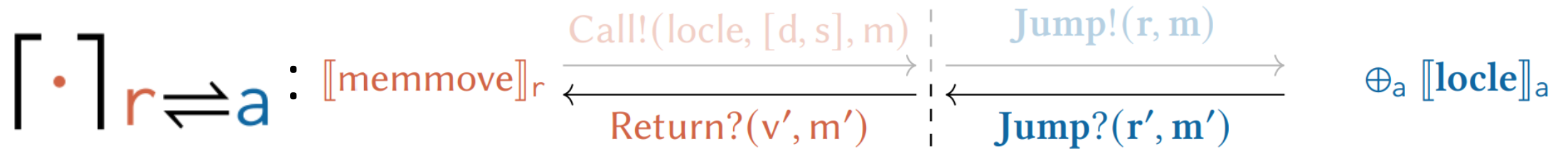
$$\lceil \cdot \rceil_{r \rightleftarrows a} : \llbracket \text{memmove} \rrbracket_r \xrightarrow{\text{Call!(locle, [d, s], m)}} \text{---} \xrightarrow{\text{Jump!(r, m)}} \oplus_a \llbracket \text{locle} \rrbracket_a$$

$$\xleftarrow{\text{Return?(v', m')}} \text{---} \xleftarrow{\text{Jump?(r', m')}}$$

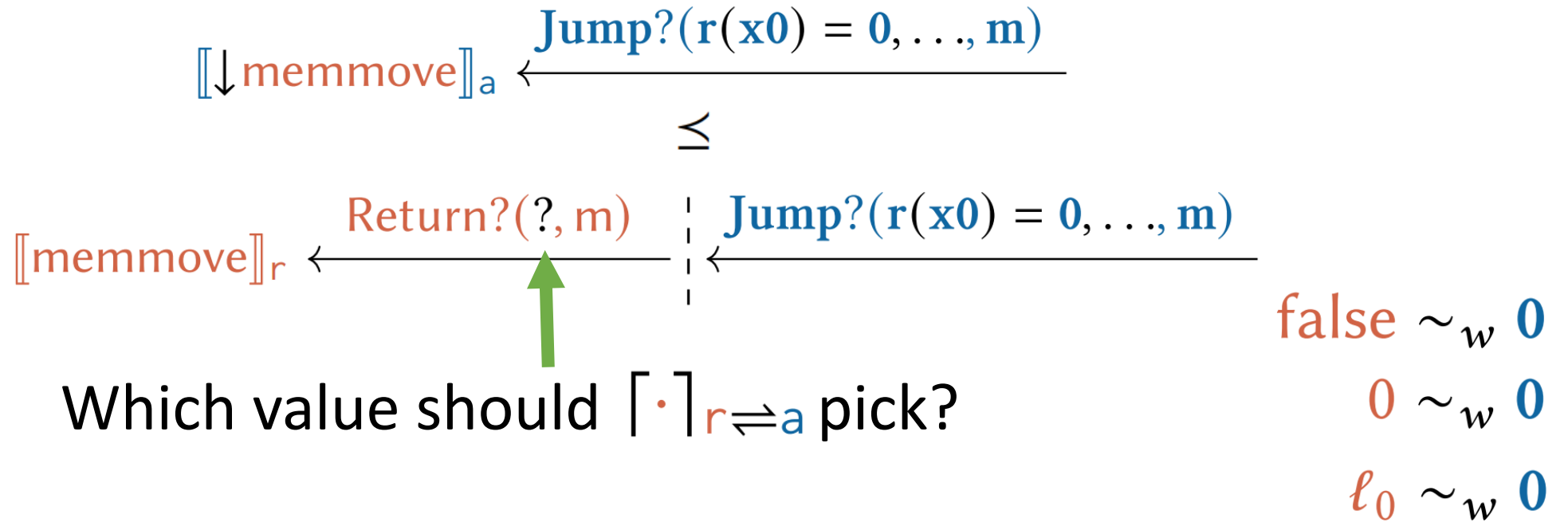
In  $\llbracket \downarrow \text{memmove} \rrbracket_a \preceq \lceil \llbracket \text{memmove} \rrbracket_r \rceil_{r \rightleftarrows a}$ , consider **locle** returning 0:

$$\llbracket \downarrow \text{memmove} \rrbracket_a \xleftarrow{\text{Jump?(r(x0) = 0, \dots, m)}} \preceq$$

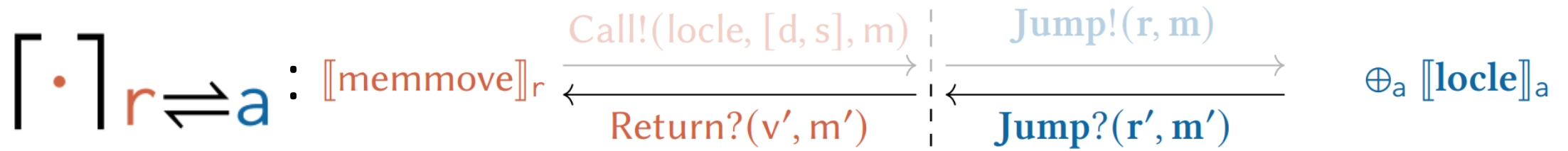
$$\llbracket \text{memmove} \rrbracket_r \xleftarrow{\text{Return?(?, m)}} \text{---} \xleftarrow{\text{Jump?(r(x0) = 0, \dots, m)}}$$



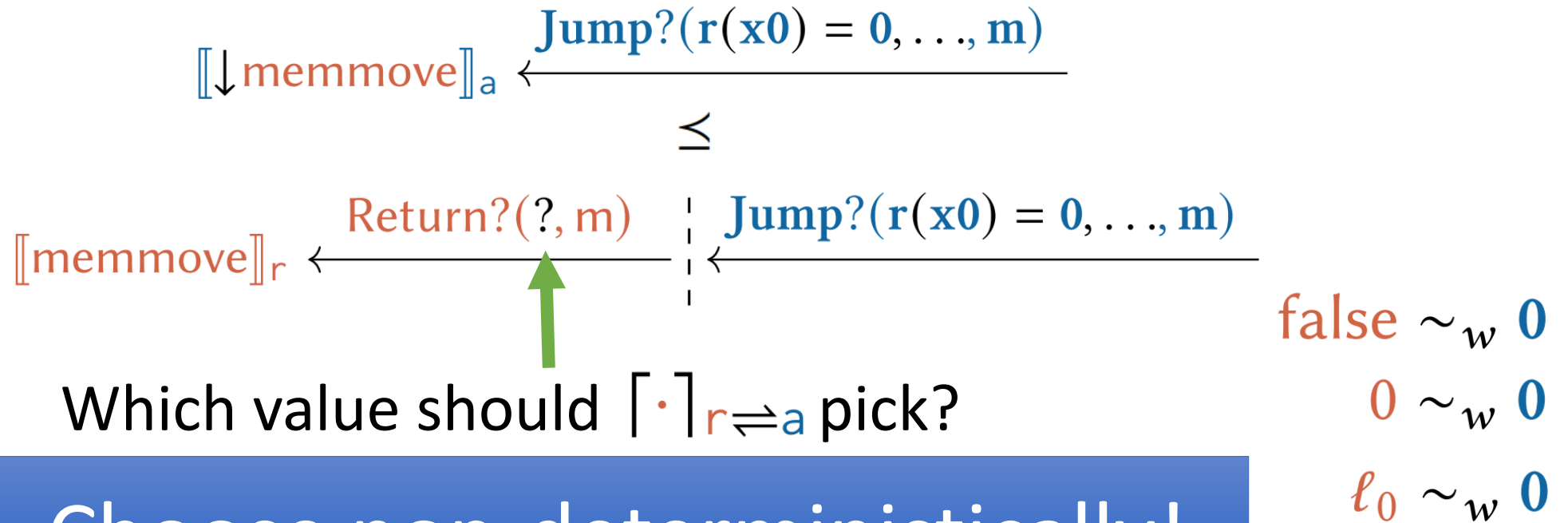
In  $\llbracket \downarrow \text{memmove} \rrbracket_a \preceq \lceil \llbracket \text{memmove} \rrbracket_r \rceil_{r \rightleftharpoons a}$ , consider **locle** returning 0:

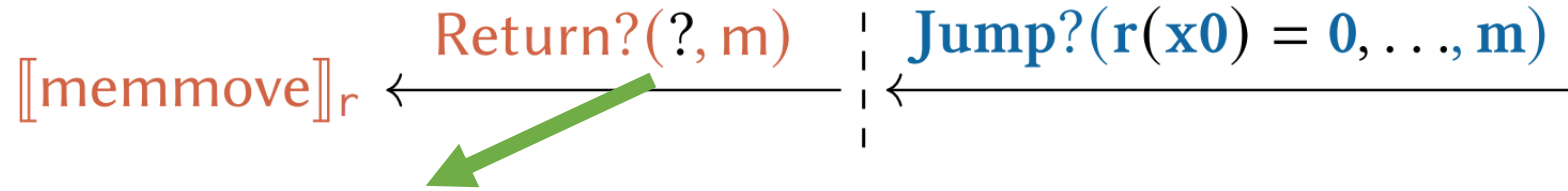
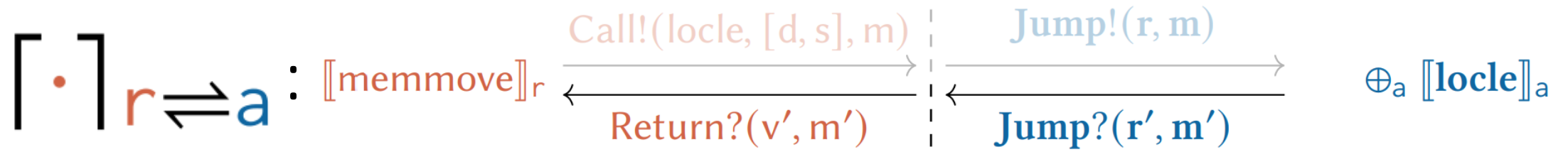


Which value should  $\lceil \cdot \rceil_{r \rightleftharpoons a}$  pick?



In  $\llbracket \downarrow \text{memmove} \rrbracket_a \preceq \lceil \llbracket \text{memmove} \rrbracket_r \rceil_{r \rightleftharpoons a}$ , consider `locle` returning 0:



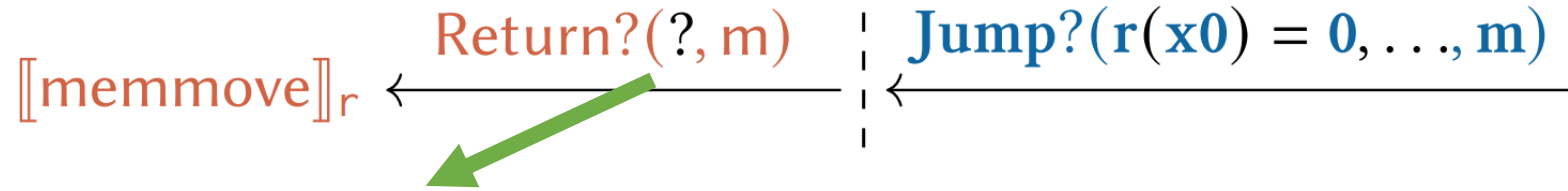
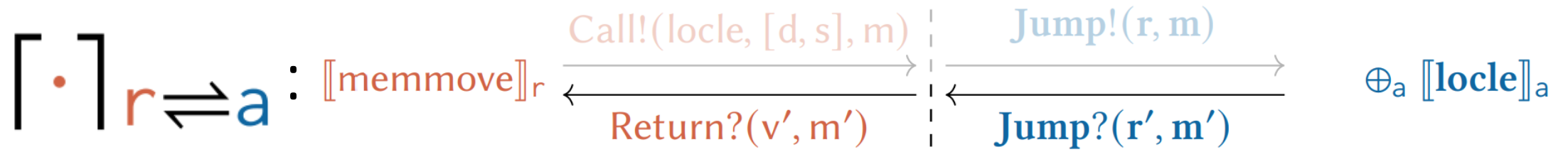


*Demonic non-determinism*

“ $\exists v. v \sim_w \mathbf{r}(\mathbf{x0}) \wedge \dots$ ”


$\text{false} \sim_w \mathbf{0} \quad \mathbf{0} \sim_w \mathbf{0} \quad \ell_0 \sim_w \mathbf{0}$

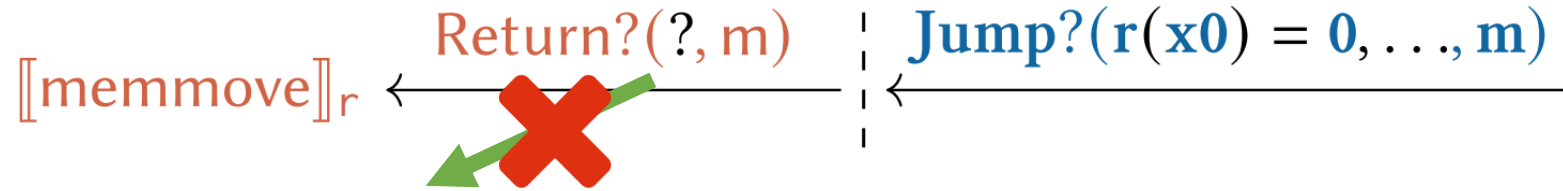
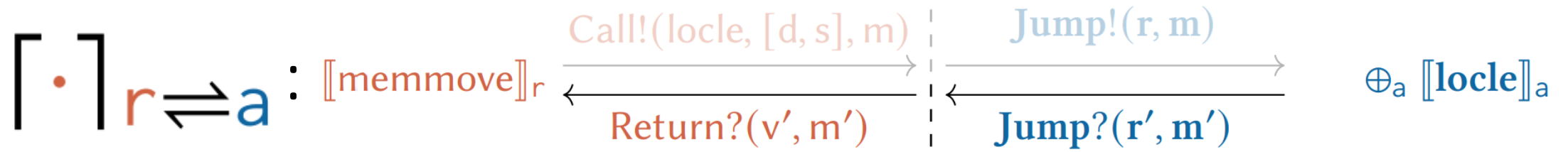




*Demonic non-determinism*

“ $\exists v. v \sim_w \mathbf{r}(\mathbf{x0}) \wedge \dots$ ”

$\text{false} \sim_w \mathbf{0} \quad \mathbf{0} \sim_w \mathbf{0} \quad \ell_0 \sim_w \mathbf{0}$   


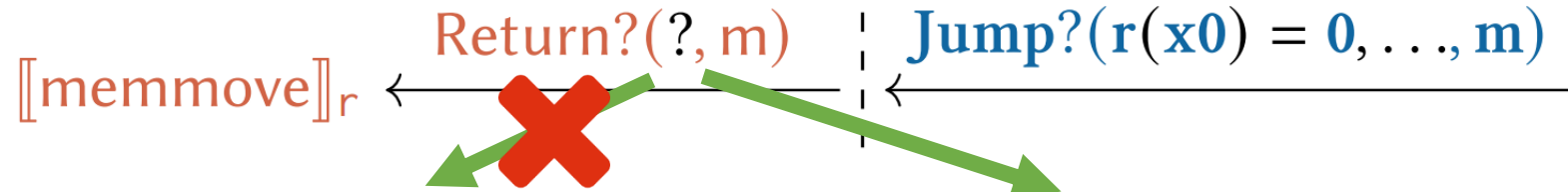
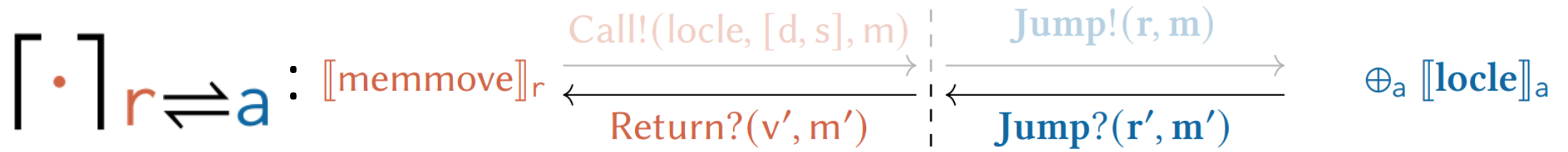


*Demonic non-determinism*

“ $\exists v. v \sim_w r(\mathbf{x0}) \wedge \dots$ ”

$\text{false} \sim_w \mathbf{0} \quad \mathbf{0} \sim_w \mathbf{0} \quad \ell_0 \sim_w \mathbf{0}$





*Demonic non-determinism*

*Angelic non-determinism*

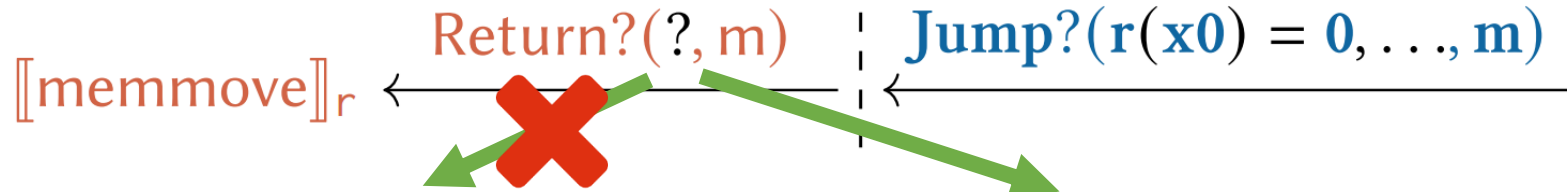
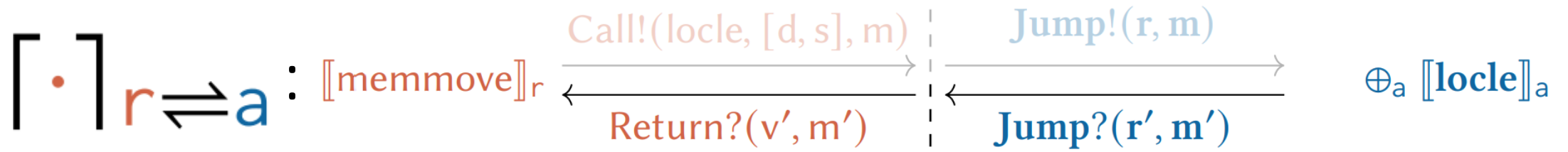
“ $\exists v. v \sim_w \mathbf{r}(\mathbf{x0}) \wedge \dots$ ”

“ $\forall v. v \sim_w \mathbf{r}(\mathbf{x0}) \Rightarrow \dots$ ”

false  $\sim_w \mathbf{0}$    0  $\sim_w \mathbf{0}$     $\ell_0 \sim_w \mathbf{0}$

false  $\sim_w \mathbf{0}$    0  $\sim_w \mathbf{0}$     $\ell_0 \sim_w \mathbf{0}$





*Demonic non-determinism*

*Angelic non-determinism*

“ $\exists v. v \sim_w \mathbf{r}(\mathbf{x0}) \wedge \dots$ ”

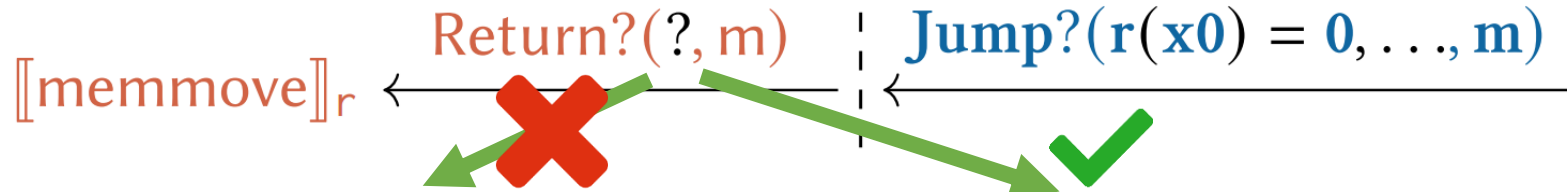
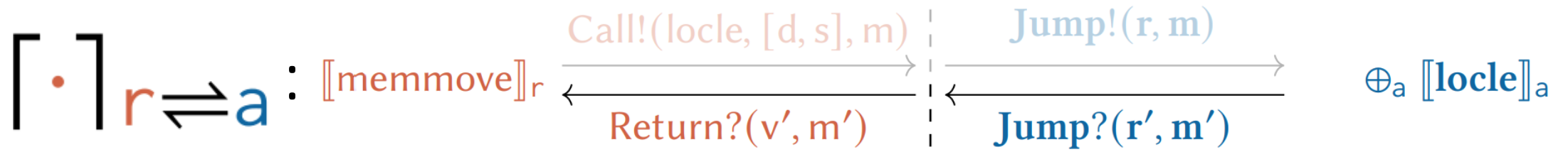
“ $\forall v. v \sim_w \mathbf{r}(\mathbf{x0}) \Rightarrow \dots$ ”

false  $\sim_w \mathbf{0}$    0  $\sim_w \mathbf{0}$     $\ell_0 \sim_w \mathbf{0}$



false  $\sim_w \mathbf{0}$    0  $\sim_w \mathbf{0}$     $\ell_0 \sim_w \mathbf{0}$





*Demonic non-determinism*

*Angelic non-determinism*

“ $\exists v. v \sim_w \mathbf{r}(\mathbf{x0}) \wedge \dots$ ”

“ $\forall v. v \sim_w \mathbf{r}(\mathbf{x0}) \Rightarrow \dots$ ”

false  $\sim_w \mathbf{0}$    0  $\sim_w \mathbf{0}$     $\ell_0 \sim_w \mathbf{0}$



false  $\sim_w \mathbf{0}$    0  $\sim_w \mathbf{0}$     $\ell_0 \sim_w \mathbf{0}$



$$\llbracket \cdot \rrbracket_r \stackrel{\text{a}}{\rightleftharpoons} \llbracket \text{memmove} \rrbracket_r : \llbracket \text{memmove} \rrbracket_r \xrightarrow{\text{Call!}(\text{locle}, [d, s], m)} \text{Jump!}(r, m) \quad \text{Jump!}(r, m) \xrightarrow{\text{Jump!}(r, m)} \oplus_a \llbracket \text{locle} \rrbracket_a$$

$$\xleftarrow{\text{Return?}(v', m')} \quad \xleftarrow{\text{Jump?}(r', m')}$$

$$\begin{aligned} \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \cup_a \downarrow \text{memmove} \cup_a \text{locle} \cup_a \text{print} \rrbracket_a \\ &\preceq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\ &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\ &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_s \\ &\preceq \llbracket \llbracket \text{main} \rrbracket_r \oplus_r \llbracket \text{memmove} \rrbracket_r \oplus_r \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_s \\ &\preceq \llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_s \\ &\preceq \llbracket \llbracket \text{main}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_s \\ &\preceq \llbracket \text{onetwo}_{\text{spec}} \rrbracket_s \end{aligned}$$

$$\llbracket \cdot \rrbracket_r \rightrightarrows_a : \llbracket \text{memmove} \rrbracket_r \begin{array}{c} \xrightarrow{\text{Call!}(\text{locle}, [d, s], m)} \\ \xleftarrow{\text{Return?}(v', m')} \end{array} \Big| \begin{array}{c} \xrightarrow{\text{Jump!}(r, m)} \\ \xleftarrow{\text{Jump?}(r', m')} \end{array} \oplus_a \llbracket \text{locle} \rrbracket_a$$

$$\begin{aligned} \llbracket \text{onetwo} \rrbracket_a &= \llbracket \downarrow \text{main} \cup_a \downarrow \text{memmove} \cup_a \text{locle} \cup_a \text{print} \rrbracket_a \\ &\preceq \llbracket \downarrow \text{main} \rrbracket_a \oplus_a \llbracket \downarrow \text{memmove} \rrbracket_a \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\ &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle} \rrbracket_a \oplus_a \llbracket \text{print} \rrbracket_a \\ &\preceq \llbracket \llbracket \text{main} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_s \\ &\preceq \llbracket \llbracket \text{main} \rrbracket_r \oplus_r \llbracket \text{memmove} \rrbracket_r \oplus_r \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_s \\ &\preceq \llbracket \llbracket \text{main} \cup_r \text{memmove} \rrbracket_r \oplus_r \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_s \\ &\preceq \llbracket \llbracket \text{main}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{print}_{\text{spec}} \rrbracket_s \\ &\preceq \llbracket \text{onetwo}_{\text{spec}} \rrbracket_s \end{aligned}$$

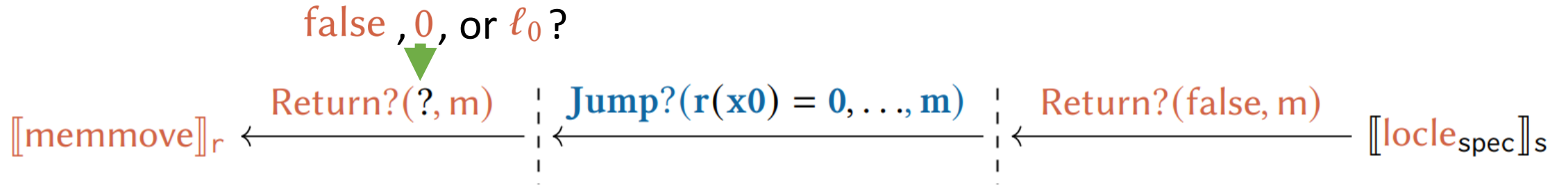
# Resolving the angelic choice

Consider  $\llbracket \text{memmove} \rrbracket_r \rrbracket_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a} \leq \llbracket \text{memmove} \rrbracket_r \oplus_r \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rrbracket_{r \Rightarrow a}$



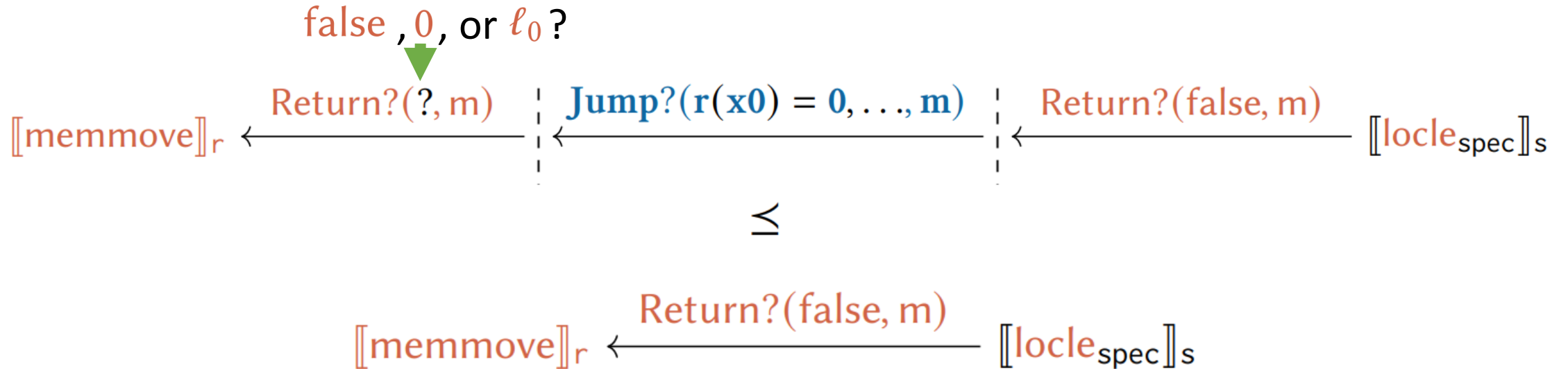
# Resolving the angelic choice

Consider  $\llbracket \text{memmove} \rrbracket_r \upharpoonright_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_s \upharpoonright_{r \Rightarrow a} \preceq \llbracket \text{memmove} \rrbracket_r \oplus_r \llbracket \text{locle}_{\text{spec}} \rrbracket_s \upharpoonright_{r \Rightarrow a}$



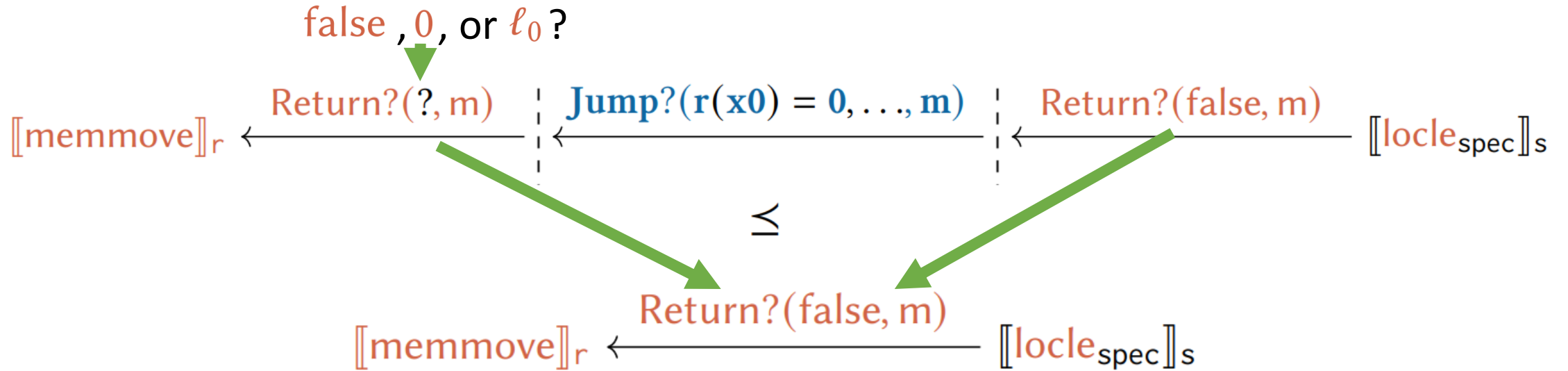
# Resolving the angelic choice

Consider  $\llbracket \text{memmove} \rrbracket_r \upharpoonright_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_s \upharpoonright_{r \Rightarrow a} \preceq \llbracket \text{memmove} \rrbracket_r \oplus_r \llbracket \text{locle}_{\text{spec}} \rrbracket_s \upharpoonright_{r \Rightarrow a}$



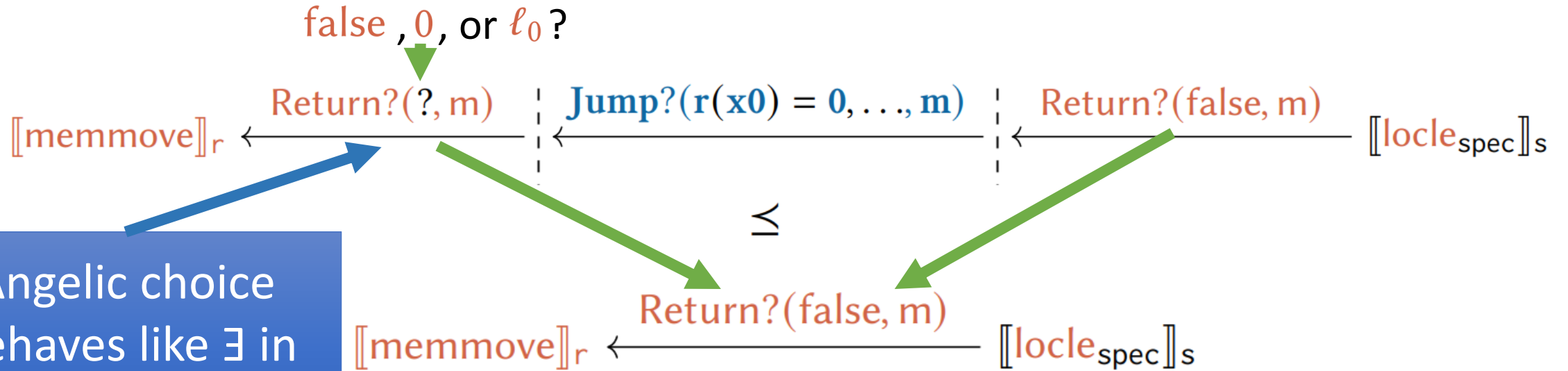
# Resolving the angelic choice

Consider  $\llbracket \text{memmove} \rrbracket_r \rceil_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a} \preceq \llbracket \text{memmove} \rrbracket_r \oplus_r \llbracket \text{locle}_{\text{spec}} \rrbracket_s \rceil_{r \Rightarrow a}$



# Resolving the angelic choice

Consider  $\llbracket \text{memmove} \rrbracket_r \upharpoonright_{r \Rightarrow a} \oplus_a \llbracket \text{locle}_{\text{spec}} \rrbracket_s \upharpoonright_{r \Rightarrow a} \preceq \llbracket \text{memmove} \rrbracket_r \oplus_r \llbracket \text{locle}_{\text{spec}} \rrbracket_s \upharpoonright_{r \Rightarrow a}$



Angelic choice behaves like  $\exists$  in implementation

# Non-determinism summary

implementation  $\leq$  specification

***Angelic non-determinism***

***Demonic non-determinism***

$\forall$  in specification

$\exists$  in specification

$\exists$  in implementation

$\forall$  in implementation

Assumption about the environment  
*Rely*

Guarantee to the environment  
*Guarantee*

**Program main** `fn main()  $\triangleq$  let  $x := \text{yield}(0)$  in print( $x$ ); let  $x := \text{yield}(0)$  in print( $x$ ); yield(0)`

**Library stream** `fn stream( $n$ )  $\triangleq$  yield( $n$ ); stream( $n + 1$ );`

**Library yield** `yield : ... save and restore registers, and switch stack ...`

CORO-LINK

$$\llbracket \text{yield} \rrbracket_a \oplus_a \llbracket M_1 \rrbracket_{r \Rightarrow a} \oplus_a \llbracket M_2 \rrbracket_{r \Rightarrow a} \leq \llbracket M_1 \oplus_{\text{coro}} M_2 \rrbracket_{r \Rightarrow a}$$

# Non-determinism in Spec

demonic          angelic

$$\text{Spec}(E) \ni p ::=_{\text{coind}} \text{vis}(e); p \mid \exists x : T; p(x) \mid \forall x : T; p(x) \quad (e \in E)$$

$$\frac{\text{SIM-EX-R} \quad \exists y \in T. M \leq \llbracket p(y) \rrbracket_s}{M \leq \llbracket \exists x : T; p(x) \rrbracket_s}$$

$$\frac{\text{SIM-EX-L} \quad \forall y \in T. \llbracket p(y) \rrbracket_s \leq M}{\llbracket \exists x : T; p(x) \rrbracket_s \leq M}$$

$$\frac{\text{SIM-ALL-R} \quad \forall y \in T. M \leq \llbracket p(y) \rrbracket_s}{M \leq \llbracket \forall x : T; p(x) \rrbracket_s}$$

$$\frac{\text{SIM-ALL-L} \quad \exists y \in T. \llbracket p(y) \rrbracket_s \leq M}{\llbracket \forall x : T; p(x) \rrbracket_s \leq M}$$

# Non-determinism in Spec

demonic          angelic



$\text{Spec}(E) \ni p ::=_{\text{coind}} \text{vis}(e); p \mid \exists x : T; p(x) \mid \forall x : T; p(x) \quad (e \in E)$

SIM-EX-R

$$\frac{\exists y \in T. M \leq \llbracket p(y) \rrbracket_s}{M \leq \llbracket \exists x : T; p(x) \rrbracket_s}$$

SIM-EX-L

$$\frac{\forall y \in T. \llbracket p(y) \rrbracket_s \leq M}{\llbracket \exists x : T; p(x) \rrbracket_s \leq M}$$

SIM-ALL-R

$$\frac{\forall y \in T. M \leq \llbracket p(y) \rrbracket_s}{M \leq \llbracket \forall x : T; p(x) \rrbracket_s}$$

SIM-ALL-L

$$\frac{\exists y \in T. \llbracket p(y) \rrbracket_s \leq M}{\llbracket \forall x : T; p(x) \rrbracket_s \leq M}$$



# Non-determinism in Spec

demonic

angelic

$$\text{Spec}(E) \ni p ::=_{\text{coind}} \text{vis}(e); p \mid \exists x : T; p(x) \mid \forall x : T; p(x) \quad (e \in E)$$

demonic:

$$\frac{\text{SIM-EX-R} \quad \exists y \in T. M \leq \llbracket p(y) \rrbracket_s}{M \leq \llbracket \exists x : T; p(x) \rrbracket_s}$$

$$\frac{\text{SIM-EX-L} \quad \forall y \in T. \llbracket p(y) \rrbracket_s \leq M}{\llbracket \exists x : T; p(x) \rrbracket_s \leq M}$$

angelic:

$$\frac{\text{SIM-ALL-R} \quad \forall y \in T. M \leq \llbracket p(y) \rrbracket_s}{M \leq \llbracket \forall x : T; p(x) \rrbracket_s}$$

$$\frac{\text{SIM-ALL-L} \quad \exists y \in T. \llbracket p(y) \rrbracket_s \leq M}{\llbracket \forall x : T; p(x) \rrbracket_s \leq M}$$

$\forall$  and  $\exists$  behave like the logical quantifiers

# Operational semantics for angelic non-determinism

Definition of modules: set of states

$$M = (S, \rightarrow, \sigma^0)$$


$$\rightarrow \in \mathcal{P}(S \times \text{option}(E) \times \mathcal{P}(S))$$

$$(\exists x : T; p(x)) \xrightarrow{\tau}_s \{p(y)\} \text{ (for } y \in T) \quad (\forall x : T; p(x)) \xrightarrow{\tau}_s \{p(y) \mid y \in T\}$$

$$(\text{vis}(e); p) \xrightarrow{e}_s \{p\}$$

# Operational semantics for angelic non-determinism

Definition of modules:                      set of states      initial state

$$M = (S, \rightarrow, \sigma^0)$$


$$\rightarrow \in \mathcal{P}(S \times \text{option}(E) \times \mathcal{P}(S))$$

$$(\exists x : T; p(x)) \xrightarrow{\tau}_s \{p(y)\} \text{ (for } y \in T) \quad (\forall x : T; p(x)) \xrightarrow{\tau}_s \{p(y) \mid y \in T\}$$

$$(\text{vis}(e); p) \xrightarrow{e}_s \{p\}$$

# Operational semantics for angelic non-determinism

Definition of modules:

$$M = (S, \rightarrow, \sigma^0)$$

set of states      initial state

$$\rightarrow \in \mathcal{P}(S \times \text{option}(E) \times \mathcal{P}(S))$$

$$(\exists x : T; p(x)) \xrightarrow{\tau}_s \{p(y)\} \text{ (for } y \in T) \quad (\forall x : T; p(x)) \xrightarrow{\tau}_s \{p(y) \mid y \in T\}$$

$$(\text{vis}(e); p) \xrightarrow{e}_s \{p\}$$







# Refinement / Simulation

$$M_1 \leq M_2 \triangleq (M_1, \sigma_{M_1}^0) \leq_{\text{co}} (M_2, \sigma_{M_2}^0)$$

$$(M_1, \sigma_1) \leq_{\text{co}} (M_2, \sigma_2) \triangleq_{\text{coind}}$$

*For each demonic choice in  $M_1$*

$$\forall e, \Sigma_1. \sigma_1 \xrightarrow{e}_{M_1} \Sigma_1 \Rightarrow \exists \Sigma_2. \sigma_2 \xrightarrow{e}_{M_2}^* \Sigma_2 \wedge$$

$$\forall \sigma'_2 \in \Sigma_2. \exists \sigma'_1 \in \Sigma_1. (M_1, \sigma'_1) \leq_{\text{co}} (M_2, \sigma'_2)$$



# Refinement / Simulation

$$M_1 \leq M_2 \triangleq (M_1, \sigma_{M_1}^0) \leq_{\text{co}} (M_2, \sigma_{M_2}^0)$$

$$(M_1, \sigma_1) \leq_{\text{co}} (M_2, \sigma_2) \triangleq_{\text{coind}}$$

*For each demonic choice in  $M_1$  exists a demonic choice in  $M_2$ , s.t.*

$$\forall e, \Sigma_1. \sigma_1 \xrightarrow{e}_{M_1} \Sigma_1 \Rightarrow \exists \Sigma_2. \sigma_2 \xrightarrow{e}_{M_2}^* \Sigma_2 \wedge$$


$$\forall \sigma'_2 \in \Sigma_2. \exists \sigma'_1 \in \Sigma_1. (M_1, \sigma'_1) \leq_{\text{co}} (M_2, \sigma'_2)$$

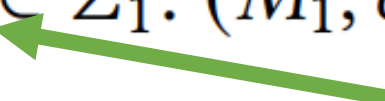
# Refinement / Simulation

$$M_1 \leq M_2 \triangleq (M_1, \sigma_{M_1}^0) \leq_{\text{co}} (M_2, \sigma_{M_2}^0)$$

$$(M_1, \sigma_1) \leq_{\text{co}} (M_2, \sigma_2) \triangleq_{\text{coind}}$$

*For each demonic choice in  $M_1$  exists a demonic choice in  $M_2$ , s.t.*


$$\forall e, \Sigma_1. \sigma_1 \xrightarrow{e}_{M_1} \Sigma_1 \Rightarrow \exists \Sigma_2. \sigma_2 \xrightarrow{e}_{M_2}^* \Sigma_2 \wedge$$

$$\forall \sigma'_2 \in \Sigma_2. \exists \sigma'_1 \in \Sigma_1. (M_1, \sigma'_1) \leq_{\text{co}} (M_2, \sigma'_2)$$


*for each angelic choice in  $M_2$  exists an angelic choice in  $M_1$ .*

# Refinement / Simulation

$$M_1 \leq M_2 \triangleq (M_1, \sigma_{M_1}^0) \leq_{\text{co}} (M_2, \sigma_{M_2}^0)$$

$$(M_1, \sigma_1) \leq_{\text{co}} (M_2, \sigma_2) \triangleq_{\text{coind}}$$

*For each demonic choice in  $M_1$  exists a demonic choice in  $M_2$ , s.t.*

$$\forall e, \Sigma_1. \sigma_1 \xrightarrow{e}_{M_1} \Sigma_1 \Rightarrow \exists \Sigma_2. \sigma_2 \xrightarrow{e}_{M_2}^* \Sigma_2 \wedge$$

$$\forall \sigma'_2 \in \Sigma_2. \exists \sigma'_1 \in \Sigma_1. (M_1, \sigma'_1) \leq_{\text{co}} (M_2, \sigma'_2)$$

*for each angelic choice in  $M_2$  exists an angelic choice in  $M_1$ .*

# Refinement / Simulation

$$M_1 \leq M_2 \triangleq (M_1, \sigma_{M_1}^0) \leq_{\text{co}} (M_2, \sigma_{M_2}^0)$$

$$(M_1, \sigma_1) \leq_{\text{co}} (M_2, \sigma_2) \triangleq_{\text{coind}}$$

*For each demonic choice in  $M_1$  exists a demonic choice in  $M_2$ , s.t.*

$$\forall e, \Sigma_1. \sigma_1 \xrightarrow{e}_{M_1} \Sigma_1 \Rightarrow \exists \Sigma_2. \sigma_2 \xrightarrow{e}_{M_2}^* \Sigma_2 \wedge$$

$$\forall \sigma'_2 \in \Sigma_2. \exists \sigma'_1 \in \Sigma_1. (M_1, \sigma'_1) \leq_{\text{co}} (M_2, \sigma'_2)$$

*for each angelic choice in  $M_2$  exists an angelic choice in  $M_1$ .*